







(0,50 mm) .0197"

HIGH SPEED GROUND PLANE HEADER

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?QTH

Insulator Material: Liquid Crystal Polymer Terminal Material: hosphor Bronze Plating: Au or Sn over 50u" (1.27 um) Ni Current Rating: Contact: 2 A per pin

2 A per piri (1 pin powered per row) Ground Plane: 25 A per ground plane (1 ground plane powered) Operating Temp Range: -55°C to +125°C

-55 C to +125 C Voltage Rating: 125 VAC (5 mm Stack Height) Max Cycles: 100 RoHS Compliant: Yes

Processing:

Lead-Free Solderable: Yes SMT Lead Coplanarity: (0,10 mm) .004" max (030-060) (0,15 mm) .006" max (090) Board Stacking: For applications requiring more than two connectors per board contact ipg@samtec.com

RECOGNITIONS

For complete scope of recognitions see www.samtec.com/quality





ALSO AVAILABLE (MOQ Required)

- 14 mm, 15 mm, 22 mm and 30 mm stack height (Caution: Some automatic placement/inspection machines may have component height restrictions. Please consult machinery specifications.)
- 30μ" (0,76 μm) Gold (Specify -H plating for Data Rate cable mating applications.)
- Edge Mount & Guide Posts
- 80 (-DP), 120, 150 positions per row
- Retention Option Contact Samtec.

Board Mates: QSH

Cable Mates: HQCD, HQDP (See Also Available note)



Call Samtec for maximum mated cycles		The state of the s	
QTH/QSH	Туре	Rated @ 3dB Insertion Loss	
5 mm Stack Height		with PCB effects*	w/o PCB effects**
Single-Ended Signaling	-D	9 GHz / 18 Gbps	9.5 GHz / 19 Gbps
Differential Pair Signaling	-D	8 GHz / 16 Gbps	10.5 GHz / 21 Gbps
Differential Pair Signaling	-DP	9.5 GHz / 19 Gbps	16.5 GHz / 33 Gbps

*Performance data includes effects of a non-optimized PCB.
**Test board losses de-embedded from performance data.

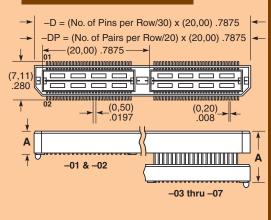
(20 pairs per bank = -D-DP)

Performance data for other stack heights and complete test data available at www.samtec.com?QTH or contact sig@samtec.com



LEAD PINS PER ROW **PLATING** NO. OF PAIRS STYLE OPTION

Specify **LEAD** -030, -060, -090 **STYLE** (60 total pins per bank = -D) from **–020, –040, –060**







= Gold Flash on Signal Pins and chart Ground Plane, Matte Tin on tails

= 10µ" (0,25 µm) Gold on Signal Pins and Ground Plane, Matte Tin on tails

-C*
= Electro-Polished
Selective 50μ" (1,27 μm) min Au over 150μ" (3,81 μm) Ni on Signal Pins in contact area, 10μ" (0,25 μm) min Au over 50μ" (1,27 μm) Ni on Ground Plane in contact area, Matte Tin over 50µ'

*Note: -C Plating passes 10 year MFG testing

(1,27 µm) min Ni

on all solder tails

Note: Some lengths, styles and options are non-standard, non-returnable.

–D = Single-Ended -D-DP Differential Pair (-01 only)

QTH HEIGHT WITH STYLE QSH* (5,00) .197 -01 .168 (7,26) .286 (8,00) -02 (11,00) (10.27 -03 .404 .433 (15,25) (16,00) -04 .630 .600 (18,26) .718 (19,00) .748 -05 (25,00) (24.24)-07

.954 *Processing conditions will affect mated height.

OTHER

OPTION

= (7,00 mm) .275" DIA Polyimide film Pick & Place Pad (N/A with -05 & 07 lead style)

-TR

= Tape & Reel -090 positions maximum)

= Latching Option (-01 lead style only) (N/A on 060 (-D-DP) & -090)

OTHER SOLUTIONS

· Board Spacing Standoffs. See SO Series.

Due to technical progress, all designs, specifications and components are subject to change without notice.

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