

L-1384AD/1YD





DESCRIPTION

• The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode

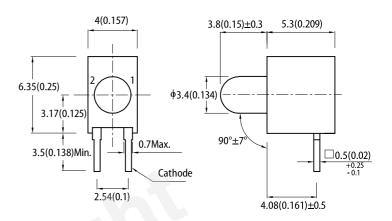
FEATURES

- Ideal for card edge status indication
- · High reliability-life measured in years
- . Housing UL rating: 94V-0
- · Housing material: Type 66 nylon
- RoHS compliant

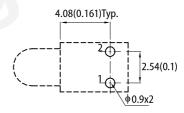
APPLICATIONS

- Status indicator
- Illuminator
- Signage applications
- · Decorative and entertainment lighting
- · Commercial and residential architectural lighting

PACKAGE DIMENSIONS



Recommended PCB Layout



- All dimensions are in millimeters (inches).
 Tolerance is ±0.25(0.01") unless otherwise noted.
- Lead spacing is measured where the leads emerge from the package.
 The specifications, characteristics and technical data described in the datasheet are subject to change
- without prior notice.

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	Iv (mcd) @ 10mA [2]		Viewing Angle [1]
			Min.	Тур.	201/2
L-1384AD/1YD	Yellow (GaAsP/GaP)	Yellow Diffused	8	15	60°

Notes.

1. 01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.

2. Luminous intensity / luminous flux: +/-15%.

3. Luminous intensity value is traceable to CIE127-2007 standards.





ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		Unit
Farameter		Emitting Color	Тур.	Max.	Offic
Wavelength at Peak Emission I _F = 10mA	λ_{peak}	Yellow	590	-	nm
Dominant Wavelength I _F = 10mA	λ_{dom} [1]	Yellow	588	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA	Δλ	Yellow	35	-	nm
Capacitance	С	Yellow	20	-	pF
Forward Voltage I _F = 10mA	V _F ^[2]	Yellow	1.95	2.4	V
Reverse Current (V _R = 5V)	I _R	Yellow	-	10	μΑ

Notes:

1. The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance λd:±1nm.)
2. Forward voltage:±0.1V.
3. Wavelength value is traceable to CIE127-2007 standards.
4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Symbol	Value	Unit	
Power Dissipation	P _D	75	mW	
Reverse Voltage	V _R	5	V	
Junction Temperature	T _j	110	°C	
Operating Temperature	T_{op}	-40 To +85	°C	
Storage Temperature	T_{stg}	-40 To +85	°C	
DC Forward Current	I _F	30	mA	
Peak Forward Current	I _{FM} ^[1]	140	mA	
Electrostatic Discharge Threshold (HBM)	-	8000	V	
Lead Solder Temperature [2]		260°C For 3 Seconds		
Lead Solder Temperature [3]		260°C For 5 Seconds		

Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

2. 2mm below package base.

3. 5mm below package base.

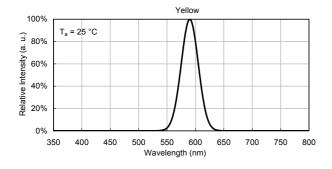
4. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.



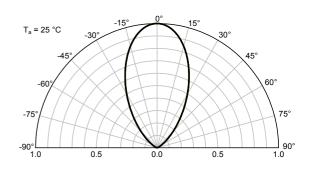


TECHNICAL DATA

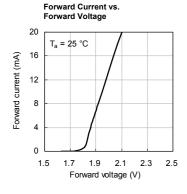
RELATIVE INTENSITY vs. WAVELENGTH

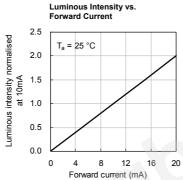


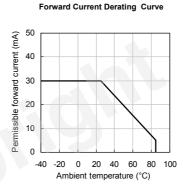
SPATIAL DISTRIBUTION

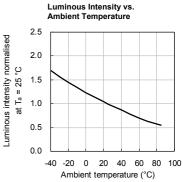


YELLOW

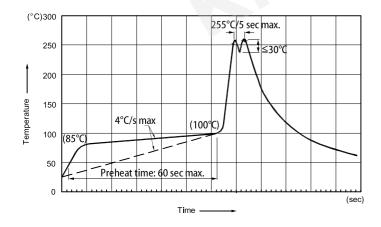






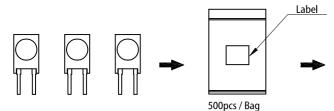


RECOMMENDED WAVE SOLDERING PROFILE

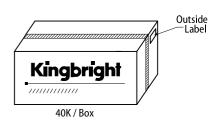


- Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
- 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
- 3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
 4. Fixtures should not incur stress on the component when mounting and during soldering process.
- 5. SAC 305 solder alloy is recommended.6. No more than one wave soldering pass

PACKING & LABEL SPECIFICATIONS













PRECAUTIONS

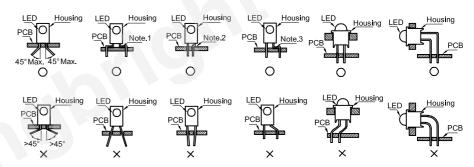
Storage Conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature ≤ 30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.

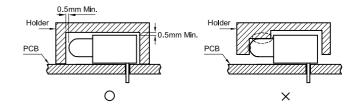
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits



○ " Correct mounting method " x " Incorrect mounting method

Lead Forming Procedures

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



PRECAUTIONARY NOTES

- The information included in this document reflects representative usage scenarios and is intended for technical reference only
- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to
- the latest datasheet for the updated specifications.

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