

# **2I392CW**

**Intel Apollo Lake N4200 / N3350 / E3950 SoC CPU ,  
DDR3L SODIMM, 2 x LAN / HDMI / USB / COM  
/ PCIe mini card**

**All-In-One eIO extension SBC**

**Intel Apollo Lake N4200 / N3350 / E3950 SoC CPU  
2 x HDMI, LVDS, 2 x mini PCIe, 2 x LAN, Audio  
SATA, USB, COM, eIO, Wide Range DC-IN**

**NO. 2I392CW**

**Release date: Apr. 11. 2018**

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## Warning !

1. Battery  
Batteries on board are consumables.  
The life time of them are not guaranteed.
2. Fless solution with HDD  
The specification & limitation of HDD should be considered carefully when the fanless solution is implemented.
3. We will not give further notification in case of changes of product information and manual.
4. SATA interface does not support Hot SWAP function.
5. There might be a 20% inaccuracy of WDT at room temperature.
6. Please make sure the voltage specification meets the requirement of equipment before plugging in.
7. There are two types of SSD, commercial grade and industrial grade, which provide different read/write speed performance, operation temperature and life cycle. Please contact sales for further information before making orders.
8. Caution! Please notice that the heat dissipation problem could cause the MB system unstable. Please deal with heat dissipation properly when buying single MB set.
9. Please avoid approaching the heat sink area to prevent users from being scalded with fanless products.
10. If users repair, modify or destroy any component of product unauthorizedly, We will not take responsibility or provide warranty anymore.
11. DO NOT apply any other material which may reduce cooling performance onto the thermal pad.
12. It is important to install a system fan toward the CPU to decrease the possibility of overheating / system hanging up issues, or customer is suggested to have a fine cooling system to dissipate heat from CPU.

## \* Hardware Notice Guide

1. Before linking power supply with the motherboard, please attach DC-in adapter to the motherboard first. Then plug the adapter power to AC outlet.  
Always shut down the computer normally before you move the system unit or remove the power supply from the motherboard. Please unplug the DC-in adapter first and then unplug the adapter from the AC outlet.  
Please refer photo 1 as standard procedures.
2. In case of using DIRECT DC-in (without adapter), please check the allowed range for voltage & current of cables. And make sure you have the safety protection for outer issues such as short/broken circuit, overvoltage, surge, lightning strike.
3. In case of using DC-out to an external device, please make sure its voltage and current comply with the motherboard specification.
4. The total power consumption is determined by various conditions (CPU / motherboard type, device, application, etc.). Be cautious to the power cable you use for the system, one with UL standard will be highly recommended.
5. It's highly possible to burn out the CPU if you change / modify any parts of the CPU cooler.
6. Please wear wrist strap and attach it to a metal part of the system unit before handling a component. You can also touch an object which is ground connected or attached with metal surface if you don't have wrist strap.
7. Please be careful to handle & don't touch the sharp-pointed components on the bottom of PCBA.
8. Remove or change any components from the motherboard will VOID the warranty of the motherboard.
9. Before you install / remove any components or even make any jumper setting on the motherboard, please make sure to disconnect the power supply first. (follow the aforementioned instruction guide)
10. "POWERON after PWR-Fair" function must be used carefully as below:  
When the DC power adaptor runs out of power, unplug it from the DC current;  
Once power returns, plug it back after 5 seconds.  
If there is a power outage, unplug it from the AC current, once power returns, plug it back after 30 seconds. Otherwise it will cause system locked or made a severe damage.

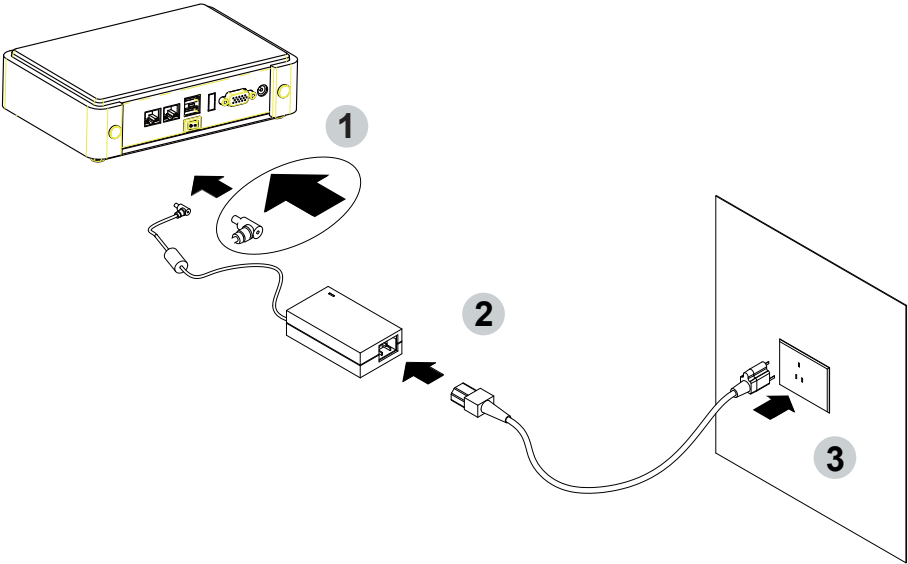
### **Remark 1:**

**Always insert / unplug the DC-in horizontally & directly to / from the motherboard. DO NOT twist, it is designed to fit snugly.**

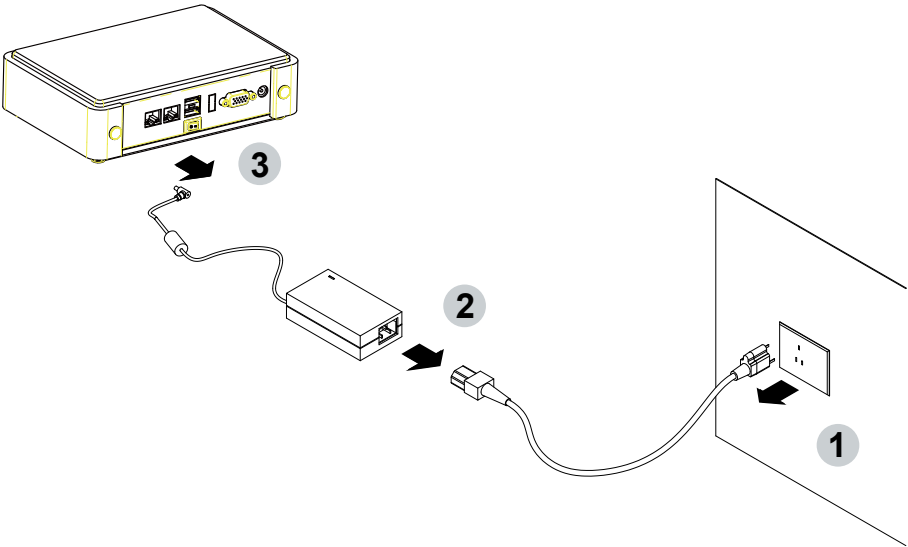
**Moreover, erratic pull / push action might cause an unpredictable damage to the component & system unit.**

**Photo 1**

**Insert**



**Unplug**



---

# Chapter-1

## General Information

The 2I392CW is a Pico-ITX SBC with LEX eIO (2 x PCIe + USB) expansion function offer the ideal & reliable platforms for high performance applications in Digital Signage, medical devices, POS / Kiosk, gaming machines, automation, military & industrial facilities. 2I392CW is powered by the latest Apollo Lake SoC 14nm quad-core Mobile Intel® Pentium processor N4200 or dual-core Mobile Intel® Celeron processor N3350 with 1 DDR3L SODIMM slot as well as triple independent display and fully 4K media playback capable with on-board HDMI output, making it ideal for deployment in transportation, digital signage and multi-display applications. 2I392CW also supports with two 10 / 100 / 1000 Mbps Ethernet for seamless broadband connectivity. With Wake-On LAN function and the PXE function in BIOS, these are perfect control boards for networking devices.

The embedded motherboard 2I392CW is specially designed with wide-Range Voltage DC in (9~36V) for widely varying input voltage requirement. This integrated platform offers superb performance and PC specification in the industry. It supports up to two serial ports RS232 / RS485 / RS422 jumper free auto switch by BIOS and 4 ports of USB 3.0 in the rear side, 2 port of USB 2.0 internal port for expansion (signal shared with Mini Cards ). The expandable interfaces include 1 full-size PCIe Mini card for PCIe x 1 or and USB interface, and 1 full-size PCIe Mini card for mSATA and USB interface.

2I392CW with LEX eIO (2 x PCIe + USB) expansion function to pair with LEX multi-function compact size eIO extension cards such as L2L001, L2L002, L2L003, L2L004, L2M001 etc. to expand more LAN ports, PCIe Mini cards or fiber optic LAN port. Please visit our website to find the Compact solution with Rich Expansion for vertical application.



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## 1-1 Major Feature

1. Mobile Intel® Pentium N4200 1.1GHz / 2.5GHz, (Quad core), Mobile Intel® Celeron N3350 1.1GHz / 2.4GHz (Dual core)
2. Apollo-Lake Integrated Graphics chipset, Mobile Intel® Pentium N4200 200MHz / 700MHz, Mobile Intel® Celeron N3350 200MHz / 650MHz
3. Support eDP 1.3 2 Lanes up to 1920 x 1200, DVI 1.2 1920 x 1080, HDMI 1.4b up to 3840 x 2160
4. DDR3L SODIMM slot x 1, up to 8GB
5. Support 2 x 10 / 100 / 1000 Mbps Intel LAN port.
6. Support 2 x RS232 auto switch to RS485 / RS422 by BIOS, 2 port internal
7. 4 x type A USB 3.0 and 2 x USB 2.0 internal (signal shared with Mini Cards)
8. ALC886 HD Audio Specification 1.0 Two channel sound
9. Support extended 1 x full-size Mini PCIe card for PCIe x 1 and USB interface, 1 x full-size Mini PCIe card for mSATA and USB interface.
10. Support 1 SATA port
11. Hardware digital Input & Output, 4 x DI / 4 x DO, Hardware Watch Dog Timer, 0~255 sec programmable
12. Wide Range DC IN +9V~36V
13. PCB Dimension: 102 x 83 mm

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## 1-2 Specification

1. **SOC:** Mobile Intel® Pentium N4200 1.1GHz / 2.5GHz, (Quad core), Mobile Intel® Celeron N3350 1.1GHz / 2.4GHz (Dual core)
2. **Memory:** DDR3L SODIMM slot x 1, up to 8GB
3. **Graphics:** Apollo-Lake Integrated Graphics chipset, Mobile Intel® Pentium N4200 200MHz / 700MHz, Mobile Intel® Celeron N3350 200MHz / 650MHz, eDP 1.3 2 Lanes up to 1920 x 1200, DVI 1.2 1920 x 1080, HDMI 1.4b up to 3840 x 2160
4. **SATA:** Integrated Serial ATA Host Controller 1 SATA port, SATA Gen3 Data transfer rates up to 6.0 Gb/s (600 MB/s). SATA signal shared with PCIe Mini card 1
5. **LAN:** 2 Intel I211-AT LAN chipset with 10 / 100 / 1000 Mbps for PCIe x 1 V2.1
6. **I/O Chip:** F81801U I/O chipset for 2 ports RS232 / RS422 / RS485 auto switch by BIOS
7. **USB:** 4 type A USB 3.0 connectors onboard, 2 USB 2.0 (internal) signal shared with Mini Cards.
8. **Sound:** Support line in, line out and MIC in
9. **WDT/DIO:** Hardware digital Input & Output, 4 x DI / 4 x DO (Option) / Hardware Watch Dog Timer, 0~255 sec programmable
10. **Expansion interface:** one full-size PCIe Mini card for PCIe x 1 and USB interface, one full-size Mini PCIe card for mSATA and USB interface
11. **BIOS:** Insyde UEFI BIOS
12. **Dimension:** 102 x 83 mm (2.5 inch)
13. **Power:** On board DC +9~36V

## 1-3 Installing the SO-DIMM

1. Align the SO-DIMM with the connector at a 45 degree angle.

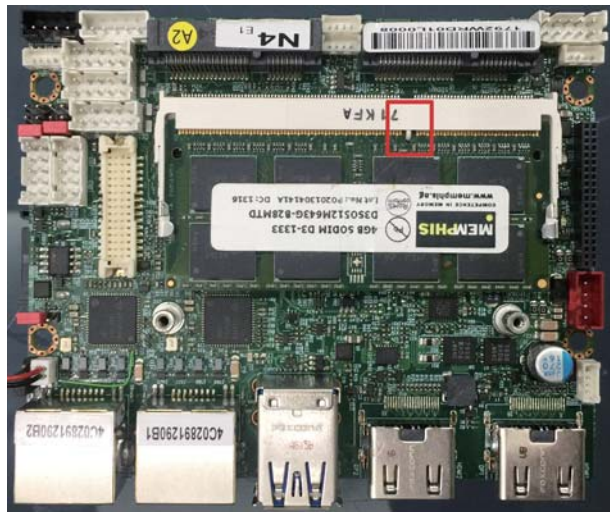


2. Press the SO-DIMM into the connector until you hear a click.

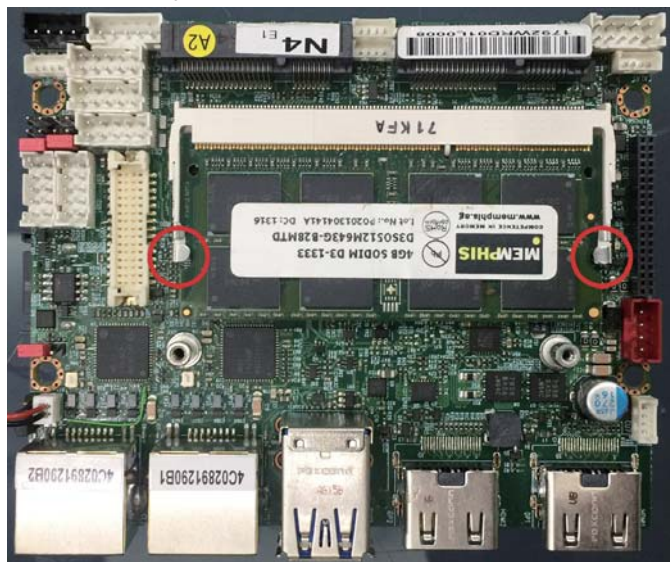


## Notices:

1. The connectors are designed to ensure the correct insertion. If you feel resistance, check the connectors & golden finger direction, and realign the card.



2. Make sure the retaining clips (on two sides of the slot) lock onto the notches of the card firmly.



### 1-3-1-1 Removing the SO-DIMM

1. Release the SO-DIMM by pulling outward the two retaining clips and the SO-DIMM pops up slightly.



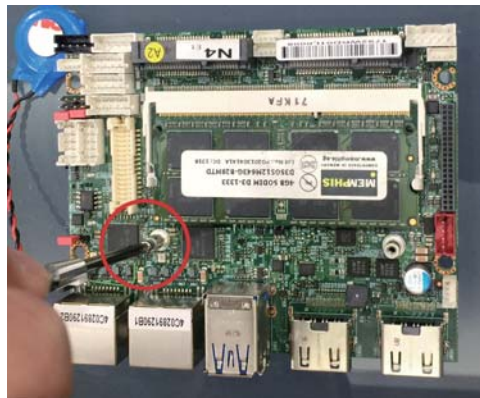
2. Lift the SO-DIMM out of its connector carefully.



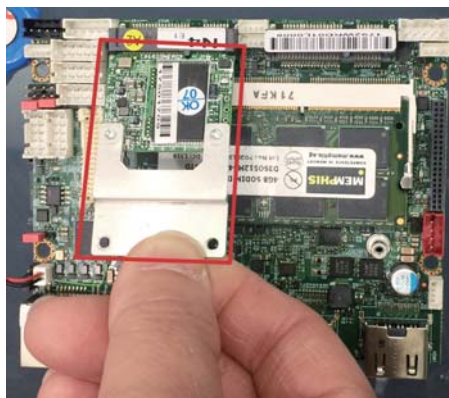


## 1-4 Installing the Mini PCI-e Card (Full Size)

1. Unscrew the screw on the board



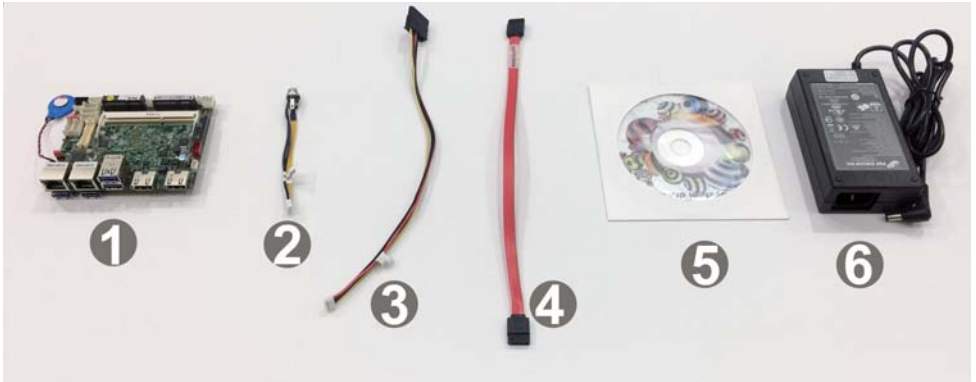
2. Plug in the Mini Card in a 45 angle



3. Gently push down the Mini Card and screw the screw back.



## 1-5 Packing List



	Material Code	Description	Detail Specification	Quantit
1	7G1901-1805001-0	MB-2I392CW-N4-001	LF,2I392CW-N4,Rev.:001	1
2	6G6003-7330-0100	Power Cable	LF,L=9cm,2.0 1*4/DC JK	1
3	6G6003-1009-0100	SATA Power Cable	LF,L=25cm,1*4/2.0 to 180° SATA 15p	1
4	6G6001-2203-0100	SATA DATA Cable (Red)	LF,L=25cm	1
5	6G8006-2350-0100	DVD	LF, Support Apollo Lake/Skylake	1
6	6G5212-0620-0100	■60W Power Adapter,12V/5A,2.5Ø	LF,L Type,FSP060-DIBAN2,FSP	1

\*The packing list above is for the users who purchase single motherboard. The users who purchase the board with chassis may refer to the packing list in the Assembly Guide.

Please contact with your dealer if any of these items is missing or damaged on delivery. And please keep all parts of the delivery package with packing materials in case if you need to deliver or store the product in the future.

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# Chapter-2

## Hardware Installation

### 2-1 Unpacking Precaution

This chapter provides the information how to install the hardware of 2I392CW. Please follow section 1-5, 2-1 and 2-2 to check the delivery package and unpack carefully. Please follow the jumper setting procedure.

**NOTE!**

1. Do not touch the board or any other sensitive components without all necessary anti-static protection.
2. Please pay attention to the voltage limitation of DC-IN 12V 5%.  
Overuse of DC-IN voltage limitation or change to another power adapter (not provided with this system) will VOID warranty.

You should follow these steps to protect the board from the static electric discharge whenever you handle the board:

1. Ground yourself by a grounded wrist strap at all times when you handle the 2I392CW.  
Well secure the ALLIGATOR clip of the strap to the end of the shielded wire lead from a grounded object. Please put on and connect the strap before handling the 2I392CW for harmlessly discharge any static electricity through the strap.
2. Please use anti-static pad to put any components, parts, or tools on the pad whenever you work on them outside the computer. You may also use the anti-static bag instead of the pad. Please ask your local supplier for necessary parts on anti-static requirement.
3. Do not plug any connector or set any jumper when the power is on.



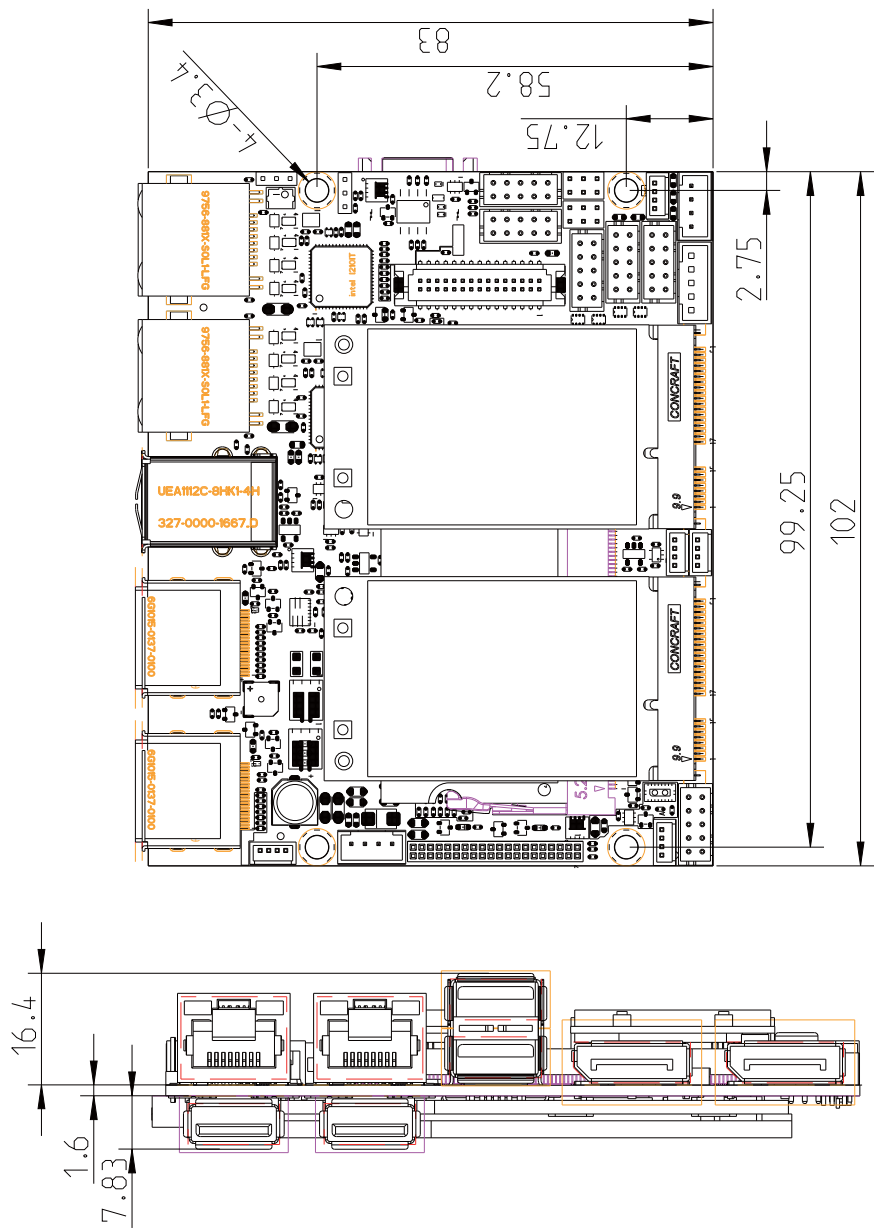
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## 2-2 Unpacking checkup

First of all, please follow all necessary steps of section 2-1 to protect 2I392CW from electricity discharge. With reference to section 1-5 please check the delivery package again with following steps:

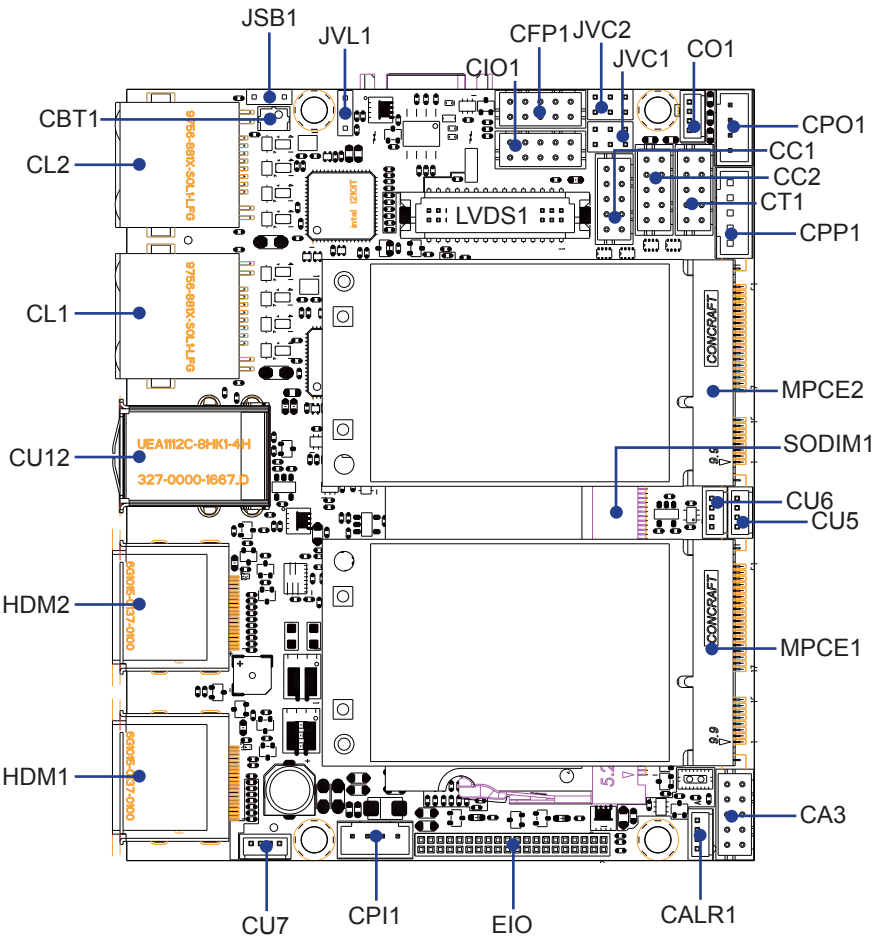
1. Unpack the 2I392CW board and keep all packing material, manual and driver disc etc, do not dispose !
2. Is there any components lose or drops from the board?  
DO NOT CONTINUE TO INSTALL THIS BOARD!  
CONTACT THE DEALER YOU PURCHASED THIS BOARD FROM, IMMEDIATELY.
3. Is there any visible damage on the board?  
DO NOT CONTINUE TO INSTALL THIS BOARD!CONTACT THE DEALER YOU PURCHASED THIS BOARD FROM, IMMEDIATELY.
4. Check your optional parts (i.e. DDR, CF etc.), all necessary jumpers setting to jumper pin-set, and CMOS setup correctly.  
Please also refer to all information of jumper settings in this manual.
5. Check your external devices (i.e. Add-On-Card, Driver Type etc.) for complete add-in or connection and CMOS setup correctly.  
Please also refer to all information of connector connection in this manual.
6. Please keep all necessary manual and driver disc in a good condition for future re-installation if you change your Operating System.

2-3 Dimension-2I392CW

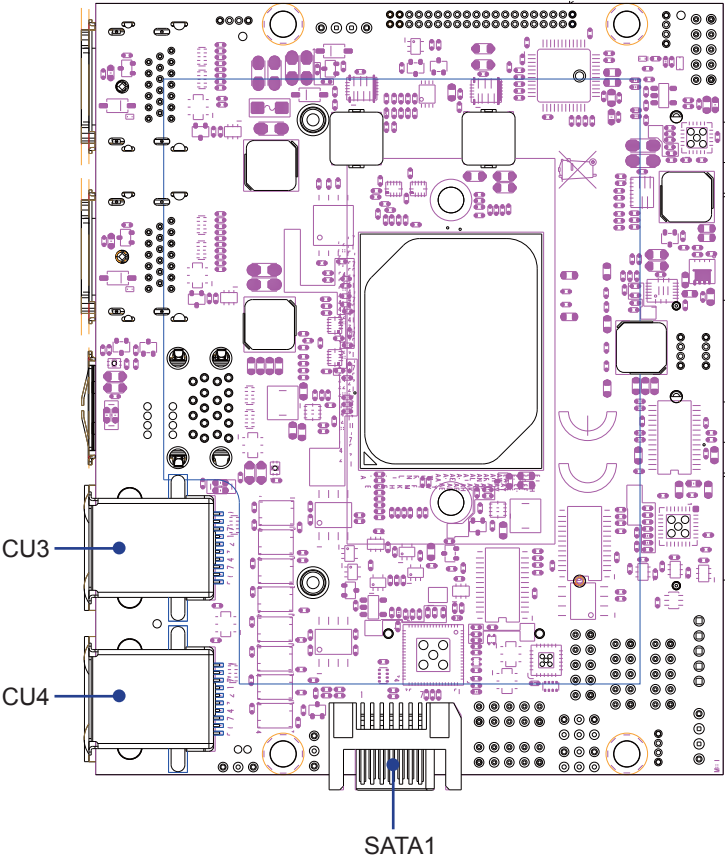


# 2-4 Layout-2I392CW-Connector and Jumper

TOP

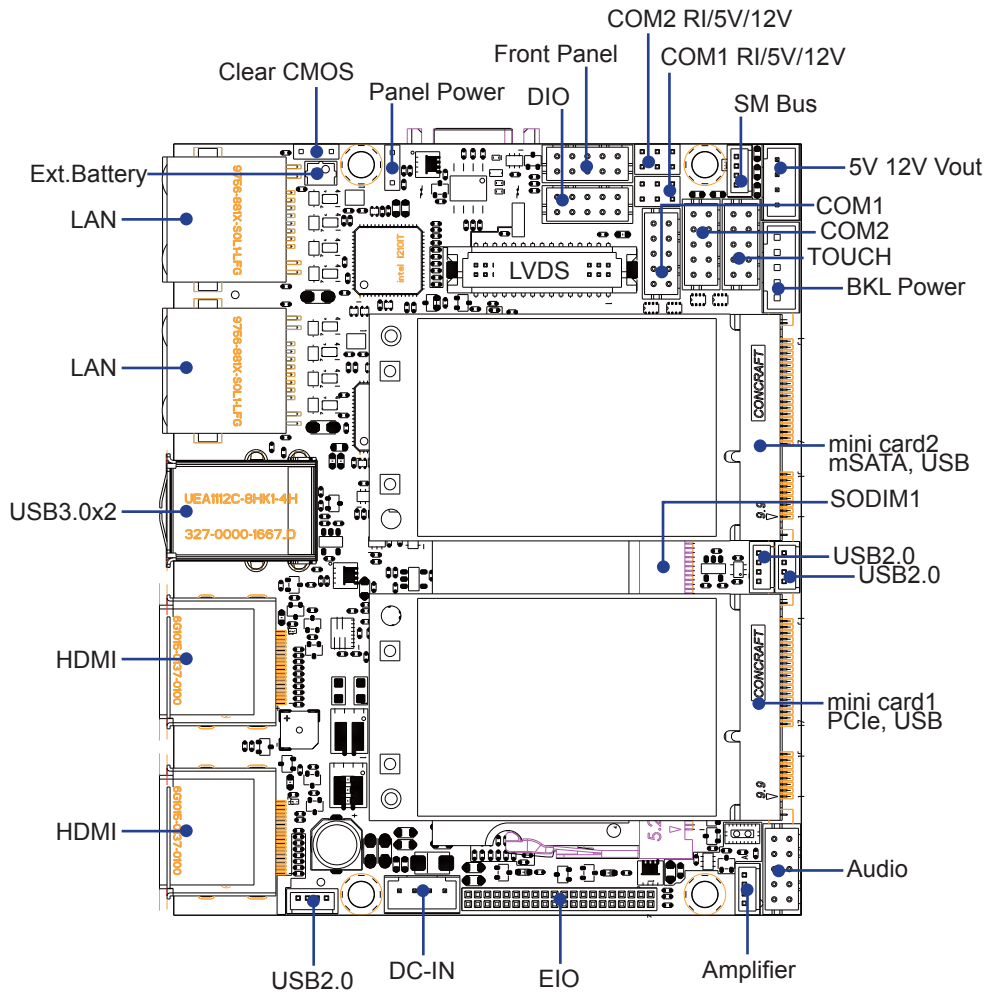


**2-4-1 Layout-2I392CW-Connector and Jumper Bottom**  
BOT



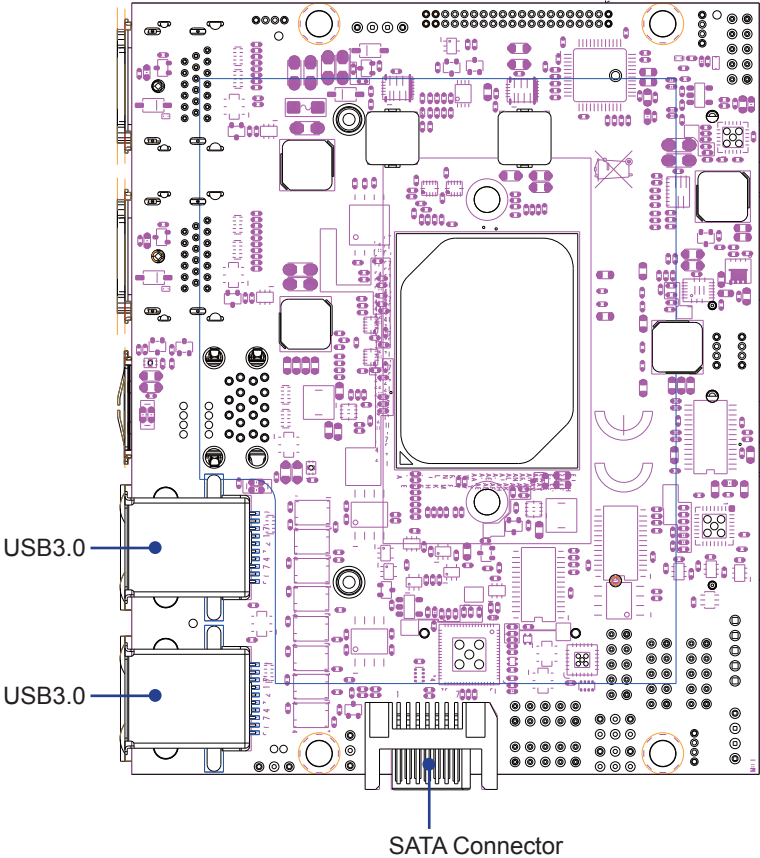
## 2-5 Layout-2I392CW-Function MAP

TOP



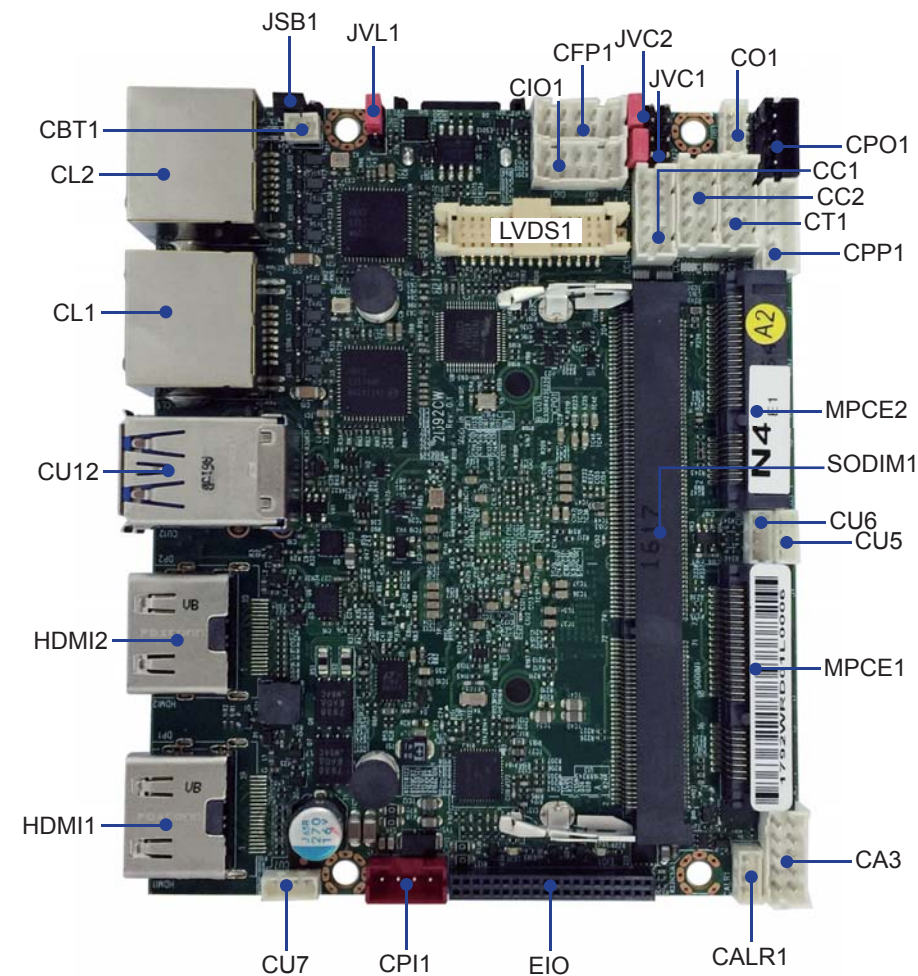
2-5-1 Layout-2I392CW-Function MAP

BOT



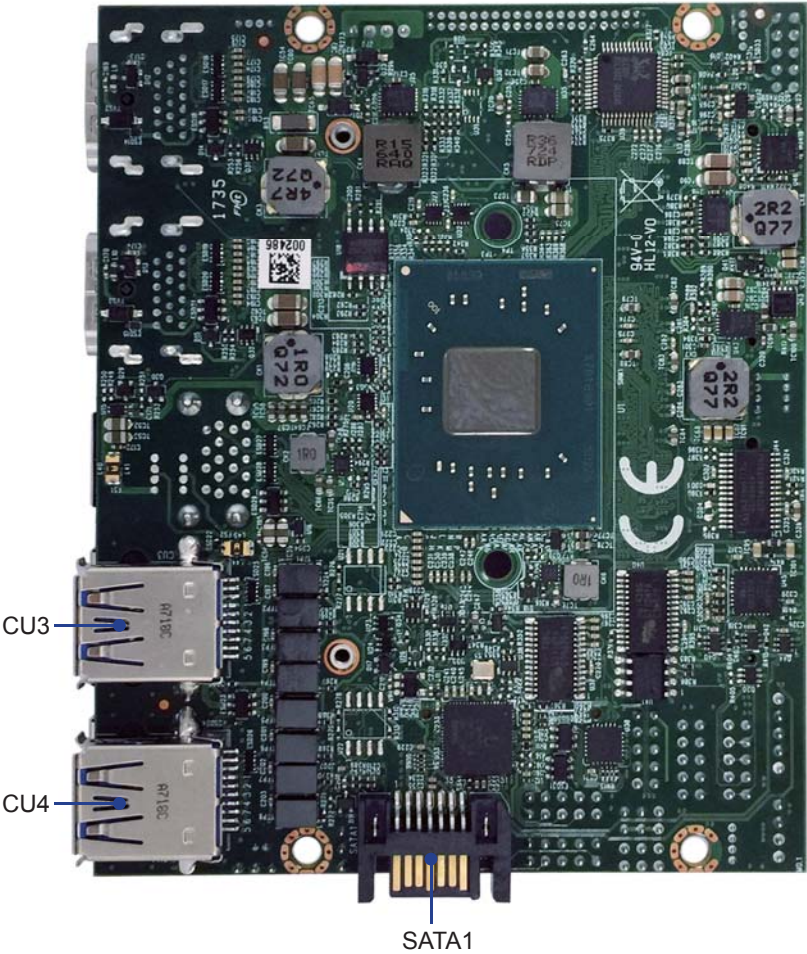
# 2-6 Diagram- 2I392CW

TOP



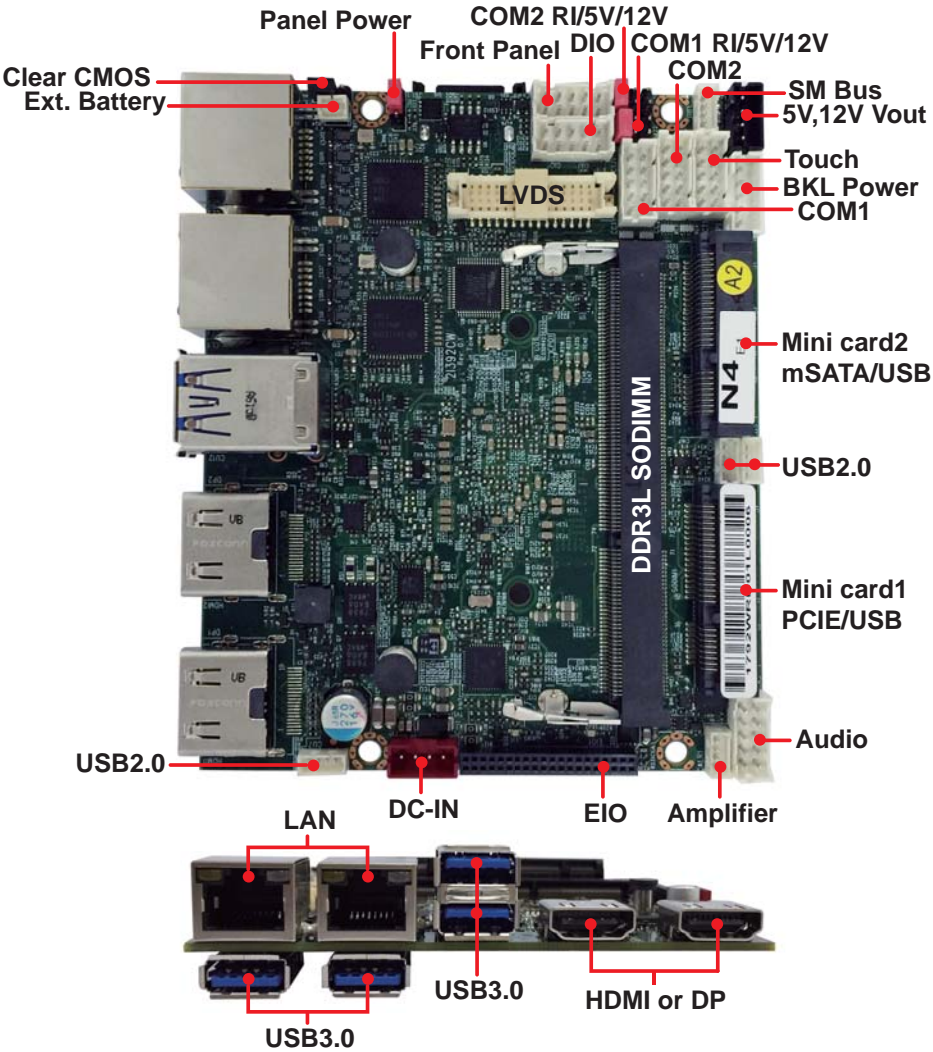
2-6-1 Diagram- 2I392CW

BOT





## 2-6-2 Function MAP- 2I392CW



## 2-7 List of Jumpers

JSB1: CMOS DATA Clear

JVL1: LCD panel power select

JVC1/JVC2: COM1/2 RI & power select

## 2-8 Jumper Setting Description

A jumper is ON as a closed circuit with a plastic cap covering two pins. A jumper is OFF as an open circuit without the plastic cap. Some jumpers have three pins, labeled 1, 2, and 3. You could connect either pin 1 and 2 or 2 and 3.

The below figure 2.2 shows the examples of different jumper settings in this manual.

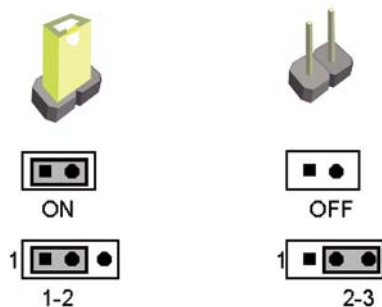


Figure 2.2

All jumpers already have its default setting with the plastic cap inserted as ON, or without the plastic cap as OFF. The default setting may be referred in this manual with a " \* " symbol .

## 2-9 JSB1: CMOS DATA Clear

A battery must be used to retain the motherboard configuration in CMOS RAM.  
Close Pin1 and pin 2 of JSB1 to store the CMOS data.

To clear the CMOS, follow the procedures below:

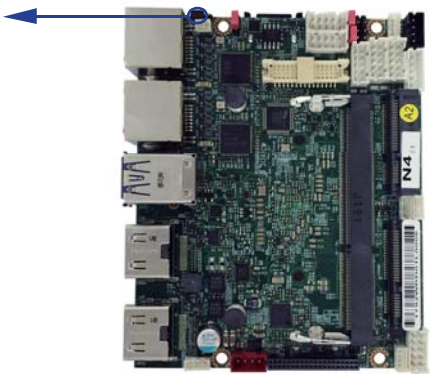
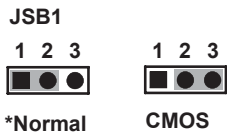
1. Turn off the system and unplug teh AC power
2. Remove DC IN power cable from DC IN power connector
3. Locate JSB1 and close pin 1-2 for few seconds
4. Return to default setting by Close pin 1-2
5. Connect DC IN power cable back to DC IN Power connector

JSB1	DESCRIPTION
*1-2	Normal set
2-3	CMOS data clear

Note: Normal work is open jumper

Note: Do not clear CMOS unless

- 1. Troubleshooting**
- 2. Forget password**
- 3. You fail over-clocking system**



## 2-10 JVL1: LCD panel power select

JVL1	DESCRIPTION
1-2	+5V
*2-3	+3.3V

Note: Attention! Check Panel Power in spec

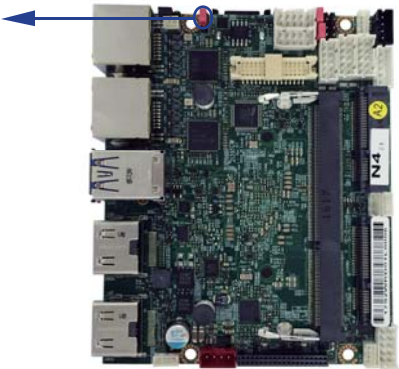
JVL1



+5V



\*+3.3V

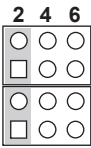


## 2-11 JVC1/JVC2: COM1/2 RI & power select

JVC1/JVC2	DESCRIPTION
*1-2	RI
3-4	+5V
5-6	+12V

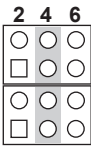


JVC2

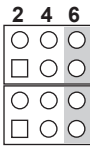


JVC1

\*RI Signal



+5V



+12V

---

# Chapter-3

## Connection

This chapter provides all necessary information of the peripheral's connections, switches and indicators. Always power off the board before you install the peripherals.

### 3-1 List of Connectors

CBT1:	Li 3V Battery 1x2pin (1.25mm) Wafer
CA3:	Line-out/Line-in/Mic-in 2x5 pin (2.0mm) Wafer
CALR1:	Amplifier 1x4 pin (1.25mm) Wafer
CC1 :	COM 2x5pin (2.0mm) wafer
CC2 :	COM 2x5pin (2.0mm) wafer
CFP1:	Front Panel connector 2x5pin (2.0mm) wafer
CIO1:	DIO 2x5pin (2.0mm) Wafer
CL1 :	LAN port 1 RJ45 Connector
CL2 :	LAN port 2 RJ45 Connector
CL11 :	LAN port 1 RJ45 2x4pin (2.0mm) wafer(option)
CL21 :	LAN port 2 RJ45 2x4pin (2.0mm) wafer(option)
CO1:	I <sup>2</sup> C Bus 1x4pin (1.25mm) Wafer
CPI1:	DC 12V-IN 1x4pin (2.0mm) Red wafer connector
CPO1:	+12V/+5V power output 1x4pin (2.0mm) Black wafer connector
CU12:	Dual USB3.0 Type A connector
CU3:	USB2.0 Type A connector
CU4:	USB3.0 Type A connector
CU5:	USB 2.0 port 1x4pin (1.25mm) Wafer
CU6:	USB 2.0 port 1x4pin (1.25mm) Wafer
CU7:	USB 2.0 port 1x4pin (1.25mm) Wafer
LVDS1:	LVDS 2x15pin (1.25mm) connector
CPP1:	BKL power 1x5pin (2.0mm) Wafer
CT1:	TOUCH 2x5pin (2.0mm) Wafer
SATA1:	One SATA connector 7pin
MPCE1:	Full size mini card port 1 sockets 52pin
MPCE2:	Full size mini card port 2 sockets 52pin

HDMI1: HDMI connector  
HDMI2: HDMI connector  
EIO1: eIO port 2x20pin (1.27mm) Wafer

### 3-2 CMOS battery connector

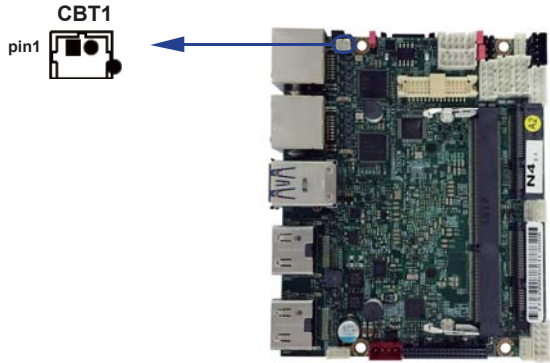
● CBT1: 3V Battery wafer connector (1x2pin 1.25mm)

PIN NO.	DESCRIPTION
1	GND
2	+3V

\* Batter use Li 3V / 220mA (CR2032)

Note:

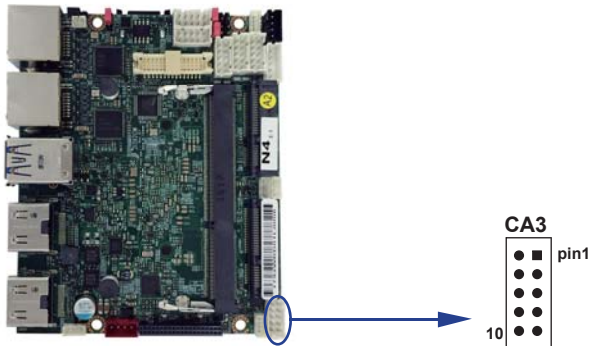
1. When the board without Adaptor plug in, this board power RTC consumption about 2.7uA
2. If adaptor always plug in RTC power consumption about 0.1uA



### 3-3 Audio interface

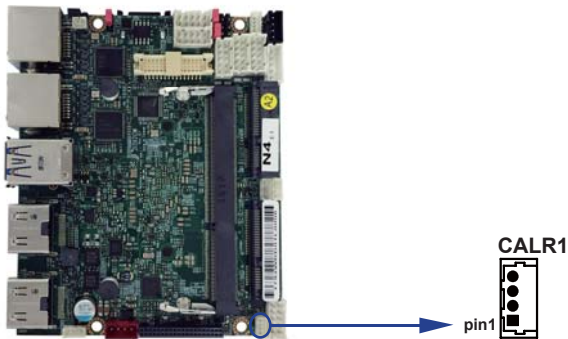
● CA3: Line-out / Line-in / Mic-in wafer connector (2x5pin 2.0mm)

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	Line-out-R	2	MIC-IN
3	Line-in-R	4	GND
5	GND	6	GND
7	Line-in-L	8	+5V
9	Line-out-L	10	MIC-IN



● CALR1: Amplifier wafer connector (1x4pin 1.25mm wafer)

PIN NO.	DESCRIPTION
1	L_OUT+
2	L_OUT-
3	R_OUT-
4	R_OUT+



### 3-4 COM interface

COM1/2 RS232 or RS485 or RS422 form BIOS select.  
COM1, COM2 default support RS232.

● CC1/2 wafer connector (2x5pin 2.0mm) (RS232 Mode)

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI/Voltage
5	GND		

Note: The pin9 RI can be modify to Power to supply device. The power voltage can be set +12V or +5V. The RI change Voltage function is OEM need change jumper. Default is RI signal.

● CC1/2 wafer connector (2x5pin 2.0mm) (RS485 Mode)

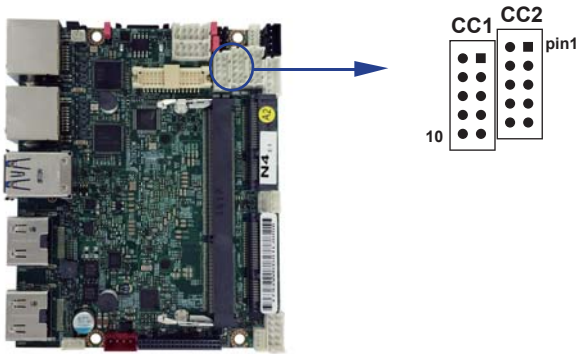
PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	RS485 Data+	6	NC
2	RS485 Data-	7	NC
3	NC	8	NC
4	NC	9	NC
5	GND		

Note: 1. BIOS need setting to RS485 mode.

● CC1/2 wafer connector (2x5pin 2.0mm) (RS422 Mode)

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	RS422 TX-	6	NC
2	RS422 TX+	7	NC
3	RS422 RX+	8	NC
4	RS422 RX-	9	NC
5	GND		

Note: 1. BIOS need setting to RS422 mode.

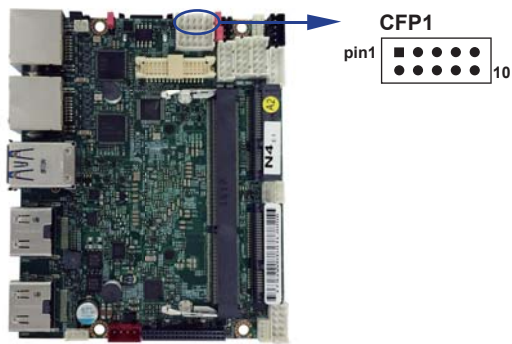




### 3-5 Front Panel Pin Header

● CFP1: Front Panel wafer connector (2x5pin 2.0mm)

PIN NO.	Description	PIN NO.	Description
1	Power button pin	2	Power button GND
3	Reset pin	4	Reset GND
5	Power LED	6	Power LED+
7	HDD LED-	8	HDD LED+
9	LAN LED-	10	LAN LED+

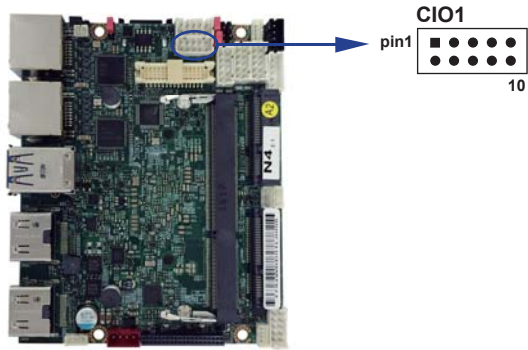


### 3-6 Digital Input / Output / Watch Dog Time

● CIO1 DIO 0--3 wafer connector (2x5pin 2.0mm)

PIN NO.	Description	PIN NO.	Description
1	DI-0	2	DO-3
3	DI-1	4	DO-2
5	DI-2	6	DO-1
7	DI-3	8	DO-0
9	GND	10	+5V

Note: 1. DI pin default pull up 10KΩ to +5V  
2. If use need isolate circuit to control external device  
3. F75111N-1 I<sup>2</sup>C bus address 0x9c



● **WDT For F75111N I<sup>2</sup>C watch dog timer device:**

DC spec :

Input low Voltage (VIL):+0.8 Max ,

Input High Voltage(VIH) : +2V Min

Output low Current (IOL):10mA (Min) VOL=0.4V

Output High Current (IOH):-10mA (Min) VOH=2.4V

Watch Dog Time value 0~255 sec

The system will be issued reset. When WDT is enable the hardware start down counter to zero.

The reset timer have 10~20% tolerance upon the Temperature.

Note: If want to SDK support. Please contact to sales window.

## 3-6-1 IO Device: F75111 under DOS

### The Sample code source you can download from

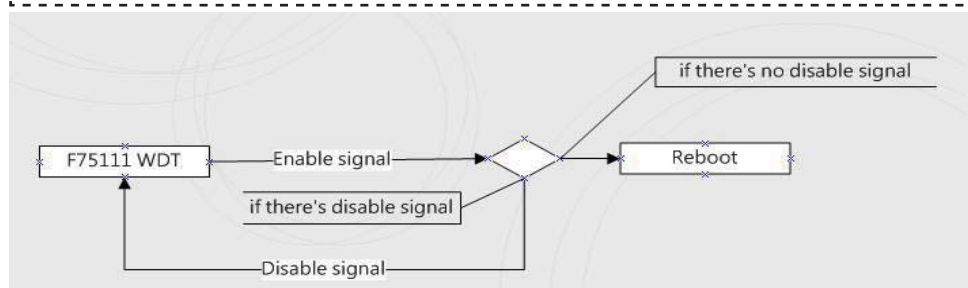
Source file: F75111\_Dos\_Src.rar [http://tprd.info/lexwiki/index.php/IO\\_Device:F75111\\_under\\_DOS](http://tprd.info/lexwiki/index.php/IO_Device:F75111_under_DOS)

Binary file: F75111\_Dos\_Bin.rar

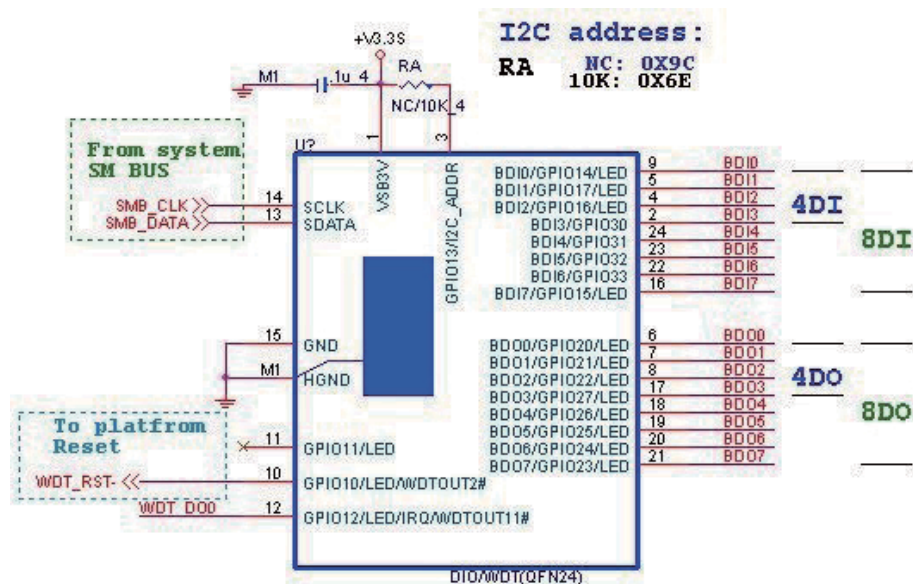
USERNAME & PASSWORD: sf

### How to use this Demo Application

- 1.Boot Ms-Dos Operating System
- 2.execute "75WDT.EXE" binary file
- 3.Input 1 to Enable WDT timer or input 0 to Disable it.
- 4.input numbers of second for chip countdown and Reset Computer



### F75111 Layout Picture



## Introduction

### How to use this Demo Application

```
WriteI2CByte(I2CADDR, CONFIG, 0x03); //Set Watch Dog Timer function
WriteI2CByte(I2CADDR, WDT_TIMER, timer); //Set Watch Dog Timer range from 0-255.
WriteI2CByte(I2CADDR, WDT_TIMER_CTL, 0x73); //Enable Watch Dog Timer in second and pulse mode
```

### How to use this Demo Application

```
WriteI2CByte(I2CADDR, WDT_TIMER_CTL, 0x00);
```

### How to use this Demo Application

```
void pause(int time)
{
    asm mov ah,0h;    //Ah = 00 Read System Time Counter
    asm int 1ah;      //read time from Time Counter and store it in DX register
    asm add dx,time;
    asm mov bx,dx;
    label:
    asm int 1ah;
    asm cmp bx,dx;
    asm jne label;
}
```

---

### 3-6-2 IO Device: F75111 under Windows

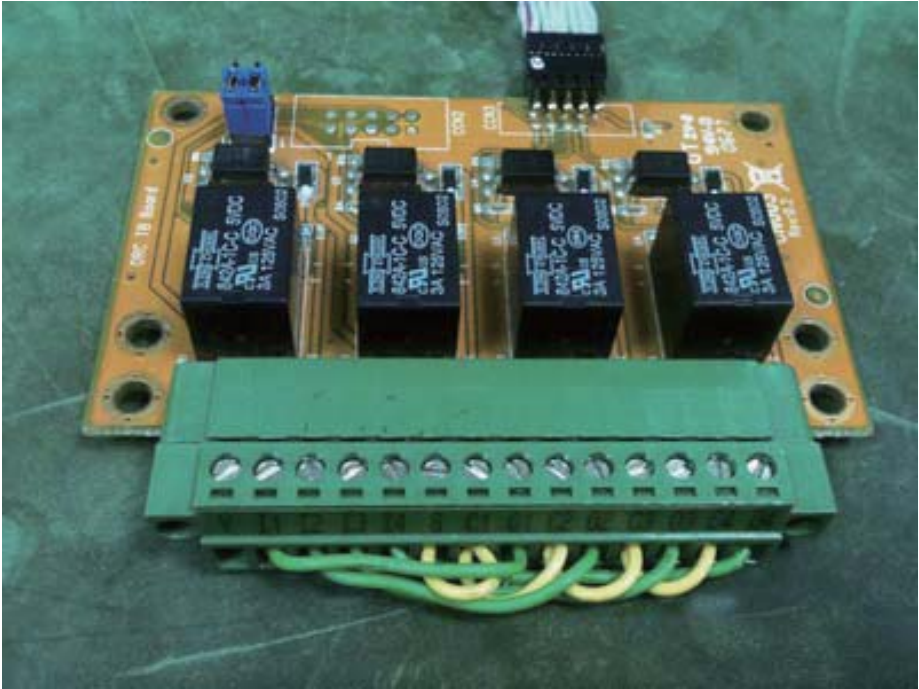
The Sample code source you can download from

Source file: F75111\_DIO\_Src\_v2.8W(32bit).zip [http://tprd.info/lexwiki/index.php/IO\\_Device:F75111](http://tprd.info/lexwiki/index.php/IO_Device:F75111)

Binary file: F75111\_DIO\_Bin\_v2.8W(32bit).zip

USERNAME & PASSWORD: sf

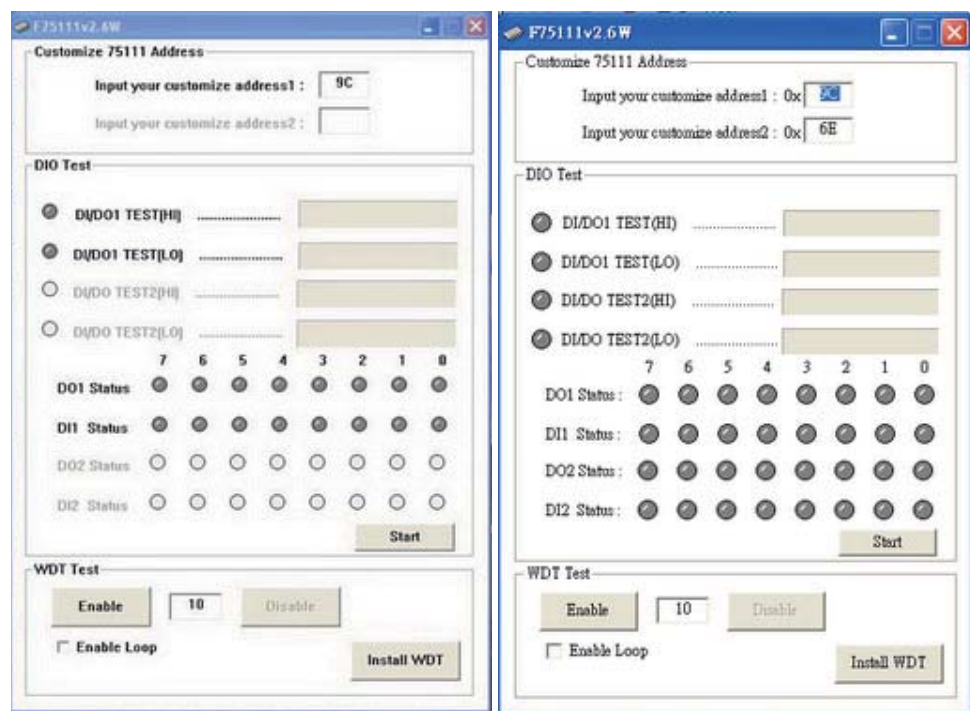
We do the demo test with a test tool which Dlx connect to DOx with Relay.



## How to use this Demo Application



one F75111

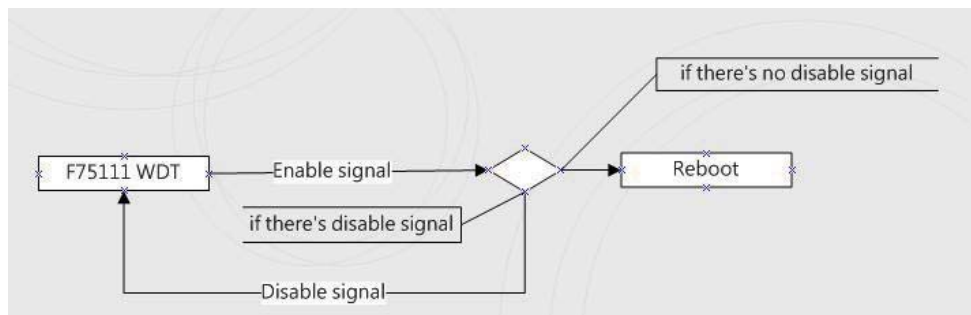
two F75111



Attention Please: You must be install vcredist\_x86.exe when first time you run the F75111\_DIO.exe DEMO AP, The vcredist\_x86.exe include all required DLL file.

WARNING: win7 system architecture, use the system administrator to open DIO utility

1. Press the "Start" button to test DIO function
2. Press the "Enable" button to test WDT function
3. Press the "Disable" button to disable WDT
4. Check the "Enable Loop" box and press "Enable" to do WDT loop test
5. Press "Install WDT" to set the system to autorun this application when booting, press again to remove this application when booting.
6. If WDT enable, system icon will be . if disable, system icon will be 



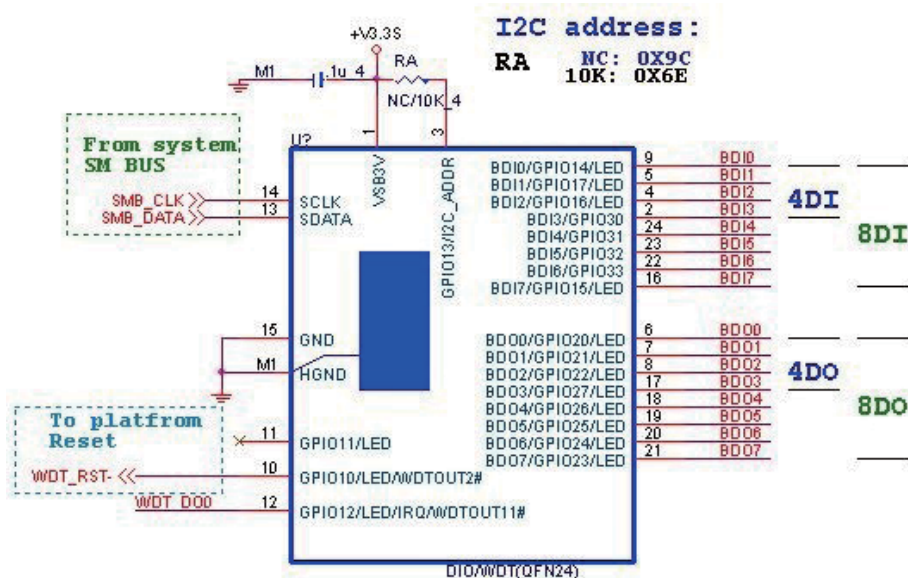
p.s.

f75111 send "F75111\_SetWDTEnable(BYTE byteTimer)" including a parameter "timer",

if there's no disable signal (F75111\_SetWDTDisable()) to stop it before timer countdown to 0, System will reboot.

if there's disable signal received, resent Enable WDT signal, for a loop to prevent from reboot

## F75111 Layout Picture



## Introduction

### Initial Internal F75111 port address (0x9c)

define GPIO1X, GPIO2X, GPIO3X to input or output  
 and Enable WDT function pin

### Set F75111 DI/DO ( sample code as below Get Input value/Set output value )

DO: InterDigitalOutput(BYTE byteValue))  
 DI: InterDigitalInput()

## Enable/Disable WDT

```
Enable : F75111_SetWDTEnable (BYTE byteTimer)
Disable: F75111_SetWDTDisable ()
```

## PULSE mode

### Sample to setting GP33, 32, 31, 30 output 1mS low pulse signal.

```
{
    this->Write_Byte(F75111_INTERNAL_ADDR, GPIO3X_PULSE_CONTROL,      0x00); //This is setting low pulse output
    this->Write_Byte(F75111_INTERNAL_ADDR, GPIO3X_PULSE_WIDTH_CONTROL, 0x01); //This selects the pulse width to 1mS
    this->Write_Byte(F75111_INTERNAL_ADDR, GPIO3X_CONTROL_MODE,        0x0F); //This is setting the GP33, 32, 31, 30 to output function.
    this->Write_Byte(F75111_INTERNAL_ADDR, GPIO3X_Output_Data ,        0x0F); //This is setting the GP33, 32, 31, 30 output data.
}
```

## Initial internal F75111

```
void F75111::InitInternalF75111()
{
    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO1X_CONTROL_MODE ,0x00); //set GPIO1X to Input function
    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO3X_CONTROL_MODE ,0x00); //set GPIO3X to Input function
    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO2X_CONTROL_MODE ,0xFF); //set GPIO2X to Output function

    this->Write_Byte(F75111_INTERNAL_ADDR,F75111_CONFIGURATION, 0x03); //Enable WDT OUT function
}
```

## Set output value

```
void F75111::InterDigitalOutput(BYTE byteValue)
{
    BYTE byteData = 0;
    byteData = (byteData & 0x01)? byteValue + 0x01 : byteValue;
    byteData = (byteData & 0x02)? byteValue + 0x02 : byteValue;
    byteData = (byteData & 0x04)? byteValue + 0x04 : byteValue;
    byteData = (byteData & 0x08)? byteValue + 0x08 : byteValue;
    byteData = (byteData & 0x40)? byteValue + 0x10 : byteValue;
    byteData = (byteData & 0x20)? byteValue + 0x20 : byteValue;
    byteData = (byteData & 0x10)? byteValue + 0x40 : byteValue;
    byteData = (byteData & 0x08)? byteValue + 0x80 : byteValue; // get value bit by bit

    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO2X_OUTPUT_DATA,byteData); // write byteData value via GPIO2X output pin
}
```



## Get Input value

```
BYTE F75111::InterDigitalInput()
{
    BYTE byteGPIO1X = 0;
    BYTE byteGPIO3X = 0;
    BYTE byteData    = 0;

    this->Read_Byte(F75111_INTERNAL_ADDR,GPIO1X_INPUT_DATA,&byteGPIO1X); // Get value from GPIO1X
    this->Read_Byte(F75111_INTERNAL_ADDR,GPIO3X_INPUT_DATA,&byteGPIO3X); // Get value from GPIO3X

    byteGPIO1X = byteGPIO1X & 0xF0; // Mask unuseful value
    byteGPIO3X = byteGPIO3X & 0x0F; // Mask unuseful value

    byteData = ( byteGPIO1X & 0x10 )? byteData + 0x01 : byteData;
    byteData = ( byteGPIO1X & 0x80 )? byteData + 0x02 : byteData;
    byteData = ( byteGPIO1X & 0x40 )? byteData + 0x04 : byteData;
    byteData = ( byteGPIO3X & 0x01 )? byteData + 0x08 : byteData;

    byteData = ( byteGPIO3X & 0x02 )? byteData + 0x10 : byteData;
    byteData = ( byteGPIO3X & 0x04 )? byteData + 0x20 : byteData;
    byteData = ( byteGPIO3X & 0x08 )? byteData + 0x40 : byteData;
    byteData = ( byteGPIO1X & 0x20 )? byteData + 0x80 : byteData; // Get correct DI value from GPIO1X & GPIO3X

    return byteData;
}
```

## Enable WatchDog

```
void F75111_SetWDTEnable (BYTE byteTimer)
{
    WriteByte(F75111_INTERNAL_ADDR,WDT_TIMER_RANGE ,byteTimer); // set WatchDog range and timer
    WriteByte(F75111_INTERNAL_ADDR,WDT_CONFIGURATION,WDT_TIMEOUT_FLAG | WDT_ENABLE | WDT_PULSE | WDT_PSWIDTH_100MS);
    // Enable WatchDog, Setting WatchDog configure
}
```

## Disable WatchDog

```
void F75111_SetWDTDisable ()
{
    WriteByte(F75111_INTERNAL_ADDR,WDT_CONFIGURATION,0x00); // Disable WatchDog
}
```

## define F75111 pin in F75111.h

```
//-----
#define F75111_INTERNAL_ADDR 0x9C // OnBoard F75111 Chipset
#define F75111_EXTERNAL_ADDR 0x6E // External F75111 Chipset
//-----
#define F75111_CONFIGURATION 0x03 // Configure GPIO13 to WDT2 Function
//-----
#define GPIO1X_CONTROL_MODE 0x10 // Select Output Mode or Input Mode
#define GPIO2X_CONTROL_MODE 0x20 // Select GPIO2X Output Mode or Input Mode
#define GPIO3X_CONTROL_MODE 0x40 // Select GPIO3X Output Mode or Input Mode
```

```

//-----
#define GPIO1X_INPUT_DATA 0x12 // GPIO1X Input
#define GPIO3X_INPUT_DATA 0x42 // GPIO3X Input
//-----
#define GPIO2X_OUTPUT_DATA 0x21 // GPIO2X Output
//-----
#define GPIO1X_PULSE_CONTROL 0x13 // GPIO1x Level/Pulse Control Register
// 0:Level Mode
// 1:Pulse Mode
#define GPIO1X_PULSE_WIDTH_CONTROL 0x14 // GPIO1x Pulse Width Control Register
#define GP1_PSWIDTH_500US 0x00 // When select Pulse mode: 500 us.
#define GP1_PSWIDTH_1MS 0x01 // When select Pulse mode: 1 ms.
#define GP1_PSWIDTH_20MS 0x02 // When select Pulse mode: 20 ms.
#define GP1_PSWIDTH_100MS 0x03 // When select Pulse mode: 100 ms.
//-----
#define GPIO2X_PULSE_CONTROL 0x23 // GPIO2x Level/Pulse Control Register
// 0:Level Mode
// 1:Pulse Mode
#define GPIO2X_PULSE_WIDTH_CONTROL 0x24 // GPIO2x Pulse Width Control Register
#define GP2_PSWIDTH_500US 0x00 // When select Pulse mode: 500 us.
#define GP2_PSWIDTH_1MS 0x01 // When select Pulse mode: 1 ms.
#define GP2_PSWIDTH_20MS 0x02 // When select Pulse mode: 20 ms.
#define GP2_PSWIDTH_100MS 0x03 // When select Pulse mode: 100 ms.
//-----
#define GPIO3X_PULSE_CONTROL 0x43 // GPIO3x Level/Pulse Control Register
// 0:Level Mode
// 1:Pulse Mode
#define GPIO3X_Output_Data 0x41 // GPIO3x Output Data Register
#define GPIO3X_PULSE_WIDTH_CONTROL 0x44 // GPIO3x Pulse Width Control Register
#define GP3_PSWIDTH_500US 0x00 // When select Pulse mode: 500 us.
#define GP3_PSWIDTH_1MS 0x01 // When select Pulse mode: 1 ms.
#define GP3_PSWIDTH_20MS 0x02 // When select Pulse mode: 20 ms.
#define GP3_PSWIDTH_100MS 0x03 // When select Pulse mode: 100 ms.
//-----
#define WDT_TIMER_RANGE 0x37 // 0-255 (second or minute program by WDT_UNIT)
#define WDT_CONFIGURATION 0x36 // Configure WDT Function
#define WDT_TIMEOUT_FLAG 0x40 // When watchdog timeout.this bit will be set to 1.
#define WDT_ENABLE 0x20 // Enable watchdog timer
#define WDT_PULSE 0x10 // Configure WDT output mode
// 0:Level Mode
// 1:Pulse Mode

#define WDT_UNIT 0x08 // Watchdog unit select.
// 0:Select second.
// 1:Select minute.

#define WDT_LEVEL 0x04 // When select level output mode:
// 0:Level low
// 1:Level high

#define WDT_PSWIDTH_1MS 0x00 // When select Pulse mode: 1 ms.
#define WDT_PSWIDTH_20MS 0x01 // When select Pulse mode: 20 ms.
#define WDT_PSWIDTH_100MS 0x02 // When select Pulse mode: 100 ms.
#define WDT_PSWIDTH_4000MS 0x03 // When select Pulse mode: 4 s.

```

### 3-6-3 IO Device: F75111 VB6 under Windows

The Sample code source you can download from

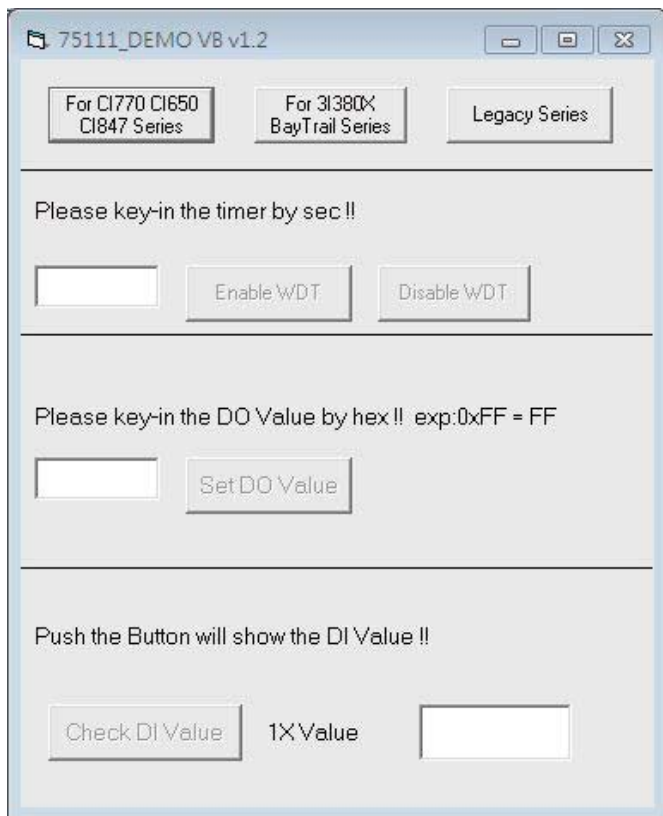
Source file: 75111\_VB\_v1.2.rar

[http://tprd.info/lexwiki/index.php/IO\\_Device:F75111\\_VB6](http://tprd.info/lexwiki/index.php/IO_Device:F75111_VB6)

Binary file: 75111\_VB\_Src1.2.rar

USERNAME & PASSWORD: sf

How to use this Demo Application



- A Function - Choose your motherboard model
- B Function - Enable WDT timer ,Key-in the value by seconds then system will reboot after value which you key-in in left text box !!
- C Function - Disable WDT timer ,Push down the button then WDT timer value will be clear !!
- D Function - Set DO Value ,Key-in the DO value by hex then push the button !!
- E Function - Check DI Value ,The right side two text box will display DI 1X & 2X Value when you push down the button!!

## SDK Function Introduction

## ! Function EnableWDT(timer As Integer)

End Function

```
Function DisableWDT()
```

End Function



RA NC: 0X9C  
10K: 0X6E

## Function SetDOValue

```
Function SetDOValue(dovalue As Integer)
```

```
Dim Data As Integer
```

```
Dim Value As Integer
```

```
Data = 0
```

```
Value = dovalue
```

```
If (Value And &H1) <> 0 Then
```

```
    Data = Data + &H1
```

```
End If
```

```
If (Value And &H2) <> 0 Then
```

```
    Data = Data + &H2
```

```
End If
```

```
If (Value And &H4) <> 0 Then
```

```
    Data = Data + &H4
```

```
End If
```

```
If (Value And &H80) <> 0 Then
```

```
    Data = Data + &H8
```

```
End If
```

```
If (Value And &H40) <> 0 Then
```

```
    Data = Data + &H10
```

```
End If
```

```
If (Value And &H20) <> 0 Then
```

```
    Data = Data + &H20
```

```
End If
```

```
If (Value And &H10) <> 0 Then
```

```
    Data = Data + &H40
```

```
End If
```

```
If (Value And &H8) <> 0 Then
```

```
    Data = Data + &H80
```

```
End If
```

```
Call Writel2CByte(&H23, &H0)
```

```
Call Writel2CByte(&H20, &HFF)
```

```
Call Writel2CByte(&H2B, &HFF)
```

```
Call Writel2CByte(&H21, Data)
```

```
End Function
```

## Function CheckDIValue

```
Function CheckDIValue()  
Dim GPIO1X As Integer  
Dim GPIO3X As Integer  
Dim DI1Xhex As String  
Dim DI3Xhex As String  
  
Dim Data As Long  
  
Data = 0  
  
Call ReadI2CByte(&H12, GPIO1X)  
Call ReadI2CByte(&H42, GPIO3X)  
  
GPIO1X = GPIO1X And &HF0  
GPIO3X = GPIO3X And &HF  
  
If (GPIO1X And &H10) <> 0 Then  
    Data = Data + &H1  
End If  
  
If (GPIO1X And &H80) <> 0 Then  
    Data = Data + &H2  
End If  
  
If (GPIO1X And &H40) <> 0 Then  
    Data = Data + &H4  
End If  
  
If (GPIO3X And &H1) <> 0 Then  
    Data = Data + &H8  
End If  
  
If (GPIO3X And &H2) <> 0 Then  
    Data = Data + &H10  
End If  
  
If (GPIO3X And &H4) <> 0 Then  
    Data = Data + &H20  
End If  
  
If (GPIO3X And &H8) <> 0 Then  
    Data = Data + &H40  
End If  
  
If (GPIO1X And &H20) <> 0 Then  
    Data = Data + &H80  
End If  
  
DI1Xhex = Hex(Data)  
  
Text3.Text = "0x" + DI1Xhex  
  
End Function
```

### 3-6-4 IO Device: F75111 under linux

The Sample code source you can download from

Source file: F75111v2.4L\_SRC.tar.gz      [http://tprd.info/lexwiki/index.php/IO\\_Device:F75111\\_under\\_linux](http://tprd.info/lexwiki/index.php/IO_Device:F75111_under_linux)

Binary file: F75111v2.4L\_BIN.tar.gz

USERNAME & PASSWORD: sf

How to compile source code

1. Compile source code with Code::Blocks

download and install the Code::Block with command "apt-get install codeblocks"

Open an exist project(F75111.cbp) in Code::Blocks, click the compile button

( add an option 'pkg-config --libs gtk+-2.0 gthread-2.0' in "Project->Build Option->Linker Setting->Other linker option")

2. Compile source code with "make"

1. cd F75111

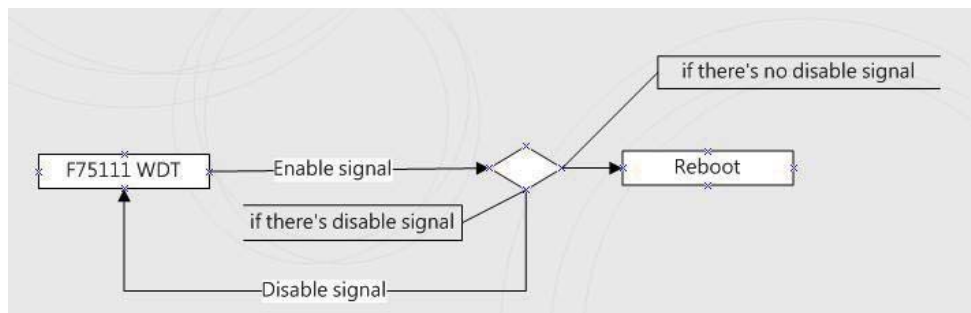
1. make

1. src/f75111 // execute the binary file

How to use this Demo Application



1. Press the "Start" button to test DIO function
2. Press the "Enable" button to test WDT function
3. Press the "Disable" button to disable WDT
4. Check the "Enable Loop" box and press "Enable" to do WDT loop test
5. Press "Install" to set the system to autorun this application when booting, press "Uninstall" to remove this application when booting.
6. If WDT enable, system icon will be blinking.



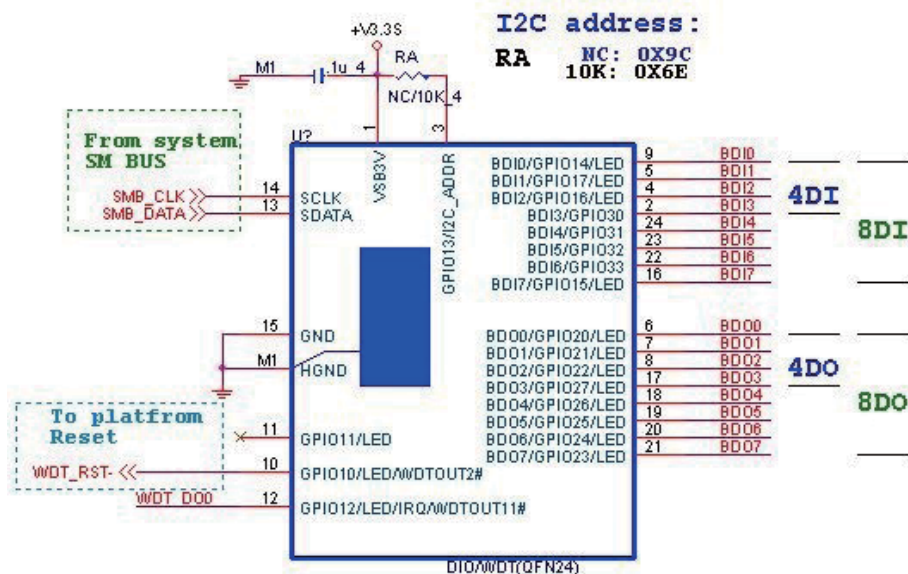
p.s.

f75111 send "F75111\_SetWDTEnable(BYTE byteTimer)" including a parameter "timer",

if there's no disable signal (F75111\_SetWDTDisable()) to stop it before timer countdown to 0, System will reboot.

if there's disable signal received, resent Enable WDT signal, for a loop to prevent from reboot p.s.

## F75111 Layout Picture





## Introduction

### IO function In file SMBus.c

```
void SMBusIoWrite(BYTE byteOffset,BYTE byteData)
{
    outb( byteData , m_SMBusMapIoAddr + byteOffset);
}

BYTE SMBusIoRead(BYTE byteOffset)
{
    DWORD dwAddrVal;

    dwAddrVal = inb(m_SMBusMapIoAddr + byteOffset);
    return (BYTE)(dwAddrVal & 0xFF);
}
```

### Initial internal F75111

```
void F75111::InitInternalF75111()
{
    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO1X_CONTROL_MODE ,0x00);    //set GPIO1X to Input  function
    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO3X_CONTROL_MODE ,0x00);    //set GPIO3X to Input  function
    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO2X_CONTROL_MODE ,0xFF);    //set GPIO2X to Output function

    this->Write_Byte(F75111_INTERNAL_ADDR,F75111_CONFIGURATION, 0x03);    //Enable WDT OUT function
}
```

### Set output value

```
void F75111::InterDigitalOutput(BYTE byteValue)
{
    BYTE byteData = 0;
    byteData = (byteData & 0x01 )? byteValue + 0x01 : byteValue;
    byteData = (byteData & 0x02 )? byteValue + 0x02 : byteValue;
    byteData = (byteData & 0x04 )? byteValue + 0x04 : byteValue;
    byteData = (byteData & 0x08 )? byteValue + 0x08 : byteValue;
    byteData = (byteData & 0x40 )? byteValue + 0x10 : byteValue;
    byteData = (byteData & 0x20 )? byteValue + 0x20 : byteValue;
    byteData = (byteData & 0x10 )? byteValue + 0x40 : byteValue;
    byteData = (byteData & 0x08 )? byteValue + 0x80 : byteValue;    // get value bit by bit

    this->Write_Byte(F75111_INTERNAL_ADDR,GPIO2X_OUTPUT_DATA,byteData);    // write byteData value via GPIO2X output pin
}
```

## Get Input value

```
BYTE F75111::InterDigitalInput()
{
    BYTE byteGPIO1X = 0;
    BYTE byteGPIO3X = 0;
    BYTE byteData = 0;

    this->Read_Byte(F75111_INTERNAL_ADDR,GPIO1X_INPUT_DATA,&byteGPIO1X); // Get value from GPIO1X
    this->Read_Byte(F75111_INTERNAL_ADDR,GPIO3X_INPUT_DATA,&byteGPIO3X); // Get value from GPIO3X

    byteGPIO1X = byteGPIO1X & 0xF0; // Mask unuseful value
    byteGPIO3X = byteGPIO3X & 0x0F; // Mask unuseful value

    byteData = ( byteGPIO1X & 0x10 )? byteData + 0x01 : byteData;
    byteData = ( byteGPIO1X & 0x80 )? byteData + 0x02 : byteData;
    byteData = ( byteGPIO1X & 0x40 )? byteData + 0x04 : byteData;
    byteData = ( byteGPIO3X & 0x01 )? byteData + 0x08 : byteData;

    byteData = ( byteGPIO3X & 0x02 )? byteData + 0x10 : byteData;
    byteData = ( byteGPIO3X & 0x04 )? byteData + 0x20 : byteData;
    byteData = ( byteGPIO3X & 0x08 )? byteData + 0x40 : byteData;
    byteData = ( byteGPIO1X & 0x20 )? byteData + 0x80 : byteData; // Get correct DI value from GPIO1X & GPIO3X

    return byteData;
}
```

## Enable WatchDog

```
void F75111_SetWDTEnable (BYTE byteTimer)
{
    WriteByte(F75111_INTERNAL_ADDR,WDT_TIMER_RANGE ,byteTimer); // set WatchDog range and timer
    WriteByte(F75111_INTERNAL_ADDR,WDT_CONFIGURATION,WDT_TIMEOUT_FLAG | WDT_ENABLE | WDT_PULSE | WDT_PSWIDTH_100MS);
    // Enable WatchDog, Setting WatchDog configure
}
```

## Disable WatchDog

```
void F75111_SetWDTDisable ()
{
    WriteByte(F75111_INTERNAL_ADDR,WDT_CONFIGURATION,0x00); // Disable WatchDog
}
```

```

//-----
#define F75111_INTERNAL_ADDR          0x9C // OnBoard F75111 Chipset
#define F75111_EXTERNAL_ADDR         0x6E // External F75111 Chipset
//-----
#define F75111_CONFIGURATION          0x03 // Configure GPIO13 to WDT2 Function
//-----
#define GPIO1X_CONTROL_MODE           0x10 // Select Output Mode or Input Mode
#define GPIO2X_CONTROL_MODE           0x20 // Select GPIO2X Output Mode or Input Mode
#define GPIO3X_CONTROL_MODE           0x40 // Select GPIO3X Output Mode or Input Mode
//-----
#define GPIO1X_INPUT_DATA              0x12 // GPIO1X Input
#define GPIO3X_INPUT_DATA              0x42 // GPIO3X Input
//-----
#define GPIO2X_OUTPUT_DATA            0x21 // GPIO2X Output
//-----
#define GPIO2X_OUTPUT_DRIVING          0x2B // Select GPIO2X Output Mode or Input Mode
//-----
#define WDT_TIMER_RANGE                0x37 // 0-255 (second or minute program by WDT_UNIT)
//-----
#define WDT_CONFIGURATION              0x36 // Configure WDT Function
#define WDT_TIMEOUT_FLAG               0x40 // When watchdog timeout,this bit will be set to 1.
#define WDT_ENABLE                     0x20 // Enable watchdog timer
#define WDT_PULSE                      0x10 // Configure WDT output mode
// 0:Level Mode
// 1:Pulse Mode
#define WDT_UNIT                      0x08 // Watchdog unit select.
// 0:Select second.
// 1:Select minute.
#define WDT_LEVEL                     0x04 // When select level output mode:
// 0:Level low
// 1:Level high
#define WDT_PSWIDTH_1MS                0x00 // When select Pulse mode: 1 ms.
#define WDT_PSWIDTH_20MS               0x01 // When select Pulse mode: 20 ms.
#define WDT_PSWIDTH_100MS              0x02 // When select Pulse mode: 100 ms.
#define WDT_PSWIDTH_4000MS             0x03 // When select Pulse mode: 4 s.
//-----
typedef struct F75111_Address
{
    BYTE bAddress;
}F75111_Address;
F75111_Address m_F75111;

bool      F75111_Init();
BYTE      F75111_GetDigitalInput ();
void      F75111_SetDigitalOutput(BYTE byteValue);

BYTE      F75111_GetWDTMode();
void      F75111_SetWDTMode(BYTE dwvalue);

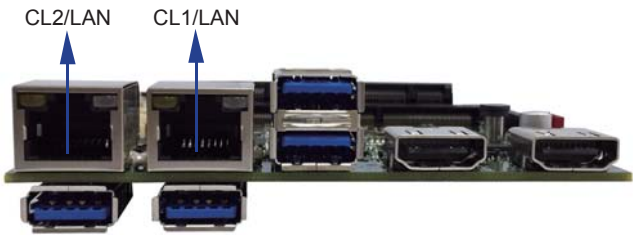
void      F75111_SetWDTEnable (BYTE byteTimer);
void      F75111_SetWDTDisable ();

```

### 3-7 LAN Interface

● CL1/CL2: RJ45 LAN port Giga / 100Mb (RJ45 Jack)

PIN NO.	Description	PIN NO.	Description
1	TR0+/TX+	2	TR2-/NC
3	TR0-/TX-	4	TR1-/RX-
5	TR1+/RX+	6	TR3+/NC
7	TR2+/NC	8	TR3-/NC



● CL11/CL21: LAN port Giga / 100Mb wafer connector (2x4pin 2.0mm) (Option)

PIN NO.	Description	PIN NO.	Description
1	TR0+/TX+	2	TR0+/TX+
3	TR2+/NC	4	TR1+/RX+
5	TR1+/RX+	6	TR2-/NC
7	TR3-/NC	8	TR3+/NC

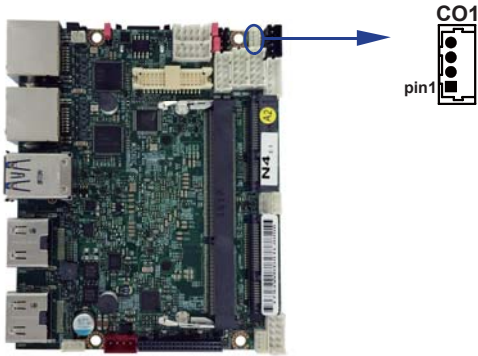
● RJ45 LAN Connector--- LED define Giga / 100 / 10MB Connector

SPEED		10 Mbps			100 Mbps			1000 Mbps		
Indicate	Side	Back		Front	Back		Front	Back		Front
	LED	Link	ACT	ACT	Link	ACT	ACT	Link	ACT	ACT
LAN Light			Orange	Orange	Green	Orange	Orange	Red	Orange	Orange

### 3-8 I<sup>2</sup>C Bus Interface

- CO1: I<sup>2</sup>C-Bus wafer connector (1x4pin 1.25mm)

PIN NO.	Description
1	+3.3V
2	GND
3	I <sup>2</sup> C Clock
4	I <sup>2</sup> C DATA

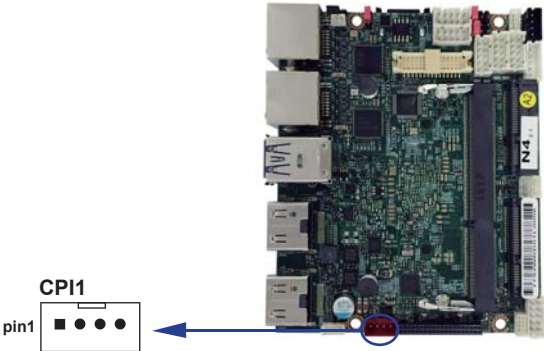


### 3-9 DC Power Input

- CPI1: DC-in wafer connector (Red) (1x4pin 2.0mm)

PIN NO.	Description
2,3	DC-IN(12V)
1,4	GND

Note: 1. Very important check DC-in Voltage type for 12V or 9~36V model spec

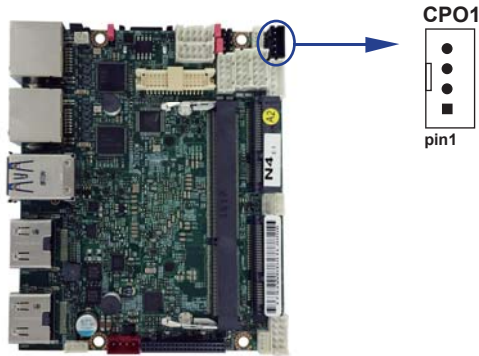


### 3-10 DC +12V / +5 Voltage Power Output

● CPO1: +12V / +5V DC voltage output wafer connector (Black) (1x4pin 2.0mm)

PIN NO.	Description
1	+5V
2	GND
3	GND
4	+12V *

\* Note: Attention! Check Device Power in spec



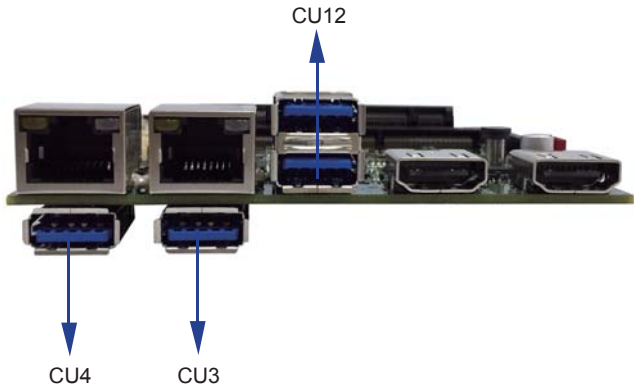
### 3-11 USB Interface

● **CU12: Dual-USB3.0 / 2.0 Type A Connector**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+5V	10	+5V
2	USB 2.0 D-(UP)	11	USB 2.0 D-(DOWN)
3	USB 2.0 D+(UP)	12	USB 2.0 D+(DOWN)
4	GND	13	GND
5	USB3.0 RX-(UP)	14	USB3.0 RX-(DOWN)
6	USB3.0 RX+(UP)	15	USB3.0 RX+(DOWN)
7	GND	16	GND
8	USB3.0 TX-(UP)	17	USB3.0 TX-(DOWN)
9	USB3.0 TX+(UP)	18	USB3.0 TX+(DOWN)

● **CU3 / CU4: USB3.0 / 2.0 Type A Connector**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
		10	GND
1	+5V	9	USB3.0 TX+
2	USB 2.0 D-	8	USB3.0 TX-
		7	GND
3	USB 2.0 D+	6	USB3.0 RX+
4	GND		
		5	USB3.0 RX-

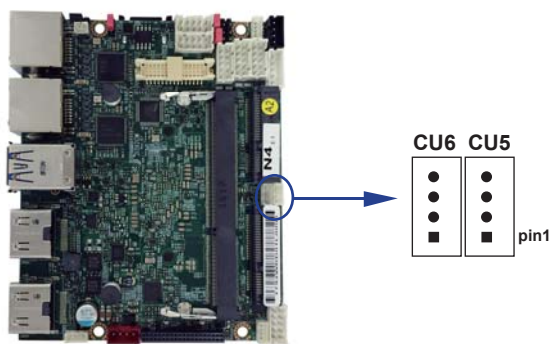


● **CU5 / CU6 / CU7: USB2.0 port wafer (1x4pin 1.25mm)**

PIN NO.	DESCRIPTION
1	+5V
2	DATA-
3	DATA+
4	GND

Note:

- 1. CU5 USB signal share with MPCE1.
- 2. CU6 USB signal share with MPCE2.
- 3. CU7 USB signal share with EIO1.





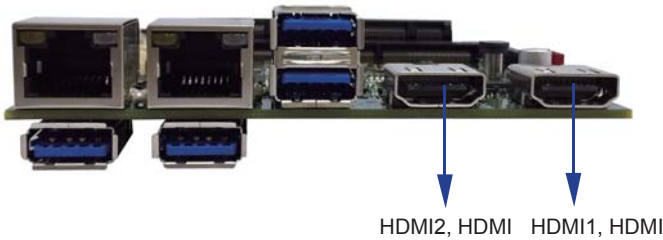
### 3-12 Display interface

● HDMI1 / HDMI2: HDMI Connector (Type A)

PIN	Description	PIN	Description
1	TMDS DATA2+	2	GND
3	TMDS DATA2-	4	TMDS DATA1+
5	GND	6	TMDS DATA1-
7	TMDS DATA0+	8	GND
9	TMDS DATA0-	10	TMDS CLK+
11	GND	12	TMDS CLK-
13	NC	14	NC
15	DDC CLK	16	DDC DATA
17	GND	18	+5V
19	H.P. Detect		

● DP1 / DP2: DP Connector (Option)

PIN	Description	PIN	Description
1	LANE0_P	2	GND
3	LANE0_N	4	LANE1_P
5	GND	6	LANE1_N
7	LANE2_P	8	GND
9	LANE2_N	10	LANE3_P
11	GND	12	LANE3_N
13	GND	14	GND
15	AUX_DP	16	GND
17	AUX_DN	18	H.P. Detect
19	GND	20	+3.3V

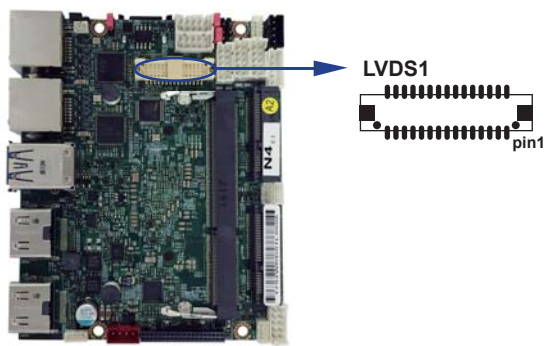


● **LVDS1: LVDS interface (2x15 pin 1.25mm wafer)**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	PWM dimming	2	+5V
3	+LCD(5V or 3.3V)	4	+LCD(5V or 3.3V)
5	Channel-1-DATA3+	6	Channel-0-DATA3+
7	Channel-1-DATA3-	8	Channel-0-DATA3-
9	Channel-0-DATA2+	10	Channel-0-CLK+
11	Channel-0-DATA2-	12	Channel-0-CLK-
13	GND	14	GND
15	Channel-0-DATA1+	16	Channel-0-DATA0+
17	Channel-0-DATA1-	18	Channel-0-DATA0-
19	GND	20	GND
21	+LCD(5V or 3.3V)	22	+LCD(5V or 3.3V)
23	Channel-1-DATA2+	24	Channel-1-CLK+
25	Channel-1-DATA2-	26	Channel-1-CLK-
27	Channel-1-DATA1+	28	Channel-1-DATA0+
29	Channel-1-DATA1-	30	Channel-1-DATA0-

Note:

1. LVDS interface support 18 / 24bits two channel.
2. JVL1: LVDS panel +5V / +3.3V (default) Voltage select.
3. LVDS1 PIN 1 for panel backlight dimming control.
4. Pin 1 back light dimming control. Provided 200Hz / 275Hz / 380 Hz / 20KHz / 25KHz / 100Hz and adjust PWM duty cycle by software program.



• LVDS Panel resolution table list

Item	Resolution	Channel	bits
1	640 x 480	1	18
2	800 x 600	1	18
3	1024 x 768	1	18
4	800 x 480	1	18
5	1024 x 600	1	18
6	1280 x 800	1	18
7	1366 x 768	1	18
8	800 x 600	1	24
9	1024 x 768	1	24
10	1280 x 800	1	24
11	1366 x 768	1	24
12	1280 x 1024	2	24
13	1440 x 900	2	24
14	1600 x 1200	2	24
15	1920 x 1080	2	24

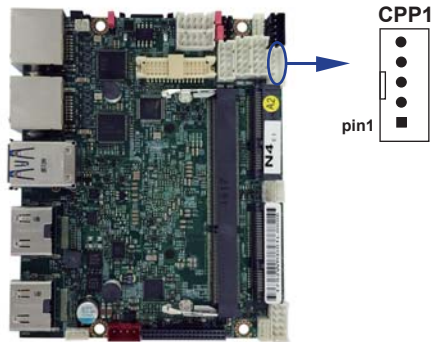
The item 3 is default resolution.

### 3-13 CPP1: Panel Inverter power (1x5pin 2.0mm wafer)

PIN NO.	DESCRIPTION
1	+12V
2	GND
3	PWM dimming
4	ENBK1 (3.3V)
5	ENBK1 ( 5V )

Note:

1. CPP1 PIN 3 and LVDS1 PIN1 is same signal.
2. Pin 3 back light dimming controls, provided 200Hz / 275Hz / 380Hz / 20KHz / 25KHz / 100Hz and adjust PWM duty cycle by software program.



### 3-14 CT1: Touch screen (2x5 pin 2.0mm wafer) USB interface

● For 8-wire type pin define

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	Bottom	2	Bottom Sense
3	Top Sense	4	Top
5	Right	6	Right Sense
7	Left	8	Left Sense
9	GND	10	NC

Note: 1. For eight wire type cable Pin 3 and Pin4 need short.

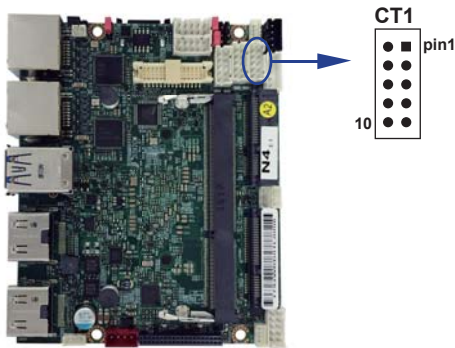
● For 4-wire type pin define

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	Bottom	2	N/A
3	N/A	4	Top
5	Right	6	N/A
7	Left	8	N/A
9	GND	10	NC

Note: 1. For four wire type cable Pin 3 and Pin4 need short.

● For 5-wire type pin define

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	UR(H)	2	N/A
3	Sense	4	UL(Y)
5	LR(X)	6	N/A
7	LL(L)	8	N/A
9	GND	10	NC



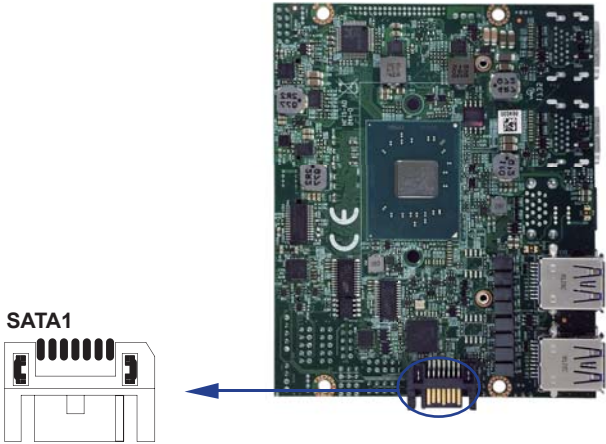
### 3-15 SATA Interface

• **SATA1: SATA Port 1x7pin connector**

PIN NO.	Description
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

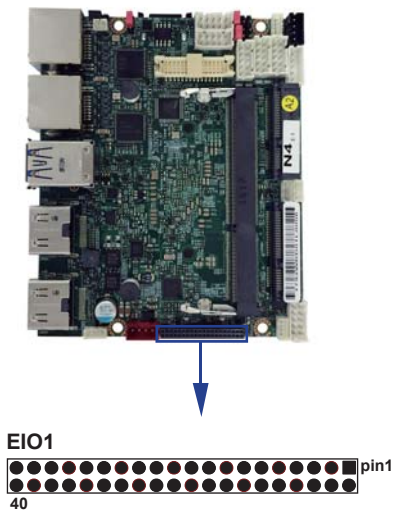
Note:

- 1. SATA1 support SATA 2.0 spec update 3Gb/sec.
- 2. CPO1 provide SATA HDD power +12V, GND, +5V



### 3-16 LEX eIO connector.

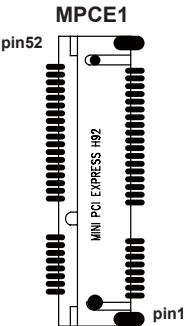
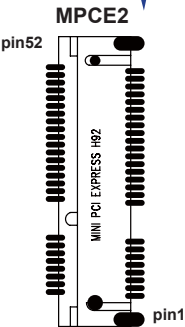
PIN	Description	PIN	Description
1	+12V	2	+12V
3	GND	4	RESET
5	PCle_TX0_P	6	RESERVED
7	PCle_TX0_N	8	GND
9	GND	10	PCle_P0_RX_N
11	PCle_CLK0_P	12	PCle_P0_RX_P
13	PCle_CLK_0_N	14	GND
15	GND	16	PCle_P1_RX_N
17	PCle_TX1_P	18	PCle_P1_RX_P
19	PCle_TX1_N	20	GND
21	GND	22	RESERVED
23	PCle_CLK1_P	24	RESERVED
25	PCle_CLK1_N	26	GND
27	GND	28	SMB_CLK
29	USB_P	30	SMB_DATA
31	USB_N	32	GND
33	GND	34	SLP_S3_N
35	+12V	36	RESERVED
37	+12V	38	GND
39	+12V	40	+12V



### 3-17 PCI Express Mini card

- MPCE1 / MPCE2: Full size mini card  
 MPCE1: Support USB and PCIe Interface (PCIe signal share with EIO)  
 MPCE2: Support USB and mSATA Interface

PIN NO.	Description	PIN NO.	Description
1	NC	2	+3.3V
3	NC	4	GND
5	NC	6	+1.5V
7	NC	8	NC
9	GND	10	NC
11	PCIe-CLK-	12	NC
13	PCIe-CLK+	14	NC
15	GND	16	NC
KEY			
17	NC	18	GND
19	NC	20	NC
21	GND	22	Reset
23	PCIe-RX-/mSATA-RX+	24	+3.3V
25	PCIe-RX+/mSATA-RX-	26	GND
27	GND	28	+1.5V
29	GND	30	SMB-CLK
31	PCIe-TX-/mSATA-TX-	32	SMB-DATA
33	PCIe-TX+/mSATA-TX+	34	GND
35	GND	36	USB-DATA-
37	GND	38	USB-DATA+
39	+3.3V	40	GND
41	+3.3V	42	NC
43	GND	44	NC
45	NC	46	NC
47	NC	48	+1.5V
49	NC	50	GND
51	mSATA/PCIe detect	52	+3.3V



### 3-18 Connector wafer of Compatible Brand and part number list

Location	CKTS	PITCH	Brand Name	Mating connector	Cable housing
CA3	2x5 10Pin	2.0mm	JST	B10B-PHDSS	PHDR-10VS
CC1	2x5 10Pin	2.0mm	JST	B10B-PHDSS	PHDR-10VS
CC2	2x5 10Pin	2.0mm	JST	B10B-PHDSS	PHDR-10VS
CBT1	1x2 2Pin	1.25mm	MOLEX	53047-0210	51021-0200
CFP1	2x5 10Pin	2.0mm	JST	B10B-PHDSS	PHDR-10VS
CIO1	2x5 10Pin	2.0mm	JST	B10B-PHDSS	PHDR-10VS
CL11	2x4 8Pin	2.0mm	JST	B8B-PHDSS	PHDR-08VS
CL21	2x4 8Pin	2.0mm	JST	B8B-PHDSS	PHDR-08VS
CO1	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CPI1	1x4 4Pin	2.0mm	JST	B4B-PH-KL	PHR-4
CALR1	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CPO1	1x4 4Pin	2.0mm	JST	B4B-PH-KL	PHR-4
CT1	2x5 5Pin	2.0mm	JST	B10B-PHDSS	PHDR-10VS
CU5	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CU6	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CU7	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
LVDS1	2x15 30Pin	1.25mm	HIROSE	DF13-30DS-1.25C	DF13-30DP-1.25V
CPP1	1x5 5Pin	2.0mm	JST	B5B-PH-KL	PHR-5
EIO1	2x20 20Pin	1.27mm	C.W	F603-SGN-D025-40	



---

# Chapter-4

## Introduction of BIOS

The BIOS is a program located in the Flash Memory on the motherboard.

This program is a bridge between motherboard and operating system.

When you start the computer, the BIOS program gains control.

The BIOS first operates an auto-diagnostic test called POST (Power on Self Test) for all the necessary hardware, it detects the entire hardware devices and configures the parameters of the hardware synchronization. After these tasks are completed, BIOS will give control of the computer back to operating system (OS). Since the BIOS is the only channel for hardware and software to communicate with, it is the key factor of system stability and of ensuring your system performance at best.

In the BIOS Setup main menu, you can see several options. We will explain these options in the following pages. First, let us see the function keys you may use here:

Press <Esc> to quit the BIOS Setup.

Press ↑↓←→(up, down, left, right) to choose the option you want to confirm or modify.

Press <F10> to save these parameters and to exit the BIOS Setup menu after you complete the setup of BIOS parameters.

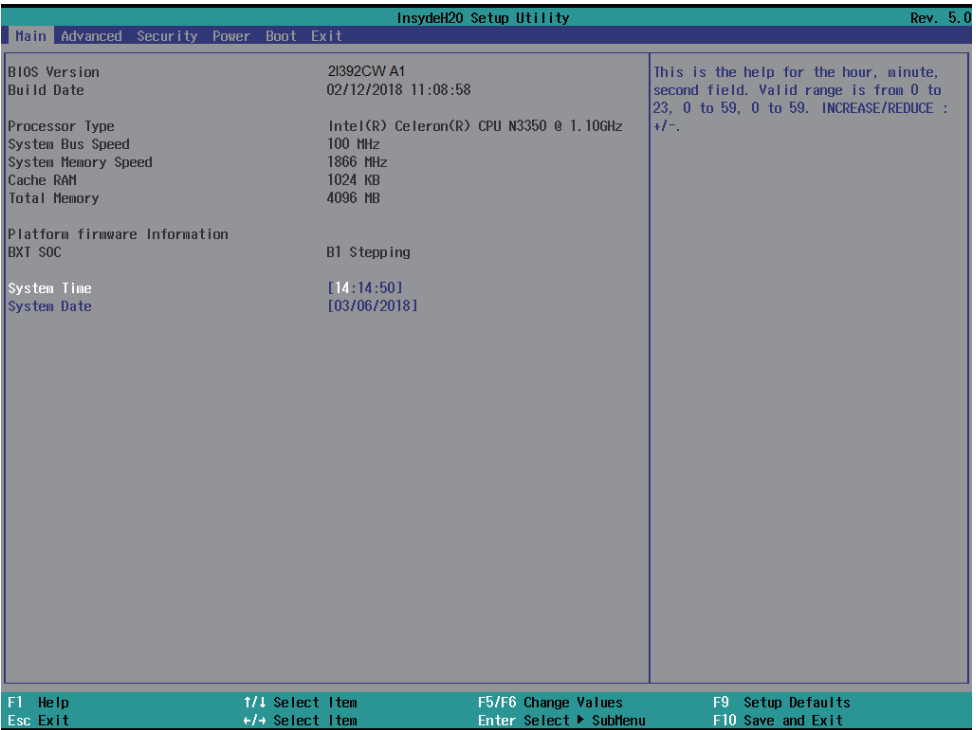
Press Page Up/Page Down or +/- keys to modify the BIOS parameters for the active option.

## 4-1 Enter Setup

Power on the computer and press <Del> key immediately to enter Setup.

If the message disappears before your respond but you still wish to enter Setup, restart the system by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart the system by simultaneously pressing <Ctrl>, <Alt> and <Delete> keys.

## 4-2 BIOS Menu Screen & Function Keys



**There are six menu bars on top of BIOS screen:**

Main To change system basic configuration

Advanced To change system advanced configuration

Security Password settings

Power ACPI and wake device settings

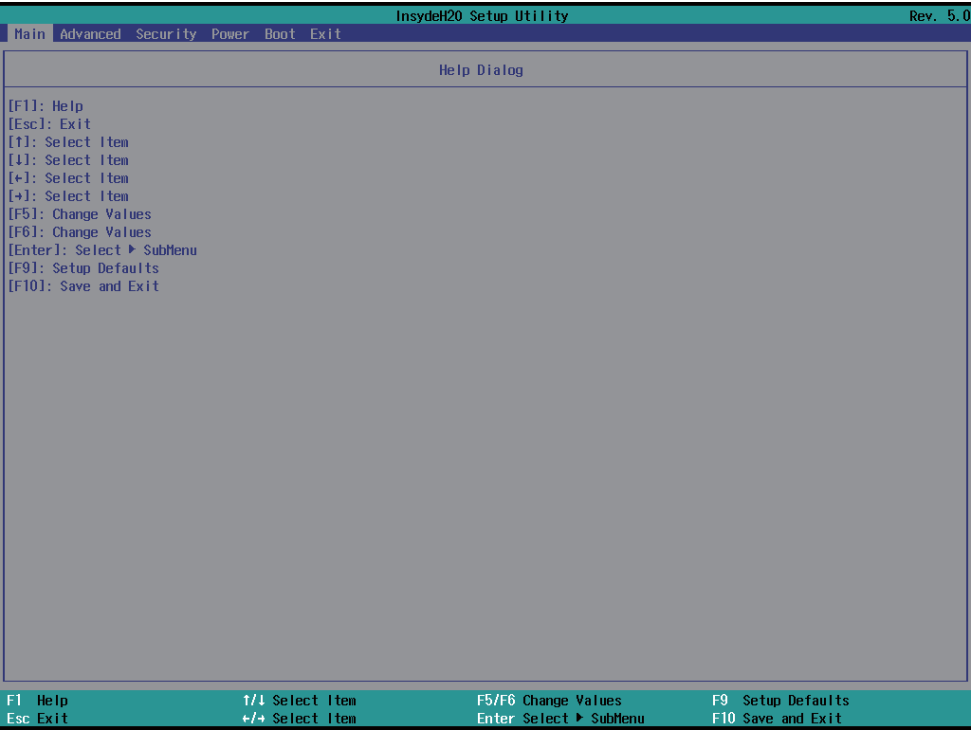
Boot To change system boot configuration

Exit Save setting, loading and exit options.

User can press the right or left arrow key on the keyboard to switch from menu bar.

The selected one is highlighted.

## 4-3 Getting Help



### Status Page Setup Menu / Option Page Setup Menu

Press F1 to pop up a help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window, press <Esc>.

We will explain these options step by step in the following pages of this chapter, but let us first see a short description of the function keys you may use here:

- Press ←→ (left, right) to select screen;
- Press ↑↓ (up, down) to choose, in the main menu, the option you want to confirm or to modify.
- Press <Enter> to select.
- Press <+>/<-> or <F5>/<F6> keys when you want to modify the BIOS parameters for the active option.
- [F1]: General help.
- [F9]: Optimized defaults.
- [F10]: Save & Exit.
- Press <Esc> to quit the BIOS Setup.

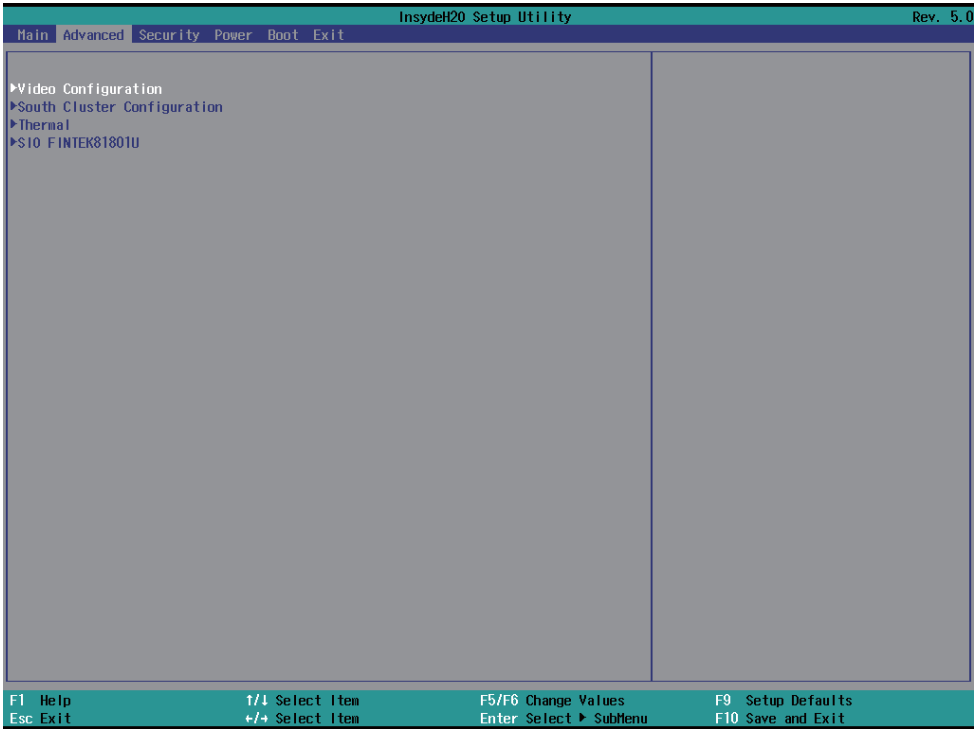
InsydeH20 Setup Utility			Rev. 5.0		
Main	Advanced	Security	Power	Boot	Exit
BIOS Version		2I392CWA1		This is the help for the hour, minute, second field. Valid range is from 0 to 23, 0 to 59, 0 to 59. INCREASE/REDUCE : +/-.	
Build Date		02/12/2018 11:08:58			
Processor Type		Intel(R) Celeron(R) CPU N3350 @ 1.10GHz			
System Bus Speed		100 MHz			
System Memory Speed		1866 MHz			
Cache RAM		1024 KB			
Total Memory		4096 MB			
Platform firmware Information		B1 Stepping			
System Time		[14:14:50]			
System Date		[03/06/2018]			
<div> <div>F1 Help</div> <div>Esc Exit</div> </div> <div> <div>F5/F6 Change Values</div> <div>Enter Select &gt; SubMenu</div> </div> <div> <div>F9 Setup Defaults</div> <div>F10 Save and Exit</div> </div> <div> <div>T/1 Select Item</div> <div>+/- Select Item</div> </div>					

System Date

## System Time

64

# 4-5 Advanced



## Video Configuration

Please refer section 4-5-1

## South Cluster Configuration

Please refer section 4-5-2

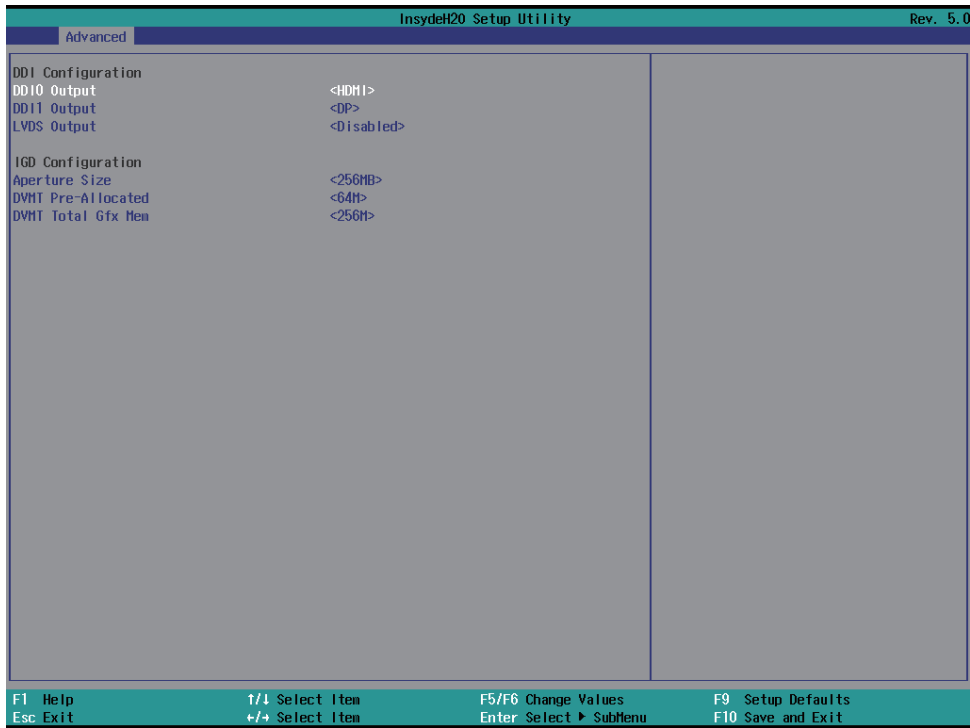
## Thermal

Please refer section 4-5-3

## SIO FINETEK 81801U

Please refer section 4-5-4

# 4-5-1 Video Configuration



## Aperture Size

The optional settings are: 128MB, 256MB (default), 512MB.

## IGD - DVMT Pre-Allocated

Use this item to select DVMT 5.0 pre-allocated (fixed) graphics memory size used by the internal graphics device.

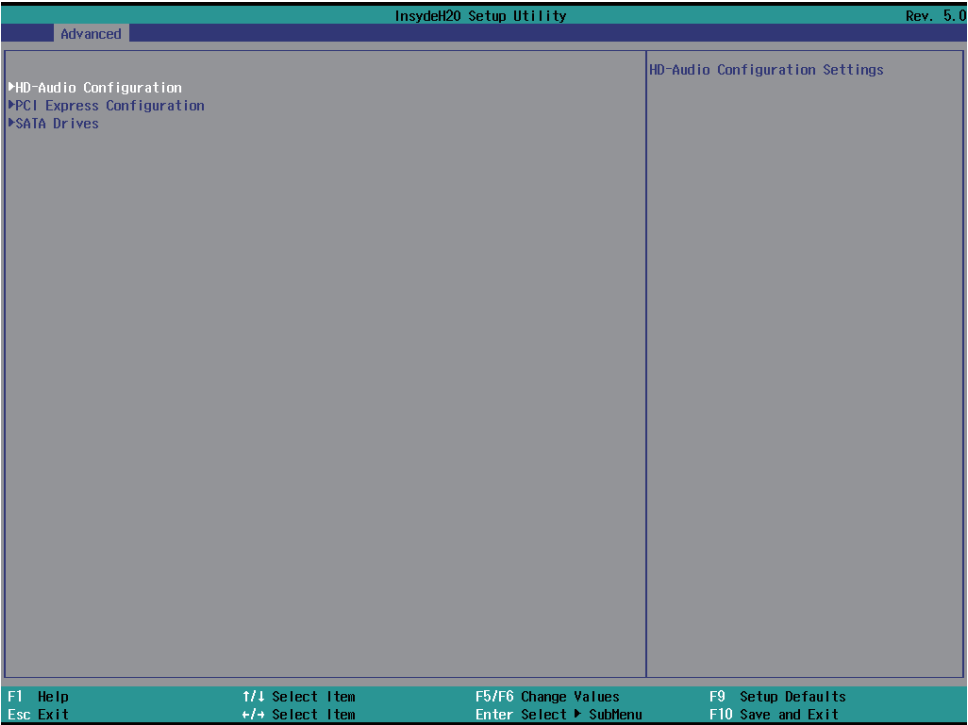
The optional settings are: 6 4 (default) / 128 / 256 / 512MB

## IGD - DVMT Total Gfx Mem

Use this item to select DVMT 5.0 total graphics memory size used by the internal graphics device

The optional settings are:128M, 256M (default), MAX

## 4-5-2 South Cluster Configuration



## 4-5-2-1 ► HD-Audio Configuration

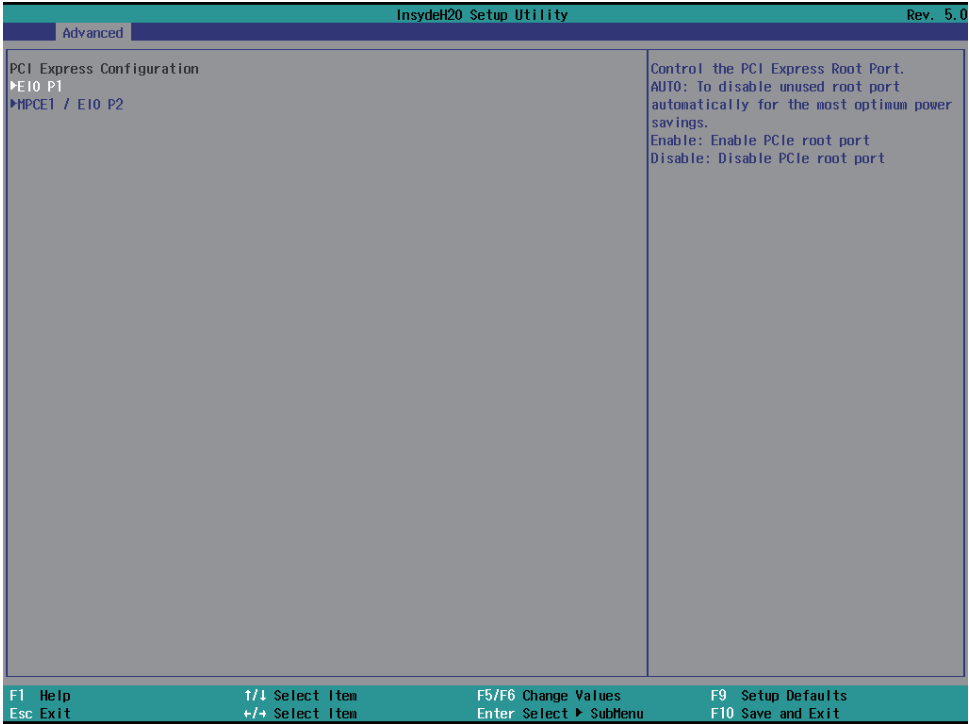
InsydeH20 Setup Utility		Rev. 5.0
Advanced		
HD-Audio Configuration HD-Audio Support <Enabled>		Enable/Disable HD-Audio Support
F1 Help Esc Exit	↑/↓ Select Item ←/→ Select Item	F5/F6 Change Values Enter Select ► Submenu F9 Setup Defaults F10 Save and Exit

### HD-Audio Support.

The optional settings are: Enabled (default), Disabled.



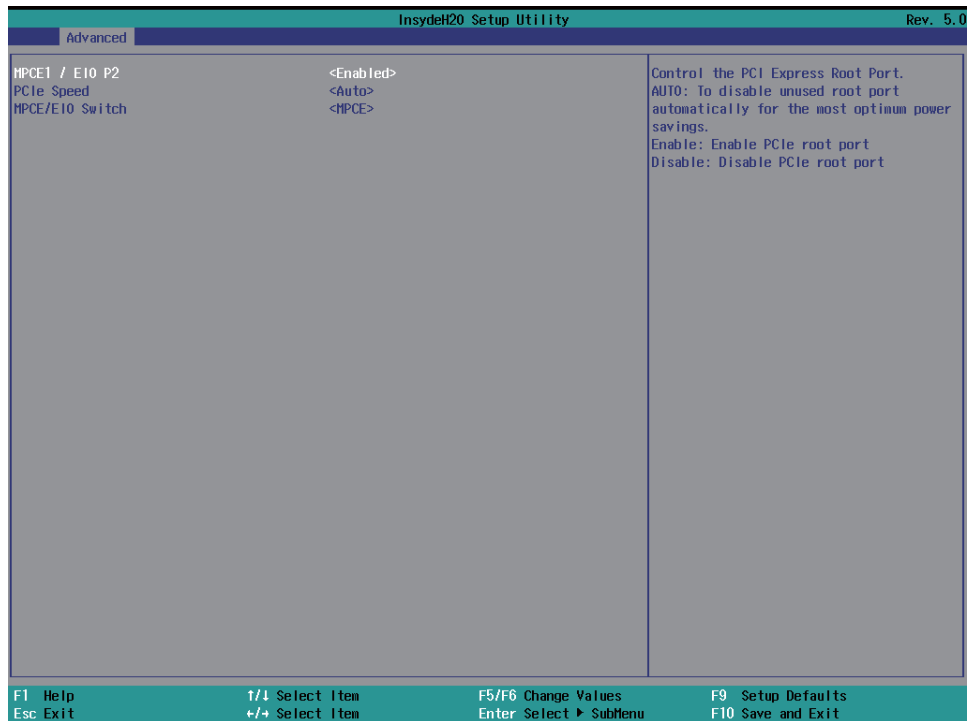
## 4-5-2-2 ► PCI Express Configuration



### EIO P1

The optional settings are: Enabled (default), Disabled.

# 4-5-2-2-1 ▶ MPC1 / EIO P2



## MPCE1 / EIO P2

The optional settings are: Disabled, Enabled (default) for MPCE1.

## PCIe speed

This item that setting PCIe speed: Auto (default), Gen1, Gen2

## MPCE / EIO Switch

This item to switch the PCIe signal to MPCE1 (default) or EIO

### 4-5-2-3 ► SATA Drives Configuration

InsydeH20 Setup Utility		Rev. 5.0
Advanced		
Chipset-SATA Controller Configuration		
Chipset SATA	<Enabled>	Enables or Disables the Chipset SATA Controller. The Chipset SATA controller supports the 2 black internal SATA ports (up to 6Gb/s supported per port).
SATA Mode Selection	<AHCI>	
SATA Interface Speed	<Gen3>	
SATA Port 0	[Not Installed]	
SATA Port 1	[Not Installed]	
F1 Help	↑/↓ Select Item	F5/F6 Change Values
Esc Exit	←/→ Select Item	Enter Select ► SubMenu
		F9 Setup Defaults
		F10 Save and Exit

**Chipset SATA**

Use this item to Enable or Disable SATA function.  
The optional settings are: Enabled (default) or Disabled

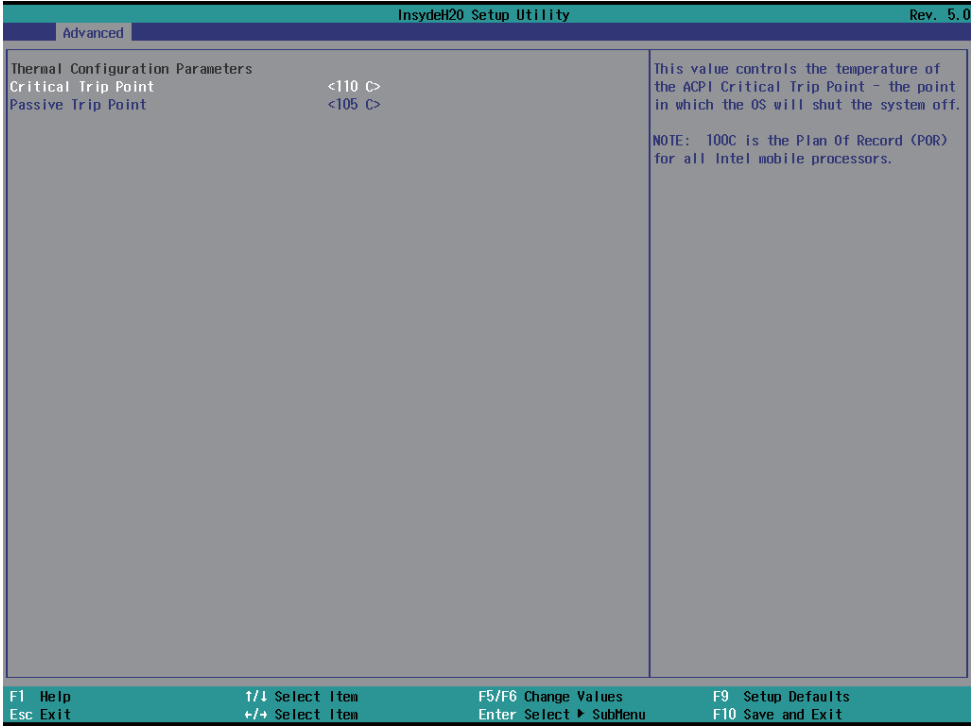
**SATA Mode Select**

This does not selected item, it will always work in ACHI mode.

**SATA Interface Speed**

This item will determine the SATA speed.  
The optional settings are: Gen1, Gen2, Gen3 (default)

### 4-5-3 Thermal



Thermal Configuration Parameters

This Value controls the temperature of the ACPI Critical Trip Point, the point in which the OS will shutdown the system.

Critical Trip point is the shutdown temperature, the default value is 110°

The CPU frequency will auto reduce when CPU temperature arrived to passive Trip point.

The default of the passive trip point is 105°

## 4-5-4 SIO FINTEK81801U configuration

InsydeH20 Setup Utility		Rev. 5.0
Advanced		
Serial Port A	<Enable>	Configure Serial port using options : [Disable] No Configuration [Enable] User Configuration
Base I/O Address	<3F8>	
Interrupt	<IRQ4>	
Serial Mode	<RS232>	
Serial Port B	<Enable>	
Base I/O Address	<2F8>	
Interrupt	<IRQ3>	
Serial Mode	<RS232>	
Power Fail	<Keep State>	
►Hardware Monitor		

F1 Help	T/↓ Select Item	F5/F6 Change Values	F9 Setup Defaults
Esc Exit	+/- Select Item	Enter Select ► SubMenu	F10 Save and Exit

### Serial Port A/B

Use this item to enable or disable serial port (COM1 or COM2).  
The optional settings are: Enabled (default), Disabled.

### Serial Port A/B Base IO Address / Interrupt

Use this item to select an optimal setting for super IO device.  
These optional settings are:

IO=3F8h; IRQ=3,4

IO=3E8h; IRQ=3,4

IO=2E8h; IRQ=3,4

IO=2F8h; IRQ=3,4

The port A default value as IO=3F8h; IRQ=4

The port B default value as IO=2F8h; IRQ=3

**Serial Mode**

RS232 driver (default) : When hardware select to RS232 or RS422 mode, please enter to RS232 driver.  
RS485 driver : When hardware select to RS485 mode, please enter to RS485 driver. It is the auto flow function for RS485.

**Power Failure**

This item specifies whether your system will reboot after a power failure or interrupt occurs.  
[Keep state] Restores the system to the status before power failure or interrupt occurred. (default)  
[Always on] Leaves the computer in the power on state.  
[Always off] Leaves the computer in the power off state.

**Hardware Monitor**

These are system voltage and temperature message.  
The voltage shows the 3 type: VCC3, VCORE, VNN  
The Temperature is sense from system not CPU.

InsydeH2O Setup Utility

Rev. 5.0

Advanced

Hardware Monitor

Voltage

VCC33.392 V

VCORE0.720 V

VNN0.920 V

Temperature

System (°C/°F)40.0°C/ 104.0°F

F1 Help

t/↓ Select Item

F5/F6 Change Values

F9 Setup Defaults

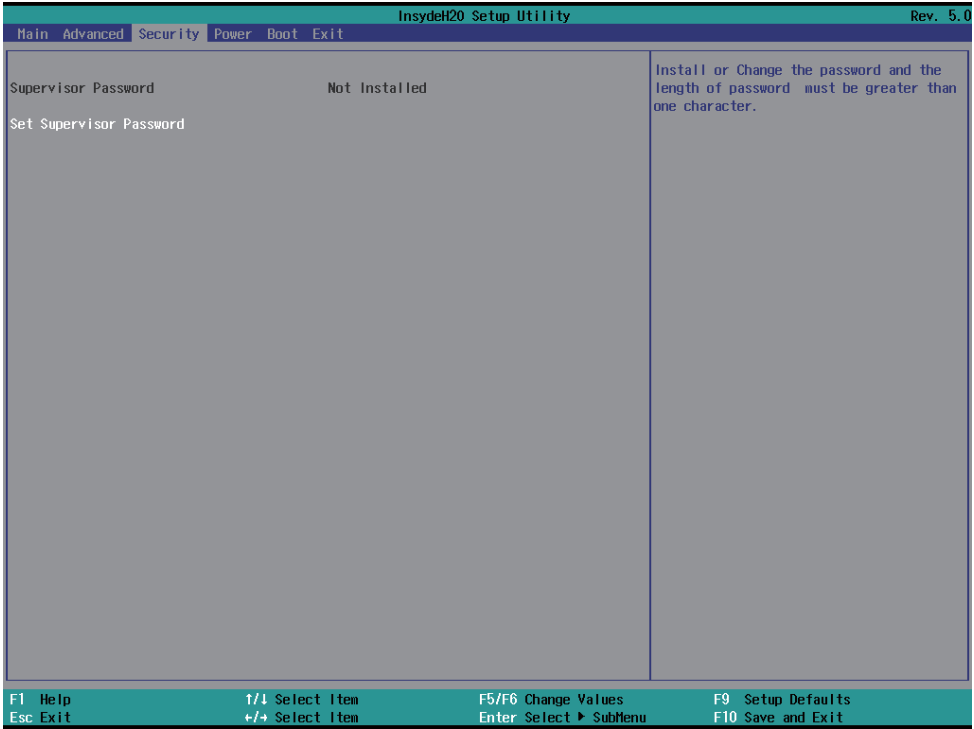
Esc Exit

+/- Select Item

Enter Select ▶ SubMenu

F10 Save and Exit

## 4-6 Security



### Supervisor Password

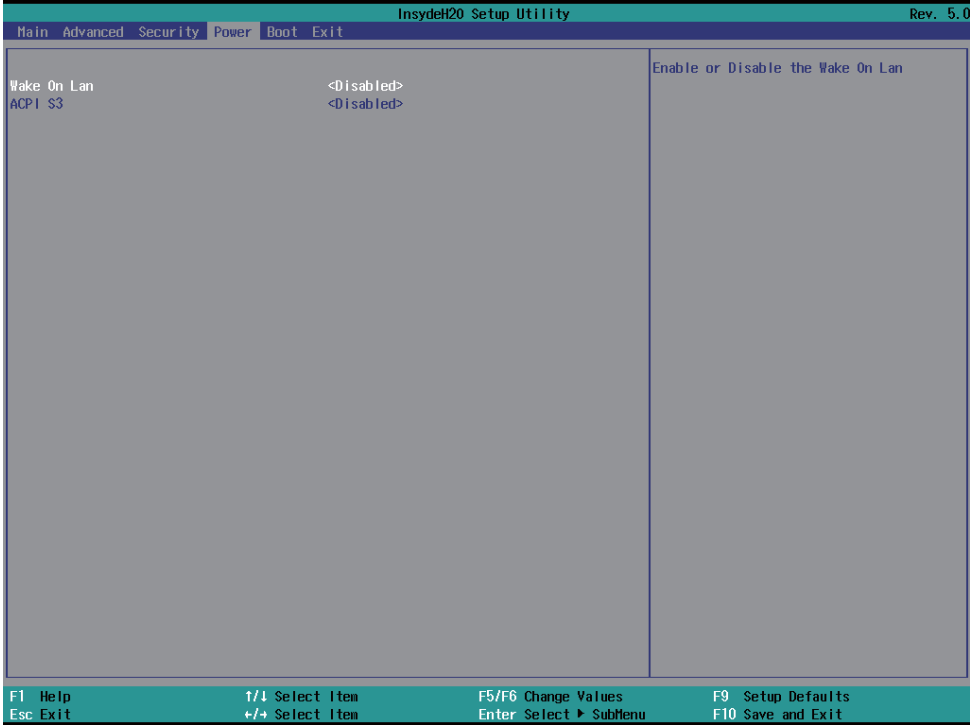
To set up an Supervisor password

1. Select Supervisor Password.

The screen then pops up an Create New Password dialog.

2. Enter your desired password that is no less than 3 characters and no more than 10 characters.
3. Hit [Enter] key to submit.

# 4-7 Power



## Wake on LAN

Wake On LAN from LAN1 when system in S3 or S5 state or both.

The optional settings: S3, S5, S3 / S5, Disabled (default)

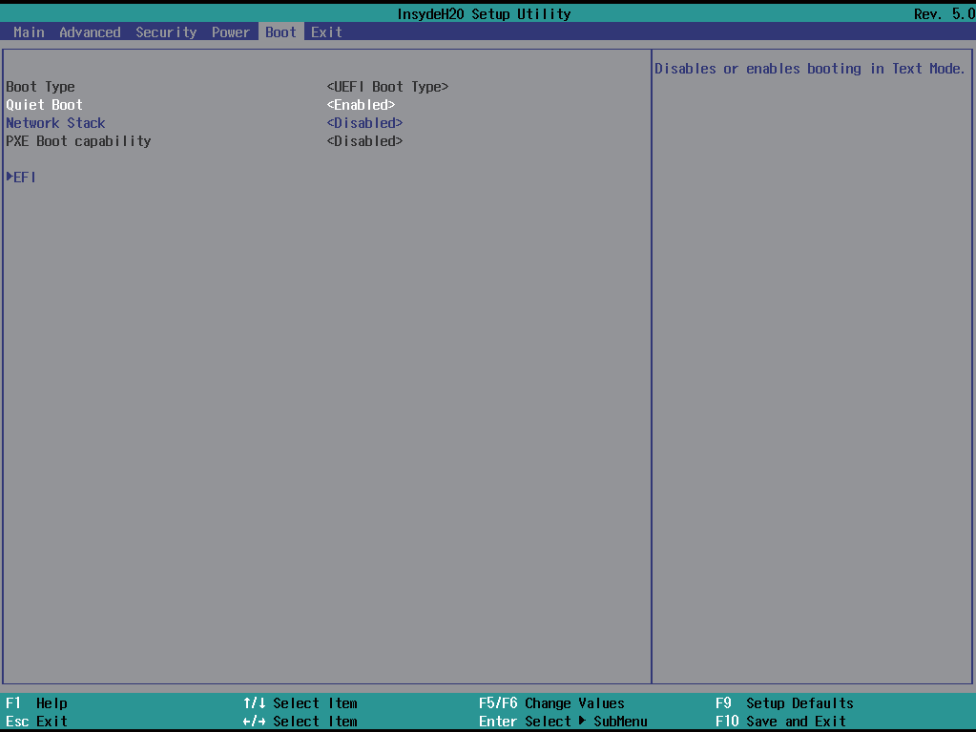
## ACPI S3

Select ACPI sleep state (S3) support.

The optional settings: Enabled or Disabled (default)



# 4-8 Boot



## Boot type

The boot type only work in UEFI Boot mode.

## Quiet Boot

The optional settings are: Enabled (default), Disabled.

## Network Stack

If use PXE function need enable otherwise disable (default)

## PXE Boot capability

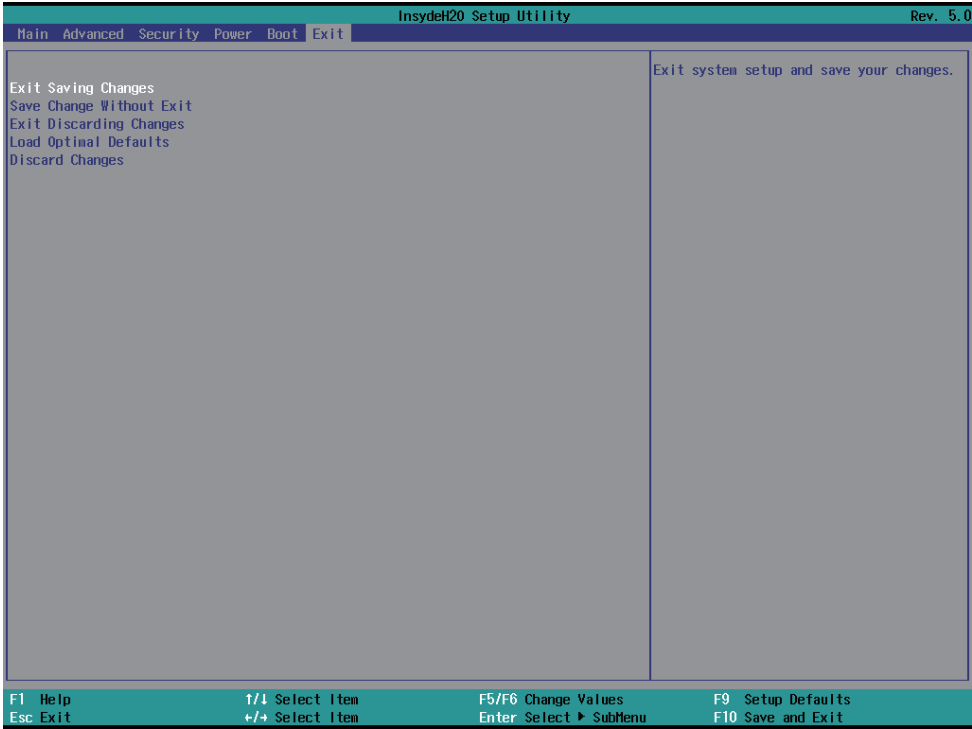
The item determines what protocol operation in PXE, these values are: Disabled (default), UEFI: IPv4, UEFI: IPv6

This is not modifying item. When above item settings disable.

## EFI

Determine which EFI storage device too booting. If storage no EFI system that will not shows on the page.

# 4-9 EXIT



## Exit Saving Changes

This item allows user to reset the system after saving the changes.

## Save Change Without Exit

This item allows user to saving the changes but doesn't restart.

## Exit Discard Changes

This item allows user restart the system but no saving the changes

## Load Optimal Default

Use this item to restore the optimal default for all the setup options.

## Discard Changes

Use this item to cancel all the setup options.

---

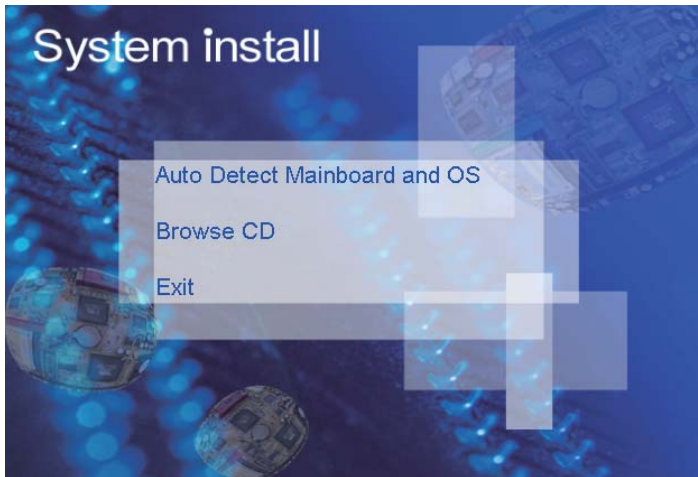
# Chapter-5

## DRIVER INSTALLATION

There is a system installation DVD in the package. This DVD does not only include all the drivers you need but also some other free application programs and utility programs. In addition, this DVD also includes an auto detect software telling you which hardware is installed and which driver is needed so that your system can function properly. We call this auto detect software SYSTEM INSTALL.

### **SYSTEM INSTALL Supports Windows 10(32bit/64bit) / Windows 8/8.1(32bit/64bit) / Windows 7(32bit/64bit)**

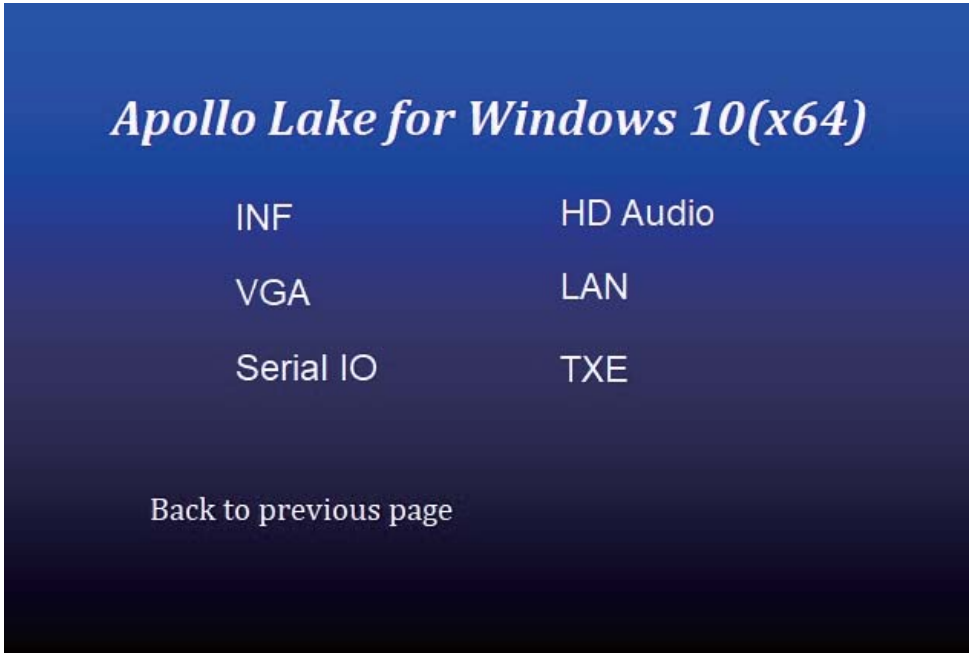
Insert the DVD into your DVD-ROM drive and the SYSTEM INSTALL menu should appear as below. If the menu does not appear, double-click MY COMPUTER and double-click DVD-ROM drive or click START, click RUN, and type X:\SETUP.EXE (assuming your DVD-ROM drive is X).



### **Make your selection from SYSTEM INSTALL menu:**

1. Auto Detect Main board and OS to AUTOMATIC DRIVER INSTALLATION menu
2. Browse DVD to view the contents of the DVD
3. Exit to exit SYSTEM INSTALL menu

**AUTOMATIC DRIVER INSTALLATION menu**



- |              |                                       |
|--------------|---------------------------------------|
| 1. INF       | Install Intel Baytrail chipset driver |
| 2. VGA       | Install onboard VGA driver            |
| 3. Serial IO | Install Serial IO driver              |
| 4. HD Audio  | Install HD Audio Codec driver         |
| 5. LAN       | To the LAN driver Readme file         |
| 6. TXE       | Install Intel TXE driver              |

Each selection is illustrated below:

## 5-1 INF Install Intel Apollo Lake Chipset Driver



1. At the "AUTOMATIC DRIVER INSTALLATION menu"screen, click "INF".

2. At the "Intel® Chipset Device Software"screen, click "Next".



3. At the "License Agreement" screen, click "Accept".

4. At the "Readme File Information" screen, click "Install".



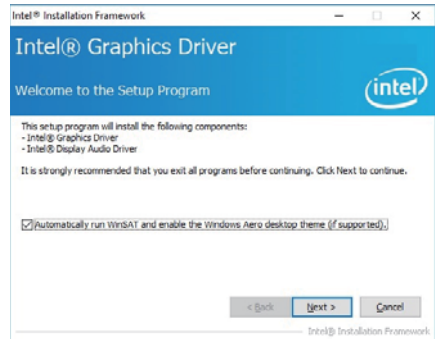
NOTE: SYSTEM INSTALL will auto detect file path X:\driver\apollo\_lake\Chipset\_10.1.1.33\_Public\SetupChipset.exe

5. Click "Finish" to finish the setup

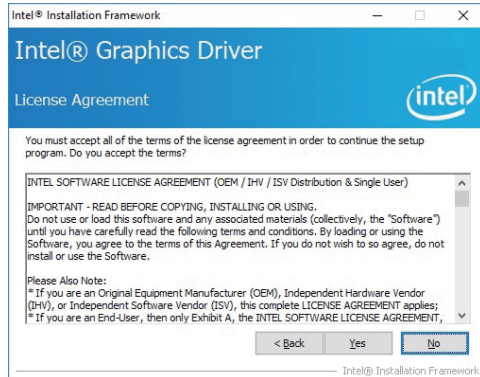
## 5-2 VGA Install Intel Apollo Lake VGA Driver



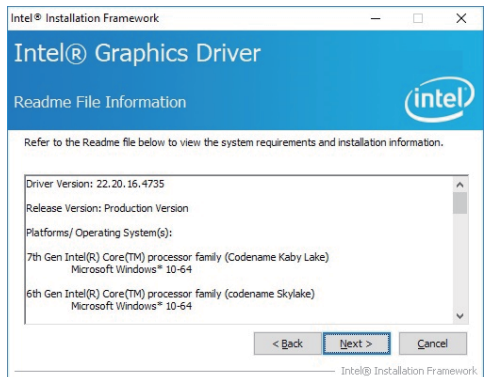
1. At the "AUTOMATIC DRIVER INSTALLATION menu" screen, click "VGA".



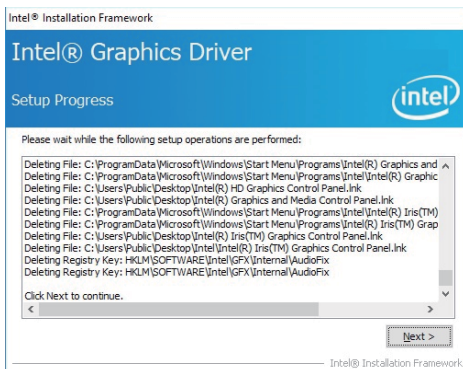
2. At the "Welcome to the Setup Program screen, Click "Next".



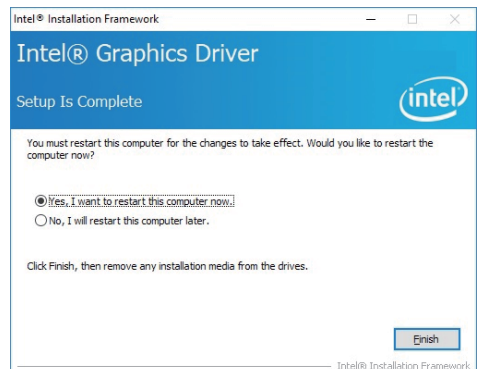
3. At the "License Agreement" screen, Click "Yes"



4. At the "Readme File Information" screen, Click "Next"



5. At the "Setup Progress" screen, Click "Next".



6. Click "Finish" to restart computer

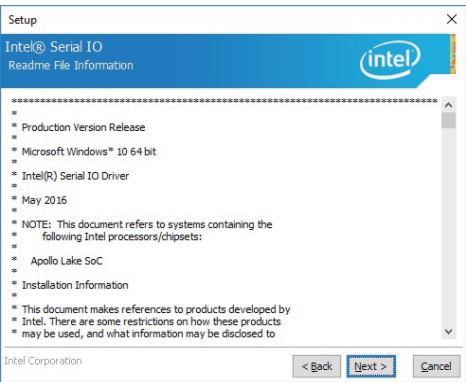
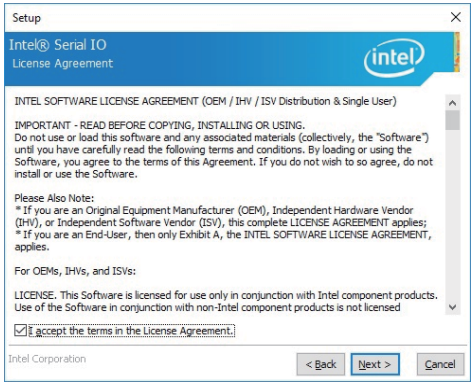
NOTE: SYSTEM INSTALL will auto detect file path  
X:\driver\apollo\_lake\gfx-15.45\Setup.exe

### 5-3 Serial IO Install Driver Apollo Lake Serial IO Driver



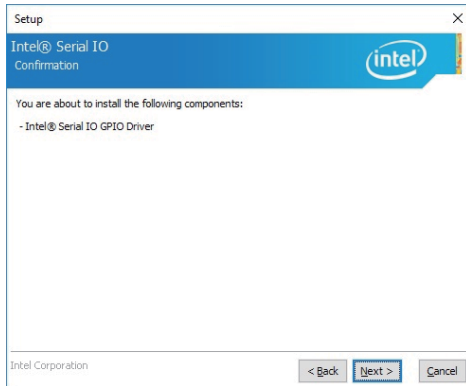
1. At the "AUTOMATIC DRIVER INSTALLATION menu" screen, click "Serial IO".

2. At the "Welcome to the Setup Program screen, Click "Next"

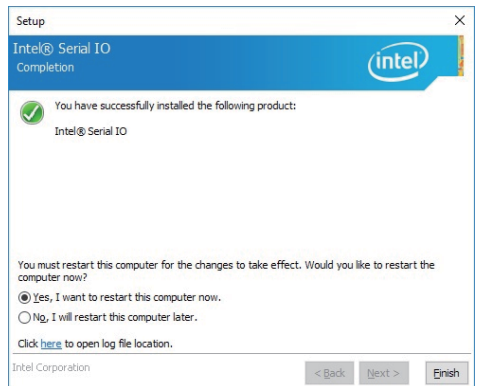


3. At the "License Agreement" screen, Click "Next"

4. At the "Setup type" screen, select "complete" and Click "Next".



5. At the "Ready to install the program" screen Click "Next"



6. Click "Finish" and restart computer.

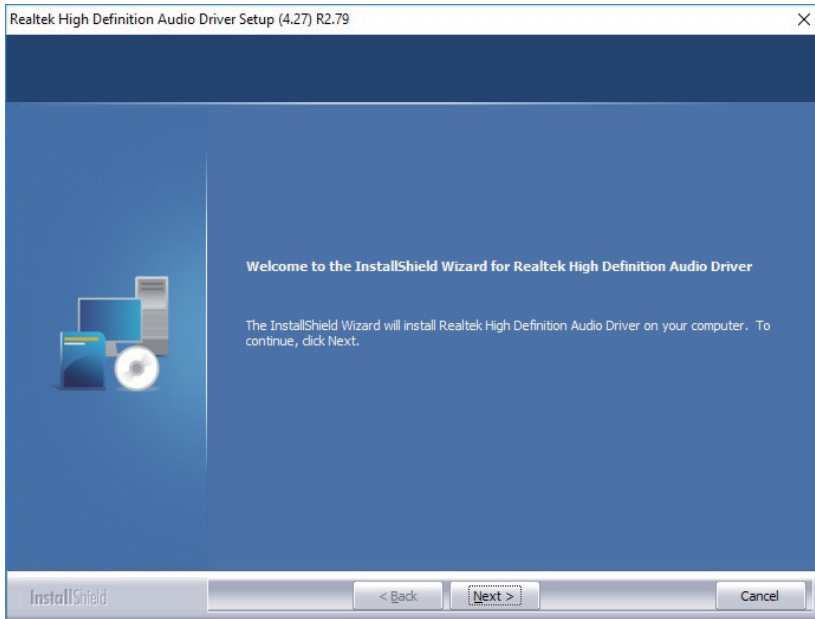
NOTE: SYSTEM INSTALL will auto detect file path  
X:\driver\apollo\_lake\SerialIO\SetupSerialIO.exe

## 5-4 HD Audio Install High Definition Audio Driver

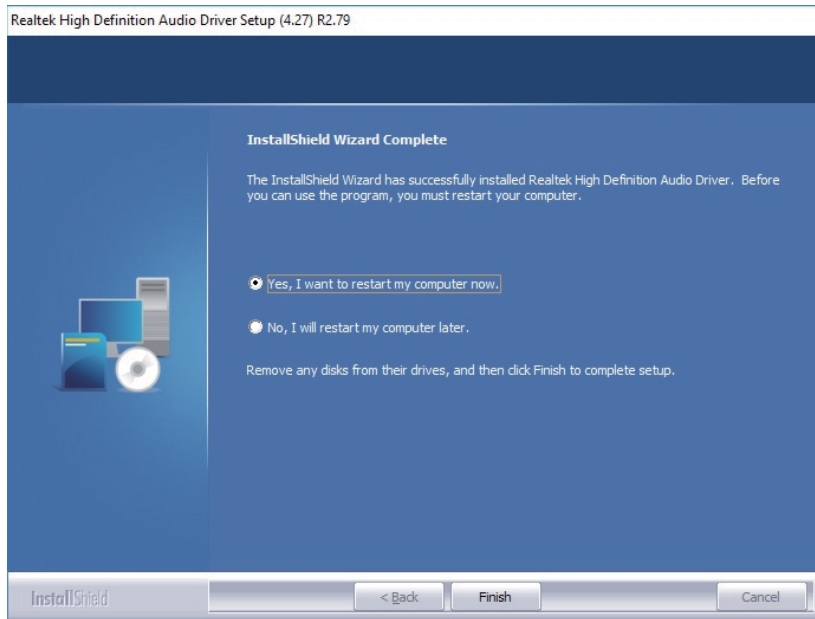


1. At the "AUTOMATIC DRIVER INSTALLATION menu", click "HD Audio"





2. Click "Next".



3. Click "Finish" to restart computer

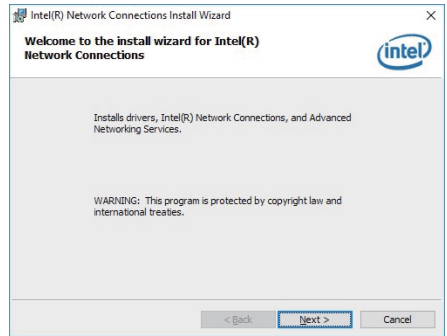
NOTE: SYSTEM INSTALL will auto detect file path

X:\driver\apollo\_lake\Audio\0006-64bit\_Win7\_Win8\_Win81\_Win10\_R279.exe

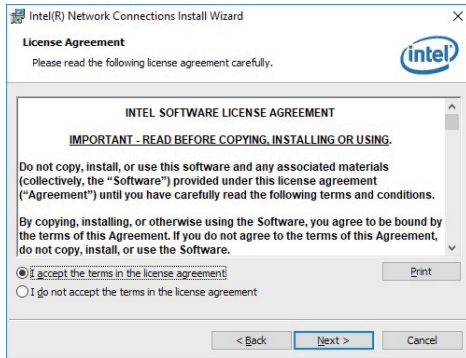
# 5-5 LAN Install Intel LAN Driver



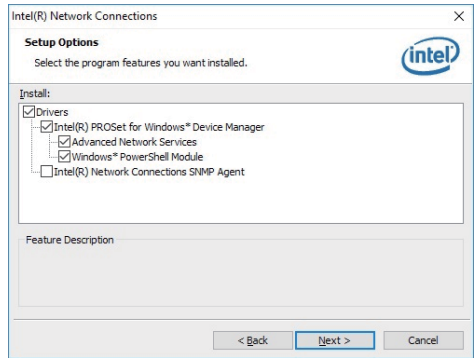
1. At the "AUTOMATIC DRIVER INSTALLATION" menu, click "LAN"



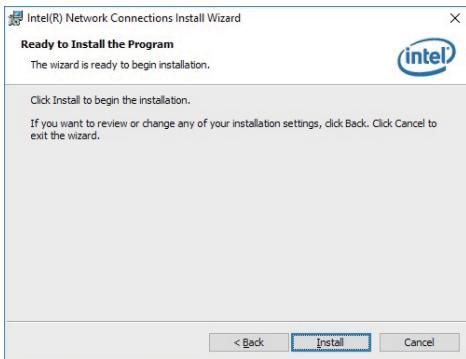
2. At the "Welcome to the Setup Program screen, Click "Next".



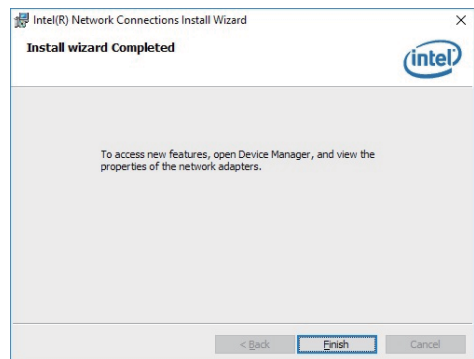
3. At the "License Agreement" screen, Click "Next".



4. At the "Setup Options" screen, Click "Next".



5. Click "Install" to install the Program



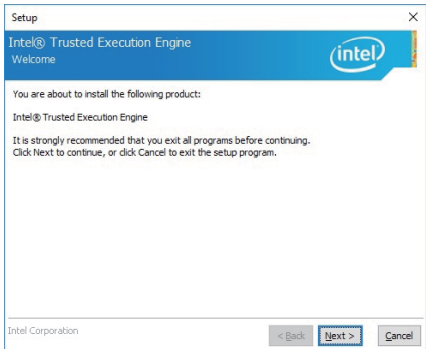
6. Click "Finish" and restart computer

NOTE: SYSTEM INSTALL will auto detect file path  
For Windows 8/8.1 32 / 64-bit,  
X:\driver\apollo\_lake\LAN\IN8.1 64bit\ PROWinx64.zip

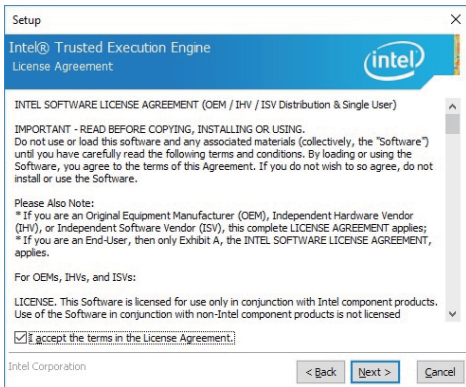
## 5-6 TXE Install Intel TXE driver



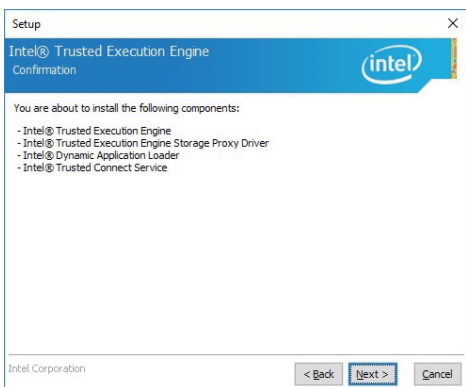
1. At the "AUTOMATIC DRIVER INSTALLATION menu", click "TXE"



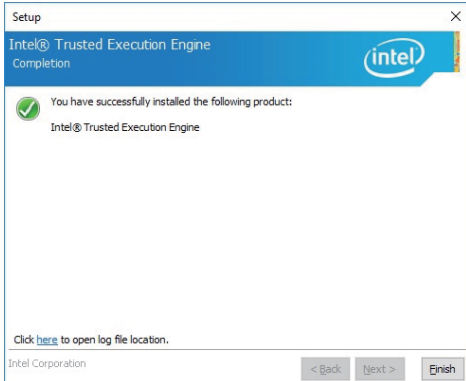
2. At the "Welcome" screen, Click "Next".



3. At the "License Agreement" screen, Click "Next".



4. Click "Next".



5. Click "Finish" & restart computer

NOTE: SYSTEM INSTALL will auto detect file path  
X:\driver\apollo\_lake\TXE\SetupTXE.exe

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## 5-7 How to update Insyde BIOS

Under DOS Mode

STEP 1. Prepare a bootable disc.

(Storage device could be USB FDD or USB pen drive.)

STEP 2. Copy utility program to your bootable disc. You may download it from our website.

STEP 3. Copy the latest BIOS for your LEX motherboard from our website to your bootable disc.

STEP 4. (Here take 2I392CW as an example, please enter your motherboard's name)

Insert your bootable disc into X: (X could be C:, A: or others.

It depends on which type of storage device you use. )

Start the computer and type

X:\: H2OFFT-D.EXE 2I392CWA2.ROM -BIOS -ALL

2I392CWA2.ROM is the file name of the latest BIOS.

It may be 2I392CWWA1.ROM or 2I392CWWA2.ROM, etc.

Please leave one space between .ROM & -BIOS -ALL

By Bay Trail series mainboard, please type

X:\: H2OFFT-D.EXE 2I392CWWA2.ROM -BIOS -ALL

-BIOS : Flash BIOS region

-ALL : Flash all

STEP 5. Press ENTER and the BIOS will be updated,  
Computer will restart automatically.

# Appendix A: Power Consumption Test

## Condition ( 2I392CW )

Item	Spec
CPU	Pentium N4200 1.10GHz / Celeron N3350 1.10GHz
Memory	DDR3L 1600 8GB
Operating System	Windows 10 Pro 64bit
Test Program	3D Mark 06
HDD 2.5" SATA	500GB
mSATA	32GB

## Test Result for reference only !

Storage	Processor	Power off	Start up		Operation Maximum	Shut down Maximum	In Put Voltage
			Maximum	Stable			
2.5" HDD	Pentium N4200	0.20A	1.79A	0.59A	1.63A	1.27A	12V
		0.15A	0.62A	0.36A	0.76A	0.68A	24V
	Celeron N3350	0.20A	1.04A	0.58A	1.43A	0.88A	12V
		0.15A	0.57A	0.34A	0.76A	0.49A	24V
mSATA	Pentium N4200	0.20A	1.08A	0.67A	1.41A	0.96A	12V
		0.15A	0.59A	0.39A	0.75A	0.52A	24V
	Celeron N3350	0.20A	0.94A	0.51A	1.38A	0.75A	12V
		0.15A	0.54A	0.31A	0.70A	0.42A	24V

The power consumption depends on your device choice!

## Appendix B: Resolution list

640 x 480 x ( 256 / 16bit / 32bit )
800 x 600 x ( 256 / 16bit / 32bit )
1024 x 768 x ( 256 / 16bit / 32bit )
1152 x 864 x ( 256 / 16bit / 32bit )
1280 x 600 x ( 256 / 16bit / 32bit )
1280 x 720 x ( 256 / 16bit / 32bit )
1280 x 768 x ( 256 / 16bit / 32bit )
1280 x 800 x ( 256 / 16bit / 32bit )
1280 x 960 x ( 256 / 16bit / 32bit )
1280 x 1024 x ( 256 / 16bit / 32bit )
1400 x 1050 x ( 256 / 16bit / 32bit )
1440 x 900 x ( 256 / 16bit / 32bit )
1600 x 900 x ( 256 / 16bit / 32bit )
1600 x 1200 x ( 256 / 16bit / 32bit )
1680 x 1050 x ( 256 / 16bit / 32bit )
1920 x 1080 x ( 256 / 16bit / 32bit )
1920 x 1200 x ( 256 / 16bit / 32bit )