

LCD TFT Datasheet

Rev.1.4 2016-10-17

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally white	/
Size	5.0	Inch
Viewing Direction	12:00 (without image inversion)	O' Clock
Gray Scale Inversion Direction	6:00	O' Clock
LCM (W × H × D)	136.00×92.80× 9.20	mm ³
Active Area (W × H)	108.00 × 64.80	mm ²
Dot Pitch (W × H)	0.045×0.135	mm ²
Number Of Dots	800 (RGB) × 480	/
Driver IC	FT813	/
Backlight Type	12 LEDs	/
Surface Luminance	510	cd/m ²
Interface Type	SPI/QSPI	/
Color Depth	16.7M	/
Pixel Arrangement	RGB Vertical Stripe	/
Surface Treatment	Clear	/
Input Voltage	3.3	V
With/Without TSP	Projected Capacitive Touch Panel	/
Weight	106.45	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.



LCD TFT Datasheet Rev.1.4 RVT50U0FNWC0x



REVISION RECORD

REVNO.	REVDATE	CONTENTS	REMARKS
1.0	2015-11-19	Initial Release	
1.2	2016-02-15	Update LED Backlight Current and Input Voltage for LED Inverter, LCD Timing Characteristics information	
1.3	2016-06-06	Update Interface description (pin 4 and pin 5)	
1.4	2016-10-17	Added Inspection Standards	

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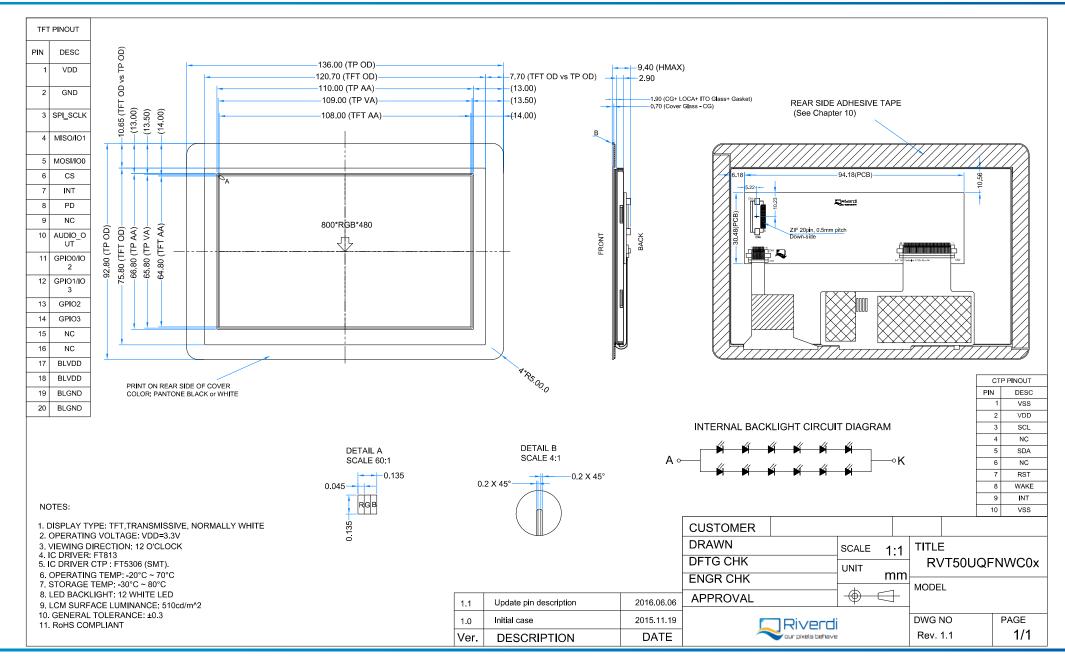
1 MODULE CLASSIFICATION INFORMATION

RV	Т	50	П	П	F	N	W		Øx
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

1.	BRAND	RV – Riverdi
2	PRODUCT TYPE	T – TFT Standard
2.	TRODUCTTILE	F – TFT Custom
		35 – 3.5"
2	DISPLAY SIZE	43 – 4.3"
3.	DISPLAT SIZE	50 – 5.0"
		70 – 7.0"
4.	MODEL SERIAL NO.	U (A-Z)
5.	RESOLUTION	Q- 800х480 рх
	INTERFACE	T – TFT LCD, RGB
6		L – TFT LCD, LVDS
6.		S – TFT + Controller SSD1963
		F – TFT+ Controller FT813
7	FRAME	N – No Frame
7.	110 1101	F – Mounting Frame
8.	BACKLIGHT TYPE	W – LED White
		N – No Touch Panel
9.	TOUCH PANEL	R – Resistive Touch Panel
		C – Capacitive Touch Panel
10.	VERSION	0x (00-99)

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3 ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage For Logic	VDD	-0.3	4.0	V
Input Voltage For Logic	VIN	VSS-0.5	VDD+0.3	V
LED forward current (each LED)	IF	-	60	mA
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C
Humidity	RH	-	90% (Max 60°C)	RH

4 ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTES
Supply Voltage For Module	VDD	3.0	3.3	3.6	V	
Input Leakage Current	I _{LKG}	-	-	-	μΑ	
Input Voltage for LED Inverter	BLVDD	2.8	3.3	5.5	V	
LED Backlight Current	IDDbacklight	-	290	363	mA	BLVDD=3.3V
LED Backlight Current	IDDbacklight	-	180	225	mA	BLVDD=5V
Input Voltage ' H ' level	V _{IH}	0.8VDD	-	VDD	V	
Input Voltage ' L ' level	V _{IL}	-0.3	-	0.2VDD	V	

5 BACKLIGHT CHARACTERISTICS

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Voltage for LED backlight	Vı	17.4	18.3	19.6	V
Current for LED backlight	l _I	30	40	50	mA
Power consumption	W _{BL}	522	732	980	mW
LED Life Time	-	30000	50000	-	Hrs

Note: The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C.

6 ELECTRO-OPTICAL CHARACTERISTICS

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK	NOTE
Response Time		Tr+Tf		-	20	-	ms	Figure 1	4
Contrast Ratio)	Cr	θ=0°	-	500	-		Figure 2	1
Luminance Ur	niformity	δ WHITE	Ø=0° Ta=25	75	80	-	%	Figure 2	3
Surface Lumin	nance	Lv		467	510	-	cd/m ²	Figure 2	2
			Ø = 90°	40	50	-	deg	Figure 3	
		θ	Ø = 270°	60	70	-	deg	Figure 3	
Viewing Angle	Viewing Angle Range		Ø = 0°	60	70	-	deg	Figure 3	6
VICWING ANGIC			Ø = 180°	60	70	-	deg	Figure 3	U
	Dod	х		0.540	0.590	0.640			
	Red	У		0.300	0.350	0.400			
	Croon	х		0.298	0.348	0.398			
	Green	У	θ=0°	0.520	0.570	0.620		:: 2	
CIE (x, y)	Dive	х	Ø=0° Ta=25	0.095	0.145	0.195		igure 2	5
Chromaticity	Blue	У	14-25	0.060	0.110	0.160			
	\A/b:+=	х		0.270	0.320	0.370			
	White	У		0.310	0.360	0.410			





Note 1. Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1.

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \text{ WHITE } = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Figure 3.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Note 8. For TFT module, Gray scale reverse occurs in the direction of panel viewing angle.

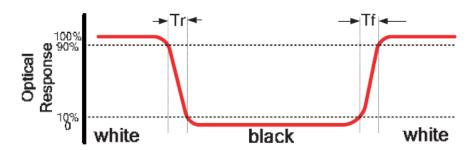


Figure 1. The definition of response time

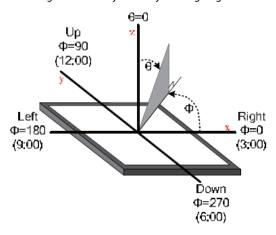


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A: 5 mm
B: 5 mm
H,V: Active Area
Light spot size Ø=5mm, 500mm distance from the
LCD surface to detector lens
measurement instrument is TOPCON's luminance
meter BM-5

P1
P2
P5

Figure 3. The definition of viewing angle



7 INTERFACE DESCRIPTION

PIN NO.	SYMBOL	DESCRIPTION
1	VDD	Power Supply
2	GND	Ground
3	SPI_SCLK	SPI SCK Signal, Internally 47k Pull UP
4	MISO/ IO1	SPI MISO Signal / SPI Quad mode: SPI data line 1
5	MOSI/ 100	SPI MOSI Signal / SPI Quad mode: SPI data line 0
6	CS	SPI Chip Select Signal , Internally 47k Pull UP
7	INT	Interrupt Signal, Active Low, Internally 47k Pull UP
8	PD	Power Down Signal, Active Low, Internally 47k Pull UP
9	NC	Not Connected
10	AUDIO_OUT	Audio Out Signal
11	GPIO0/IO2	SPI Single mode: General purpose IOO/ SPI Quad mode: SPI data line 2
12	GPIO1/IO3	SPI Single mode: General purpose IO1/ SPI Quad mode: SPI data line 3
13	GPIO2	General purpose IO2
14	GPIO3	General purpose IO3 or analog input for ADC
15	NC	Not Connected
16	NC	Not Connected
17	BLVDD	Backlight Power Supply, Can Be Connected to VDD
18	BLVDD	Backlight Power Supply, Can Be Connected to VDD
19	BLGND	Backlight Ground, Internally connected to GND
20	BLGND	Backlight Ground, Internally connected to GND



8 FT813 CONTROLLER SPECIFICATIONS

FT813 or EVE (Embedded Video Engine) simplifies the system architecture for advanced human machine interfaces (HMIs) by providing functionality for display, audio, and touch as well as an object oriented architecture approach that extends from display creation to the rendering of the graphics.

8.1 Serial host interface

Figure 4.SPI interface connection

 SCLK
 SCLK

 MISO
 MISO/IO0

 MOSI
 → MOSI/IO1

 CS
 CS

Figure 5. QSPI interface connection

SCL	< ≻	SPI_SCLK
IO0	\leftrightarrow	MISO/IO0
IO1	\leftrightarrow	MOSI/IO1
IO2	\leftrightarrow	IO2
IO 3	\leftrightarrow	IO3
cs	>	cs

SPI Interface – the SPI slave interface operates up to 30MHz.

Only SPI mode 0 is supported. The SPI interface is selected by default (MODE pin is internally pulled low by 47k resistor).

QSPI Interface – the QSPI slave interface operates up to 30MHz. Only SPI mode 0 is supported. The QSPI can be configured as a SPI slave in SINGLE, DUAL or QUAD data bus modes.

By default the SPI slave operates in the SINGLE channel mode with MOSI as input from the master and MISO as output to the master. DUAL and QUAD channel modes can be configured through the SPI slave itself. To change the channel modes, write to register REG_SPI_WIDTH.



8.2 **Block Diagram**

Figure 6. FT813 Block diagram MEMORY AND GRAPHIC CONTROL PROCESSING R7...R0 REGISTERS PCLK RAM PIXEL CLOCK GENERATOR ROM

TOUCH CONTROL

CAPACITIVE/

RESISTIVE

TOUCH

CTP_RST_N CTP_INT_N CTP_SCL

CTP_SDA

X+,X-,Y+,Y-

VCCIO1 MCU INTERFACE VCCI02 SCK MISO/100 MOSI/IO1 GPI00/I02 B7...B0 GPI01/I03 CS_N INTERRUPT INT_N GENERATOR VSYNC DE DISP BACKLIGHT AUDIO CONTROL **EXTRAS** GRAPHICS X1/CLK OSC/PLL AUDIO_L Х2 GPI02 AUDIO GPIO3/AIN ADC **ENGINE**

Host interface SPI mode 0 8.3

LDO

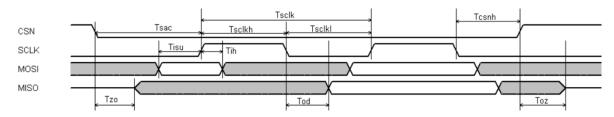
VOUT1V2

PD_N

GND

Figure 7. SPI timing diagram

TOUCH

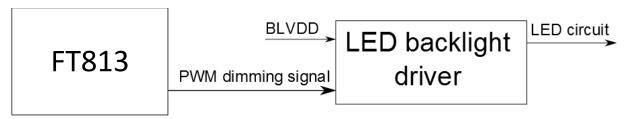


For more information about FT813 controller please go to official FT81x website http://www.ftdichip.com/Products/ICs/FT81X.html

8.4 Backlight driver block diagram

Backlight enable signal is internally connected to FT813 Backlight control pin. This pin is controlled by two FT813's registers. One of them specifies the PWM output frequency, second one specifies the duty cycle. Refer to FT813 datasheet for more information.

Figure 8. Backlight driver block diagram



(DE Mode)



HS Front Porch

9 LCD TIMING CHARACTERISTICS

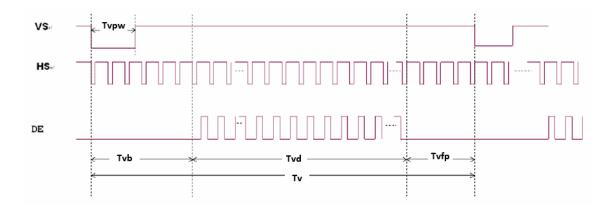
9.1 Clock and data input time diagram

HS Blaking

Figure 9. Horizontal input timing diagram

Figure 10. Vertical input timing diagram

Active Area



9.2 Parallel RGB timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Horizontal Display Area	Thd	-	800	-	DCLK
DCLK Frequency	Fclk	-	30	50	MHz
One Horizontal Line	Th	889	928	1143	DCLK
HS pulse width	Thpw	1	48	255	DCLK
HS Blanking	Thb	-	88	-	DCLK
HS Front Porch	Thfp	1	40	255	DCLK
Vertical Display Area	Tvd	-	480	-	TH
VS period time	Tv	513	525	767	TH
VS pulse width	Tvpw	3	3	255	TH
VS Blanking	Tvb	-	32	-	TH
VS Front Porch	Tvfp	1	13	255	TH



10 CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

The Capacitive Touch Panel is directly connected to FT813 module. Therefore communication with Capacitive Touch Panel is simplified to read registers of FT813.

FT813 Capacitive touch panel

Figure 11.Capacitive Touch Panel Connection

10.1 Mechanical characteristics

DESCRIPTION	INL SPECIFICATION	REMARK
Touch Panel Size	5.0 inch	
Outline Dimension (OD)	136.0mm x 92.8mm	Cover Lens Outline
Product Thickness	1.9mm	
Glass Thickness	0.7mm	
Ink View Area	109.00mm x 65.80mm	
Sensor Active Area	110.0mm x 66.8mm	
Input Method	5 Finger	
Activation Force	Touch	
Surface Hardness	≥7H	

10.2 Electrical characteristics

DESCRIPTION		SPECIFICATION
Operating Voltage		DC 2.8~3.3V
Power Consumption (IDD)	Active Mode	TBD mA
	Sleep Mode	TBD μA
Interface		I ² C
Controller		FT5306
12C address		0x38 (7 bit address)
Resolution		800*480



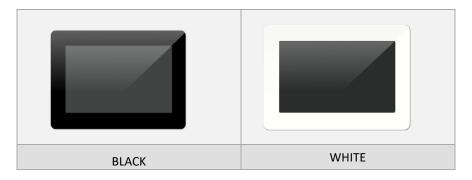
11 ORDERING INFORMATION

Three options of rear side adhesive tape are available: double side adhesive tape 0.2 mm with 3M 467MP glue, foam double side adhesive tape 0.5 mm with 3M 467MP glue or without any tape. There are also two versions of glass color: black and white.

Rear side adhesive tape options:



Cover glass color options:



Product options:

PN	DESCRIPTION
RVT50UQFNWC 00	 Double side adhesive tape with 3M 9495LE glue (total thickness 0.2mm) Cover glass color- black
RVT50UQFNWC 01	 Foam double side adhesive tape with 3M 9495LE glue (total thickness 0.5mm) Cover glass - black
RVT50UQFNWC 02	Without tapeCover glass color- black
RVT50UQFNWC 03	 Double side adhesive tape with 3M 9495LE glue (total thickness 0.2mm) Cover glass color- white
RVT50UQFNWC 04	 Foam double side adhesive tape with 3M 9495LE glue (total thickness 0.5mm) Cover glass color- white
RVT50UQFNWC 05	Without tapeCover glass color- white





12 CUSTOMIZATION LEVEL

Beside standard product (BASIC LEVEL), there are two levels of product customization available:

- 1. ADVANCED LEVEL
- 2. **PROFESSIONAL** LEVEL

Basic level - standard version of product with black or white cover glass color.

Advanced level- product with modified cover glass color, company logo or with special transparent spots for diodes.



Professional level- product with changed panel parameters including glass dimension and shapes.



For more information go to http://riverdi.com/uxtouch/.



13 INSPECTION

Standard acceptance/rejection criteria for TFT module.

13.1 Inspection condition

Ambient conditions:

Temperature: 25±°C

• Humidity: (60±10) %RH

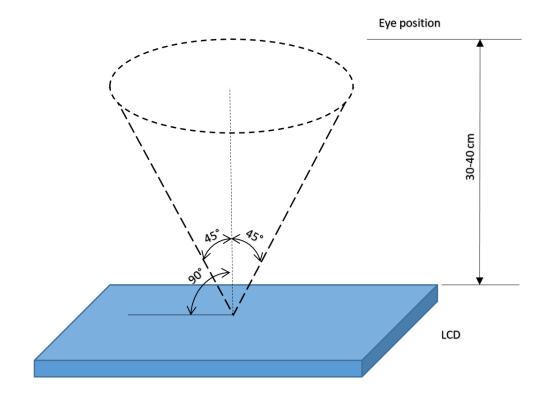
• Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance:

35±5cm between inspector bare eye and LCD.

Viewing Angle:

U/D: 45°/45°, L/R 45°/45°

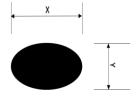




13.2 Inspection standard

Item
Black spots, white
spots, light leakage,
Foreign Particle
(round Type)





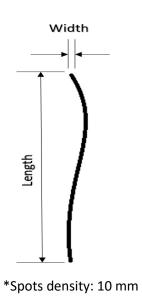
$$D = \frac{(x+y)}{2}$$

*Spots density: 10 mm

Size < 5"			
Average Diameter	Qualified Qty		
D < 0.2 mm	Ignored		
0.2 mm < D < 0.3 mm	3		
0.3 mm < D < 0.5 mm	2		
0.5 mm < D	0		

Size >= 5"			
Average Diameter	Qualified Qty		
D<0.2 mm	Ignored		
0.2 mm < D < 0.3 mm	4		
0.3 mm < D < 0.5 mm	2		
0.5 mm < D	0		

LCD black spots, white spots, light leakage (line Type)



Size < 5"			
Length	Width	Qualified	
		Qty	
-	W< 0.02	Ignored	
L < 3.0	0.02 < W < 0.05	2	
L < 2.5	0.05 < W < 0.08		
-	0.08 < W	0	

Size >= 5"			
Length	Width	Qualified	
		Qty	
-	W< 0.02	Ignored	
L < 3.0	0.02 < W < 0.05	4	
L < 2.5	0.05 < W < 0.08		
-	0.08 < W	0	





Item	Criterion		
Clear spots		Size < 5"	
	Average Diameter		Qualified Qty
	D < 0.2 mm		Ignored
	0.2 mm < D < 0.3 mm		3
	0.3 mm < D < 0.5 mm		2
	0.5 mm < D		0
			<u>'</u>
		Size >= 5"	
	Average Diameter		Qualified Qty
	D<0.2 mm		Ignored
	0.2 mm < D < 0.3 mm		4
	0.3 mm < D < 0.5 mm		2
	0.5 mm < D		0
	*Spots density: 10 mm		
Polarizer bubbles			
		Size < 5"	
	Average Diameter		Qualified Qty
	D < 0.2 mm		Ignored
	0.2 mm < D < 0.5 mm		3
	0.5 mm < D < 1 mm		2
	1 mm < D		0
	Total Q'ty		3
		ς:> - Γ"	
	Average Diameter	Size >= 5"	Qualified Oty
	Average Diameter D<0.25 mm		Qualified Qty Ignored
	0.25 mm < D < 0.5 mm		3
	0.5 mm < D		0
	0.5 11111 \ D		U
Electrical Dot			
Defect		Size < 5"	
	item		Qualified Qty
	Black do defect		4
	Bright dot defect		2
	Total Dot		5
		Size >= 5"	
	item		Qualified Qty
	Black do defect		5
	Bright dot defect		2
	Total Dot		5





Item	Criterion			
Touch panel spot				
		Size < 5" Average Diameter Qualified Qty		
	Average Diam	Average Diameter		
	D < 0.2 mm	D < 0.2 mm		
	0.2 mm < D <	0.2 mm < D < 0.4 mm		
	0.4 mm < D <	0.4 mm < D < 0.5 mm		
	0.5 mm < D		0	
		Size >= 5"		
	Average Diam	Average Diameter Qualified Qty		
	D<0.25 mm		Ignored	
	0.25 mm < D <	< 0.5 mm	4	
		0.5 mm < D		
	0.5 mm < D		0	
	0.5 mm < D		0	
	0.5 mm < D		0	
Touch panel White			0	
•		Size < 5"	0	
•		Size < 5" Width	Qualified Qty	
•	е			
•	е	Width	Qualified Qty	
•	Length	Width W< 0.02	Qualified Qty Ignored	
•	Length	Width W< 0.02 0.02 < W < 0.05	Qualified Qty Ignored	
•	Length	Width W< 0.02 0.02 < W <0.05 0.05 < W <0.08	Qualified Qty Ignored 2	
•	Length	Width W< 0.02 0.02 < W <0.05 0.05 < W <0.08	Qualified Qty Ignored 2	
•	Length	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08 0.08 < W	Qualified Qty Ignored 2	
•	Length - L < 3.0 L < 2.5 -	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08 0.08 < W Size >= 5"	Qualified Qty Ignored 2	
Touch panel White Line Scratch	Length - L < 3.0 L < 2.5 -	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08 0.08 < W Size >= 5" Width	Qualified Qty Ignored 2 0	

RVT50UQFNWC0x



14 RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION
1	High Temperature Storage	80±2°C/240hours
2	Low Temperature Storage	-30±2°C/240hours
3	High Temperature Operating	70±2°C/240hours
4	Low Temperature Operating	-20±2°C/240hours
5	Temperature Cycle	-30±2°C~25~80±2°C × 20 cycles (30min.) (5min.) (30min.)
6	Damp Proof Test	60°C ±5°C × 90%RH/240hours
7	Vibration Test	Frequency 10Hz~55Hz Amplitude of vibration: 1.5mm Sweep: 10Hz~55Hz~10Hz X, Y, Z 2 hours for each direction.
8	Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfaces
9	ESD Test	Air: ± 4 KV 150 pF/ 330Ω 5 times Contact: ± 2 KV 150 pF/ 330Ω 5 time





15 INFORMATION

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