

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		Unit
			Typ.	Max.	
Wavelength at Peak Emission I _F = 10mA	λ _{peak}	Hyper Red Green	645 574	-	nm
Dominant Wavelength I _F = 10mA	λ _{dom} [1]	Hyper Red Green	630 570	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA	Δλ	Hyper Red Green	28 20	-	nm
Capacitance	C	Hyper Red Green	35 15	-	pF
Forward Voltage I _F = 10mA (Segment) Forward Voltage I _F = 10mA (DP) Forward Voltage I _F = 10mA (Segment) Forward Voltage I _F = 10mA (DP)	V _F [2]	Hyper Red Green	3.7 1.85 4 2	4.7 2.35 4.9 2.45	V
Reverse Current (V _R = 5V) (Per chip)	I _R	Hyper Red Green	-	10 10	μA

Notes:

1. The dominant wavelength (λ_d) above is the setup value of the sorting machine. (Tolerance λ_d : ±1nm.)
2. Forward voltage: ±0.1V.
3. Wavelength value is traceable to CIE127-2007 standards.
4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

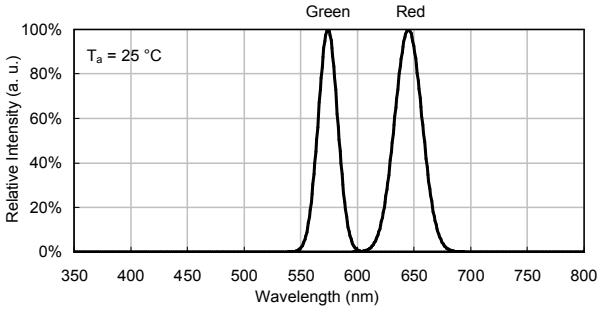
Parameter	Symbol	Value		Unit
		Hyper Red	Green	
Power Dissipation (Per chip)	P _D	75	75	mW
Reverse Voltage (Per chip)	V _R	5	5	V
Junction Temperature	T _j	115	115	°C
Operating Temperature	T _{op}	-40 to +85		°C
Storage Temperature	T _{stg}	-40 to +85		°C
DC Forward Current (Segment) DC Forward Current (DP)	I _F	30 30	30 30	mA
Peak Forward Current (Segment) Peak Forward Current (DP)	I _{FM} [1]	185 185	150 150	mA
Electrostatic Discharge Threshold (HBM)	-	3000	3000	V
Lead Solder Temperature [2]		260°C For 3-5 Seconds		

Notes:

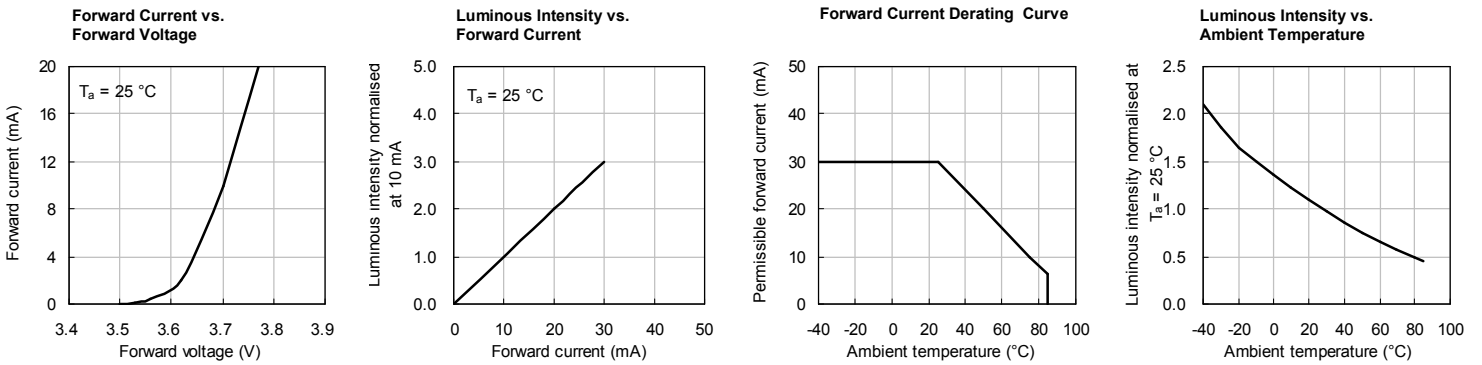
1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. 2mm below package base.
3. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

TECHNICAL DATA

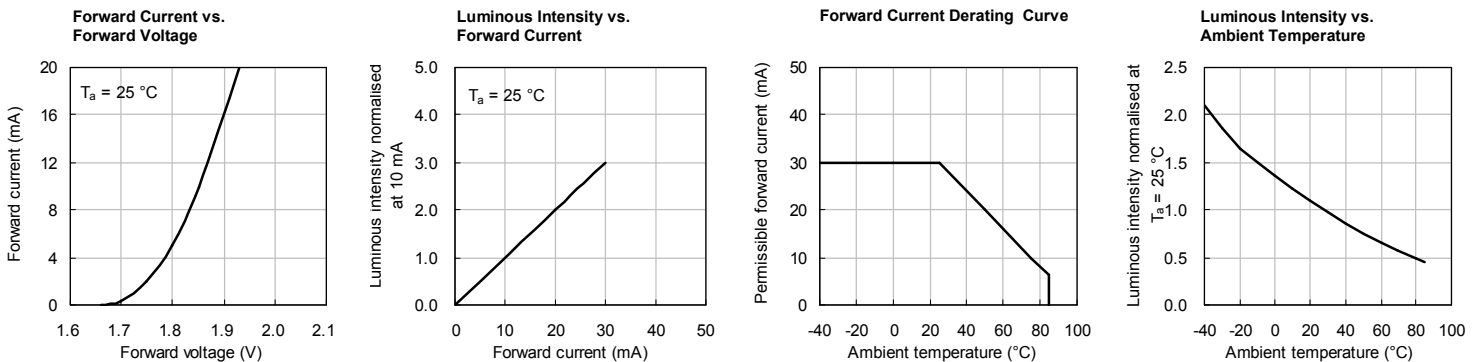
RELATIVE INTENSITY vs. WAVELENGTH



HYPER RED (SEGMENT)

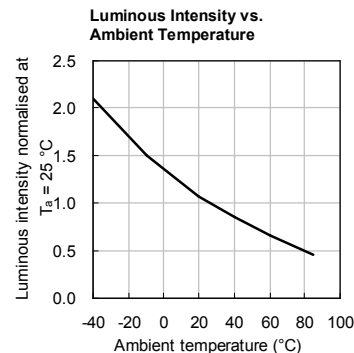
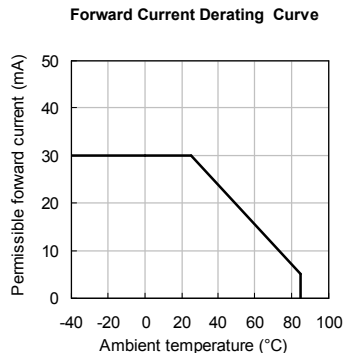
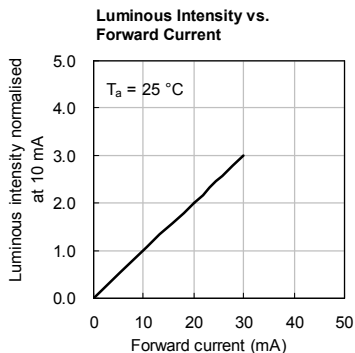
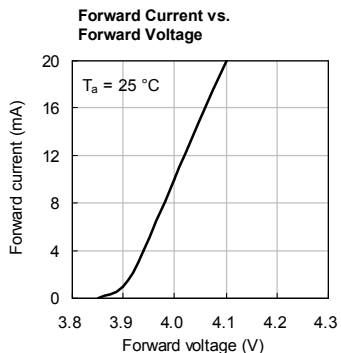


HYPER RED (DP)

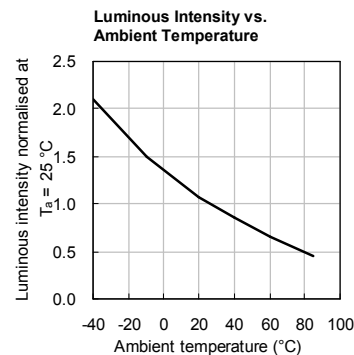
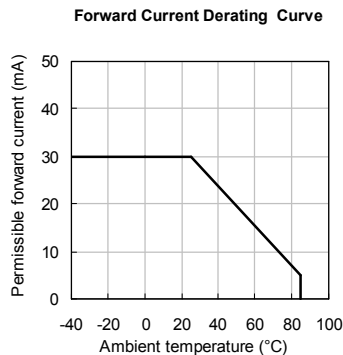
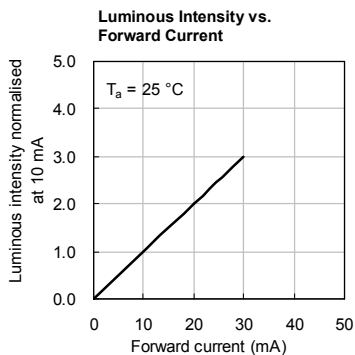
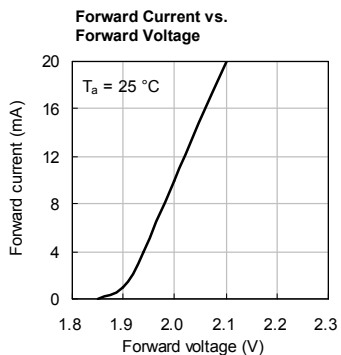


TECHNICAL DATA

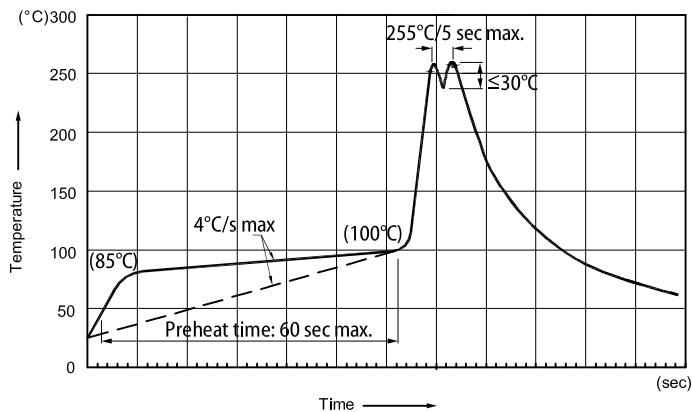
GREEN (SEGMENT)



GREEN (DP)



RECOMMENDED WAVE SOLDERING PROFILE



- Notes:
1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
 3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
 4. Fixtures should not incur stress on the component when mounting and during soldering process.
 5. SAC 305 solder alloy is recommended.
 6. No more than one wave soldering pass.
 7. During wave soldering, the PCB top-surface temperature should be kept below 105°C.

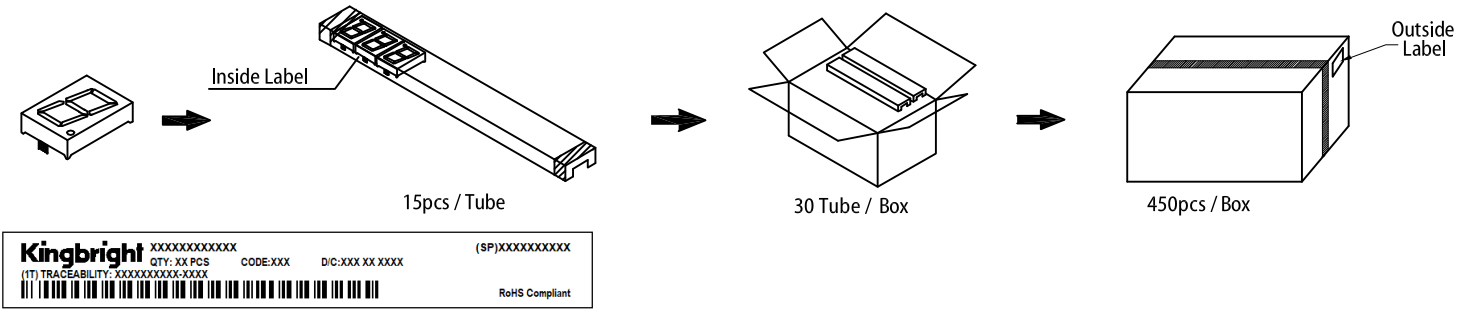
Soldering General Notes

1. Through-hole displays are incompatible with reflow soldering.
2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

CLEANING

1. Mild "no-clean" fluxes are recommended for use in soldering.
2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning because they may damage the plastic parts.
3. The cleaning process should take place at room temperature and the devices should not be washed for more than one minute.
4. When water is used in the cleaning process, immediately remove excess moisture from the component with forced-air drying afterwards.

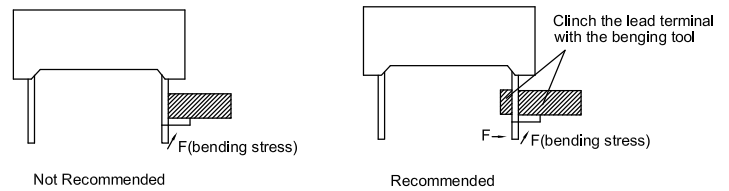
PACKING & LABEL SPECIFICATIONS



THROUGH HOLE DISPLAY MOUNTING METHOD

Lead Forming

1. Do not bend the component leads by hand without proper tools. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.



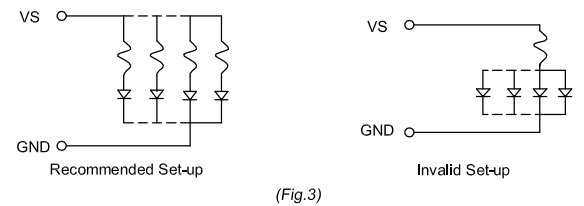
Installation

1. The installation process should not apply stress to the lead terminals.
2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals. (Fig.1)
3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.(Fig.2)



CIRCUIT DESIGN NOTES

1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor. (Fig.3)
3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.



PRECAUTIONARY NOTES

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
3. When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
4. The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening liabilities, such as automotive or medical usage, please consult with Kingbright representative for further assistance.
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6. When any special process such as potting is required for LED assembly, please consult with Kingbright representative before proceeding.
7. All design applications should refer to Kingbright application notes available at https://www.Kingbright.com/application_notes