



# 4D SYSTEMS TURNING TECHNOLOGY INTO ART

Embedded "DOS micro-Drive" Module μDRIVE-μSD-G1

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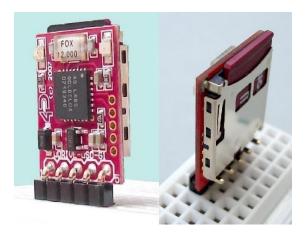
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# 1. Description

The **microDRIVE** ( $\mu$ DRIVE- $\mu$ SD-G1 or  $\mu$ DRIVE for short) is a compact high performance "Embedded Disk Drive" module that can be easily added to any micro-controller design that requires a DOS compatible file and data storage system. Most micro-controllers have small and limited on-chip memory. For those applications that require large volumes of data, the  $\mu$ DRIVE integrates the GOLDELOX processor onto a tiny 'drop-in-module'. A simple serial interface is all that is required to take away the burden of low level design that would otherwise be required for the host controller.

The  $\mu DRIVE$  module utilises off the shelf removable microSD memory cards of up to 4GB capacity and above as it's medium. A handful of straightforward commands provide direct access to the on-board memory card for storing and retrieving any size or type of data. Access to the card can be at (FAT based) file levels or at raw disk byte levels. Applications can range from data logging, program storage, music files, image and video files or any type of general purpose data storage. So next time your host micro-controller application requires file/data storage, the  $\mu DRIVE$  maybe the ideal solution.



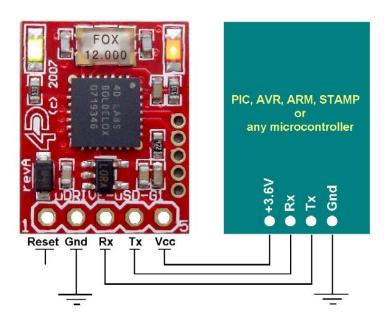
# 2. Features

- General purpose data storage device with a simple serial interface that can be added to any design in a wide range of embedded applications.
- DOS compatible file access (FAT16 format) as well as low level access to card memory.
- On board microSD memory card interface and adaptor.
- Hardware and software interface for SD, micro-SD or MMC memory cards for multimedia storage and data logging purposes.
- HC memory card support is also available for HC cards larger than 4Gb.
- Memory cards must be SPI Compatible.
- Simple serial commands provide full read-write access to the card.
- Easy 5 pin interface to any host device: VCC, TX, RX, GND, RESET.
- Serial UART interface (TTL levels) with autobaud feature from 300 to 256K baud rates.
- On-board Status LEDs:
- GREEN: Power and Memory Card detect indicator
- RED: Drive data access indicator
- 3.6V to 5.5V range operation.
- Tiny footprint: 14.9 x 18.9 x 3.5mm.
- Weight ~1.5g
- · RoHS Compliant.

# 3. Applications

- General purpose embedded data logging.
- Embedded program, data or configuration storage for processors and FPGAs.
- Audio, Video and Image file storage.
- GPS type data base storage.
- Industrial, medical, automotive, security and gaming equipment.
- Test, measurement and general purpose instrumentation.

# 4. Pin Configuration and Description



Interface/Programming Header				
Pin	Symbol	1/0	Description	
1	RESET	I	Active Low pulse greater than 2 micro-seconds will reset the module. Only use open collector type circuits to reset the device if an external reset is required. This pin is not driven low by any internal conditions. The host should control this pin via one of its port pins using an open collector/drain arrangement. Internally pulled up to 3.3 Volts.	
2	GND	Р	Supply Ground.	
3	RX	I	Asynchronous Serial Receive pin. Connect this pin to host micro-controller Serial Transmit (Tx) signal. The host transmits commands and data to the $\mu$ DRIVE via this pin. This pin is tolerant up to 5.5V levels.	
4	TX	0	Asynchronous Serial Transmit pin. Connect this pin to host micro-controller Serial Receive (Rx) signal. The host receives data from $\mu DRIVE$ via this pin. This pin is tolerant up to 5.5V levels.	
5	VCC	Р	Supply input, 3.6V to 5.5V DC.	

# 5. Host Interface

The  $\mu DRIVE$  provides both a physical and a software interface.

#### 5.1. Physical Interface - UART

The physical or hardware interface is provided by the on-board Serial UART and the host communicates to the  $\mu DRIVE$  via this serial link. The host uses this physical interface to send commands to the module for data storage and retrieval. Only the serial TX, RX and GND signals are required for a minimal configuration (see Figure 1).

Serial Data Format: 8 Bits, No Parity, 1 Stop Bit.

### 5.2. Software Interface – Command Set

The software interface provided by the  $\mu DRIVE$  module is a set of easy to use serial commands. For a complete list of  $\mu DRIVE$  commands refer to the separate document titled "microDRIVE GOLDELOX Command Set - Software Interface Specification".

#### 5.3. Auto-Baud Set-Up

The  $\mu DRIVE$  has an auto-baud feature which can automatically detect the host speed and can set its internal baud rate to operate from 300 to 256K baud.

Prior to any commands being sent to the module, it must first be initialised by sending the auto-baud character 'U' (55hex) after any power-up or reset. This will allow the module to determine and lock on to the baud rate of the host automatically without needing any further set-up. Once the  $\mu DRIVE$  has locked onto the host baud rate it will respond with an ACK byte (06hex).

Auto-Bauding must be performed each time the  $\mu DRIVE$  is powered up or reset.

#### 5.4. Command Protocol – Flow Control

The Each command is made up of a sequence of data bytes. When a command is sent to the module and the operation is completed, the  $\mu DRIVE$  will always return a response. For a command that has no specific response the module will send back a single acknowledge byte called the ACK (06hex), in the case of success, or NAK (15hex), in the case of failure.

Commands having specific responses may send back varying numbers of bytes, depending upon the command and response. It will take the module a certain amount of time to respond, depending on the command type and the operation that has to be performed. If the  $\mu DRIVE$  receives a command that it does not understand it will reply back with a negative acknowledge called the NAK (15hex). Since a command is only identified by its 'position' in the sequence of data bytes sending incorrect data can result in wildly incorrect operation.

No termination character is to be sent at the end of a command sequence, such as CR, Null, etc.

#### 5.5. Power-Up and Reset

When the  $\mu DRIVE$  module comes out of a power up or external reset, a sequence of events must be observed before attempting to communicate with the module:

- Allow up to 500ms delay after power-up or reset for the  $\mu$ DRIVE to settle. Do not attempt to communicate with the module during this period. The module may send garbage on its TX Data line during this period, the host should disable its Rx Data reception.
- Within 100ms of powering up, the host should make sure it has its Tx line pulled HIGH. If the host Tx (μDRIVE RX) is LOW or floating after the 100ms period, the module may misinterpret this

as the START bit of the auto-baud character and lock onto some undesired baud rate. If the host has a slow wake up time, i.e. less than 100ms, its Tx line maybe floating. This can be easily resolved by adding a pull up resistor on the host Tx line which will ensure the module does not encounter a false start bit. The pull up resistor can be any value within 10K to 100K.

 The host transmits the auto-baud character (capital U, 55hex) as the first command so the module can lock onto the host's baud rate.

Once the host receives the ACK the  $\mu$ DRIVE is now ready to accept Disk Drive commands from..

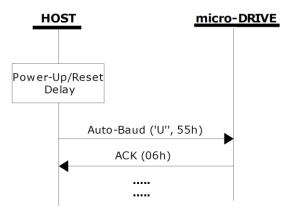


Figure 2. Reset Sequence of Events

# 6. Memory Cards – FAT16 Format

The  $\mu$ Drive uses off the shelf standard SD/microSD memory cards with up to 32GB capacity. For any FAT file related operations, before the memory

card can be used it must first be formatted with FAT16 option. The formatting of the card can be done on any PC system with a card reader. Select



the appropriate drive and choose the FAT16 (or just FAT in some systems) option when formatting.

The  $\mu$ Drive also supports high capacity HC memory cards (4GB and above). The available capacity of SD-HC cards varies according to the way the card is partitioned and the commands used to access it.

For the RAW partition, byte reads and writes can access 2^32 (i.e. 4GB) of the card, Sector reads and writes can access 2^24 sectors (of 512 bytes, i.e. 8GB). For the FAT partition, the maximum FAT16 partition can be accessed, which is 4GB (subject to your computers OS formatting capability, see below). The total amount of the card usable is the sum of the FAT and RAW partitions.

The FAT partition is always first (if it exists) and can be up to the maximum size permitted by FAT16. Windows 7 will format FAT16 up to 4GB. Windows XP will format FAT16 up to 2GB and the Windows XP command prompt will format FAT16 up to 4GB.

# 7. PmmC/Firmware Programming

The GOLDELOX processor on the  $\mu DRIVE$  module can be re-programmed with the latest PmmC configuration for updates and future proofing.

The chip-level configuration is available as a PmmC (Personality-module-micro-Code) file and the programming must be performed over the serial interface. All of the high level software interface commands are part of the PmmC configuration file so please check regularly for the latest updates and enhancements.

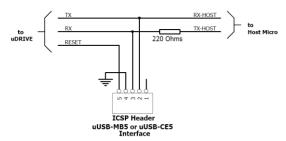


Figure 3. ICSP Interface

It is recommended that the  $\mu DRIVE$  module be socketed on the application board so that it can be easily removed for PmmC programming. If the module is to be permanently fixed and cannot be removed then access must be provided for PmmC programming on the application board. This is referred to as "In Circuit Serial Programming" (ICSP). Figure 3 provides a typical implementation for the ICSP interface.

The PmmC file is programmed into the device with the aid of Workshop 4, the 4D Systems IDE software (See Section 9). To provide a link between the PC and the ICSP interface, a specific 4D Programming Cable is required and is available from 4D Systems.

Using a non-4D programming interface could damage your display, and <u>void your Warranty.</u>

#### 8. Hardware Tools

The following hardware tools are required for full control of the  $\mu DRIVE$  module.

#### 8.1. 4D Programming Cable / Adaptor

The 4D Programming Cable and uUSB-PA5 Programming Adaptor are essential hardware tools to program, customise and test the PICASO Processor.

Either the 4D Programming Cable or the uUSB-PA5 Programming Adaptor can be used.

The 4D programming interfaces are used to program a new Firmware/PmmC and for downloading compiled 4DGL code into the processor. They even serve as an interface for communicating serial data to the PC.

The 4D Programming Cable and uUSB-PA5 Programming Adaptor are available from 4D Systems, www.4dsystems.com.au

Using a non-4D programming interface could damage your processor, and **void your Warranty**.



4D Programming Cable



uUSB-PA5 Programming Adaptor

# 9. 4D Systems - Workshop 4 IDE

Workshop 4 is a comprehensive software IDE that provides an integrated software development platform for all of the 4D family of processors and modules. The IDE combines the Editor, Compiler, Linker and Downloader to develop complete 4DGL application code, for relevant modules.

For the  $\mu$ Drive-uSD-G1 module, Workshop 4 provides the required FAT Controller toolset required to test all of the functionality of the  $\mu$ DRIVE module. Files can be removed, read and written directly to the memory card on the module. The software tool not only tests the entire high level file handling capability, it also allows the user to test the raw byte level read/write options.

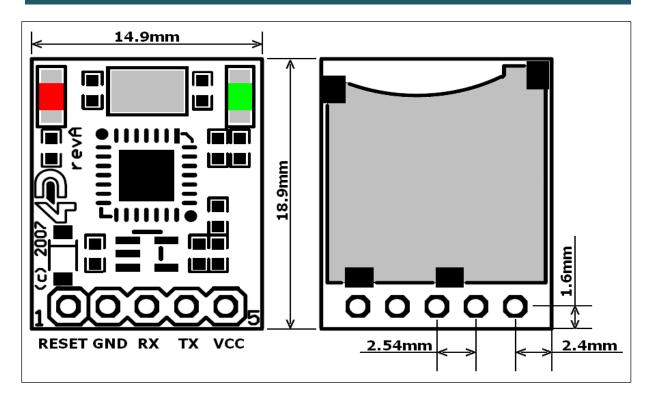
For raw read/write, the 'Protected FAT' check box needs to be unchecked. The user can also do random tests, available under the 'Test' tab.

The Workshop 4 IDE is available from the 4D Systems website. www.4dsystems.com.au

For a comprehensive manual on the Workshop 4 IDE Software, refer to its documentation from the 4D Systems website.

"Workshop-4-IDE-User-Manual.pdf"

# 10. Mechanical Details



# 11. Specifications and Ratings

ABSOLUTE MAXIMUM RATINGS			
Operating ambient temperature	40°C to +80°C		
Storage temperature	65°C +150°C		
Voltage on any digital input pin with respect to GND	0.3V to 6.0V		
Voltage on VCC with respect to GND	0.3V to 6.0V		
Maximum current out of GND pin	300mA		
Maximum current into VCC pin	250mA		
Total power dissipation	1.0W		

**NOTE**: Stresses above those listed here may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the recommended operation listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING CONDITIONS					
Parameter	Conditions	Min	Тур	Max	Units
Supply Voltage (VCC)		3.6	5.0	5.5	V
Operating Temperature		-10		+70	°C
Input Low Voltage	RX pin	GND		0.8	V
Input High Voltage	RX pin	2.0	3.3	5.0	V
Reset Pulse	External Open Collector	2.0			μs
Operational Delay	Power-Up or external reset	500			ms

GLOBAL CHARACTERISTICS BASED ON OPERATING CONDITIONS					
Parameter	Conditions	Min	Тур	Max	Units
Supply Current (ICC)	VCC = 5V, no memory card		15	26	mA
Output Low Voltage	TX pin, IOL = 3.4mA			0.4	V
Output High Voltage	TX pin, IOL = -2.0mA	2.4		3.3	V
Flash Memory Endurance	GOLDELOX PmmC programming.		1000		E/W

# **ORDERING INFORMATION**

**Order Code:** μDRIVE-μSD-G1 **Package:** 90mm x 130mm

Packaging: Module sealed in antistatic padded ZIP bag.

# 12. Legal Notice

#### **Proprietary Information**

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### 13. Contact Information

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