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PRODUCT CHANGE NOTICE (PCN)

| PCN# | WPC-160309-001 | Issuing Date | March 9 th , 2016 |
|------|----------------|--------------|------------------------------|
|------|----------------|--------------|------------------------------|

| PRODUCT(S) AFFECTED: | Attachment: ⊔Yes ■No | | |
|----------------------------------|-------------------------------|--|--|
| WIZ550WEB Rev1.1, WIZ550WEB-EVB | | | |
| Hardware Version | Supplying Schedule | | |
| 1.0 -> 1.1 | | | |
| Firmware Version 1.0.1 -> 2.0.0 | March 21 th , 2016 | | |

Description of Change from Hardware Ver. 1.0 to Ver. 1.1

Description of Change from Hardware Ver. 1.0 to Ver. 1.1 Rev 1.0 is easily interfered due to the EMI(Electro-Magnetic Interference). So, we resolved the problem and increased its efficiency by Revisioning PCB. Added damping resistors (R24,R28,R32,R33(33R)) in ethernet signals line(TXP/N, RXP/N). (for EMI problem resolved) Changed Resistor, Inductor, Capacitor size: 1608(0603) -> 1005(0402). Added EEPROM(24AAXX-I-T/OT) IC. Changed MCU Parts(STM32F103RCT6). Because we need more Memory size. So, we changed MCU Parts. STM32F103RBT6(128KB Flash Memory, 20KB SRAM), STM32F103RCT6(256KB Flash Memory, 48KB SRAM). And possible change Cortex-M4(pin to pin). If need more performance MCU. Changed Serial Flash Memory(AT45DB161E-SHF-T(16 Megabit)). Because AT45DB081D-SU(8 Megabit) model has been discontinued. Added SWD Port(J3). So, Possible JTAG test. Added BOOT SW(Slide SW) & APP BOOT PIN, BOOT SW is Access to ISP mode. APP BOOT PIN is Application jump mode SW.

Changed GPIO pinout. Refer to below description.

| Changed GPIO Pinout | Rev 1.0 | Rev 1.1 |
|---------------------|---------|---------|
| D0 | PC06 | PC00 |
| D1 | PC07 | PC01 |
| D2 | PC08 | PC02 |
| D3 | PC09 | PC03 |
| D4 | PA08 | PC04 |
| D8 | PC12 | PC05 |
| D13 | PB06 | PC06 |
| D14 | PB07 | PC07 |

| Flash memory CS pin | PB11 | PB09 |
|---------------------|------|------|
| Debug LED 0 | PC04 | PA08 |
| Debug LED 1 | PC05 | PC12 |
| I2C_EEPROM_SCL | - | PB06 |
| I2C_EEPROM_SDA | - | PB07 |
| App_Boot pin | PC12 | PC08 |

Description of Change from Firmware Ver. 1.0.1 to Ver. 2.0.0

- Added EEPROM Code
 - Added EEPROM(I2C) operation code
- Changed GPIO pin
 - Modify GPIO pinout list
 - refer to GPIO pinout list
- Changed App Boot pin Code
 - Changed application mode jump pin code (PC12 -> PC08)
- Changed Flash memory CS pin
 - Changed CS pin (PB11 -> PB09)
- Changed TFTP Code
 - All use the TFTP Flash memory size.
- Changed MCU Memory size.
 - Changed Memory Spaces Definitions(RAM, FLASH)

■ BOOT Size: 24KB -> 28KB

■ APP Size: 97KB -> 222KB

DISTRIBUTORS NOTIFICATIOON:

Main reason of this message is to inform hardware modification of WIZ550WEB. This change was inevitable resolution for a better performance and convenience. All of changes are specified right above in "Description of Changes" section. Despite the adjustment of hardware, its functions and features still remain same as previous. In needs of customers, we can supply them with condition of BTO (Bill to Order) anytime.



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