LCD / LCM SPECIFICATION

WINSTAR Display Co.,Ltd.

華凌光電股份有限公司

Winstar Display Co., LTD
華凌光電股份有限公司
WEB: https://www.winstar.com.tw E-mail: sales@winstar.com.tw

SPECIFICATION

CUSTOMER : _____

MODULE NO.: WO12864D3-YTI#

APPROVED BY: (FOR CUSTOMER USE ONLY)	PCB VERSION: DATA:

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

VERSION	DATE	REVISED PAGE NO.	SUMMARY
J	2023/01/11		Modify B/L information

Winstar Display Co.,

華凌光電股份有限公司

MODLE NO：

RECORDS OF REVISION

DOC. FIRST ISSUE

VERSION	DATE	REVISED PAGE NO.	**SUMMARY**
0	2011/11/25		First issue
A	2013/05/15		Correct Vo-Vss
B	2013/10/31		Add Pull Tape
C	2015/03/12		Modify Contour Drawing(H=9.5mm) & Response Time
D	2016/01/27		Modify Precautions in use of LCD Modules & Static electricity test
E	2016/03/10		Modify Length of FPC.
F	2016/11/21		Add FPC bending rule
G	2019/08/27		Modify Material List of Components for RoHs
H	2019/12/17		Modify Precautions in use of LCD Modules
I	2020/12/22		Add Interface
J	2023/01/11		Modify B/L information

Contents

1.Module Classification Information

W	O	12864	D3	—	Y	T	I	—	#
①	②	③	④		⑤	⑥	⑦		⑧

① Brand：WINSTAR DISPLAY CORPORATION

② Display Type：H→Character Type, G→Graphic Type , X→TAB Type, O→COG Type

③ Display Font： 128 * 64 dot

④ Model serials no.

⑤ Backlight Type：

N→Without backlight	T→LED, White	L→LED, Full color
B→EL, Blue green	A→LED, Amber	J→DIP LED,Blue
D→EL, Green	R→LED, Red	K→DIP LED,White
W→EL, White	O→LED, Orange	E→DIP LED, Yellow Green
M→EL, Yellow Green	G→LED, Green	H→DIP LED,Amber
F→CCFL, White	P→LED, Blue	I→DIP LED, Red
Y→LED, Yellow Green	X→LED, Dual color	
G→LED, Green	C→LED, Full color	

⑥ LCD Mode：

B→TN Positive, Gray	V→FSTN Negative, Blue
N→TN Negative,	T→FSTN Negative, Black
L→VA Negative	D→FSTN Negative (Double film)
H→ HTN Positive, Gray	F→FSTN Positive
I→HTN Negative, Black	K→FSC Negative
U→HTN Negative, Blue	S→FSC Positive
M→STN Negative, Blue	E→ISTN Negative, Black
G→STN Positive, Gray	C→CSTN Negative, Black
Y→STN Positive, Yellow Green	A→ASTN Negative, Black

⑦ LCD Polarize Type/ Temperature range/ View direction

A→Reflective, N.T, 6:00	H→Transflective, W.T,6:00
D→Reflective, N.T, 12:00	K→Transflective, W.T,12:00
G→Reflective, W. T, 6:00	C→Transmissive, N.T,6:00
J→Reflective, W. T, 12:00	F→Transmissive, N.T,12:00
B→Transflective, N.T,6:00	I→Transmissive, W. T, 6:00
E→Transflective, N.T.12:00	L→Transmissive, W.T,12:00

⑧ Special Code #:Fit in with the ROHS Directions and regulations

2.Precautions in use of LCD Modules

(1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.

(3)Don't disassemble the LCM.

(4)Don't operate it above the absolute maximum rating.

(5)Don't drop, bend or twist LCM.

(6)Soldering: only to the I/O terminals.

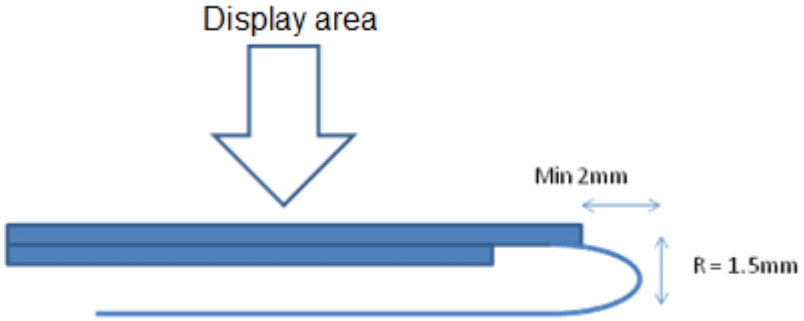
(7)Storage: please storage in anti-static electricity container and clean environment.

(8) Winstar have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors,capacitors and other passive components will have different appearance and color caused by the different supplier.)

(9)Winstar have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Winstar have the right to modify the version.)

(10) To ensure the stability of the display screen, please apply screen saver after showing 30 mins of fixed display content.

(11) The limitation of FPC bending



(12)Please heat up a little the tape sticking on the components when removing it; otherwise the components might be damaged.

3.General Specification

Item	Dimension	Unit
Number of Dots	128 x 64 dots	—
Module dimension	80.0x 54.0 x 9.5	mm
View area	70.7 x 38.8	mm
Active area	66.52 x 33.24	mm
Dot size	0.48 x0.48	mm
Dot pitch	0.52 x 0.52	mm
LCD type	FSTN Negative, Transmissive (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty	1/64 , 1/9 Bias	
View direction	6 o'clock	
Backlight Type	LED, Yellow Green	
IC	ST7565P	
Interface	6800/8080/4-Line SPI	

4.Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Power Supply Voltage	VDD	-0.3	—	3.6	V
Power supply voltage (VDD standard)	V0, VOUT	-0.3	—	14.5	V
Power supply voltage (VDD standard)	V1, V2, V3, V4	-0.3	—	V0+0.3	V

5.Electrical Characteristics

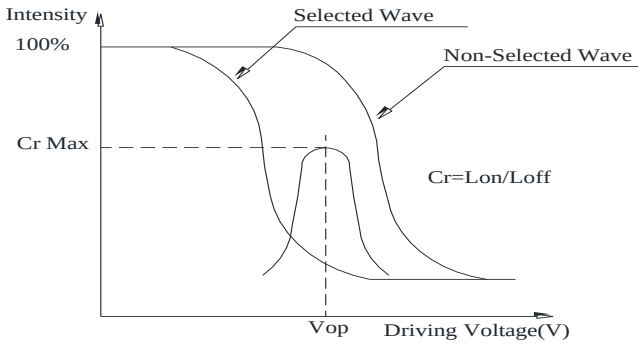
Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	V_{DD}-V_{SS}	—	2.7	—	3.3	V
Supply Voltage For LCM *NOTE	V_0-V_{SS}	Ta=-20℃ Ta=25℃ Ta=70℃	10.0 9.8 9.6	10.2 10.0 9.8	10.4 10.2 10.0	V V V
Input High Volt.	V_{IH}	—	0.8 V_{DD}	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	Vss	—	0.2 V_{DD}	V
Output High Volt.	V_{OH}	—	0.8 V_{DD}	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	Vss	—	0.2V_{DD}	V
Supply Current(No include LED Backlight)	I_{DD}	—		0.6	1	mA

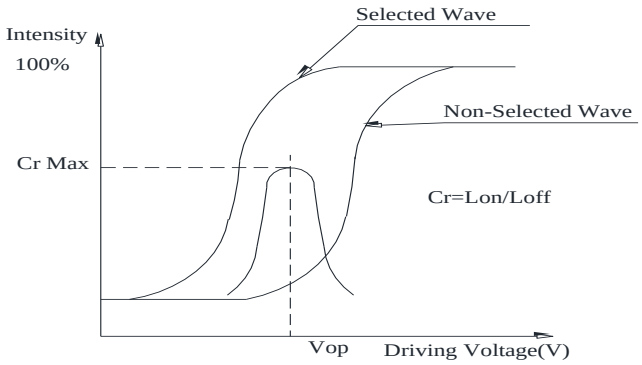
NOTE: Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance

6.Optical Characteristics

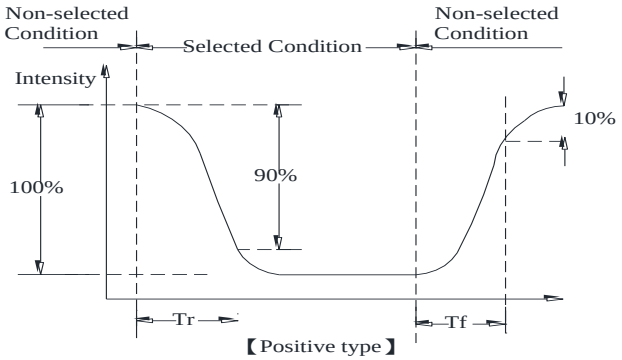
Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	θ	CR≧2	0	—	30	$\phi = 180^\circ$
	θ	CR≧2	0	—	60	$\phi = 0^\circ$
	θ	CR≧2	0	—	45	$\phi = 90^\circ$
	θ	CR≧2	0	—	45	$\phi = 270^\circ$
Contrast Ratio	CR	—	—	5	—	—
Response Time	T rise	—	—	200	300	ms
	T fall	—	—	250	350	ms

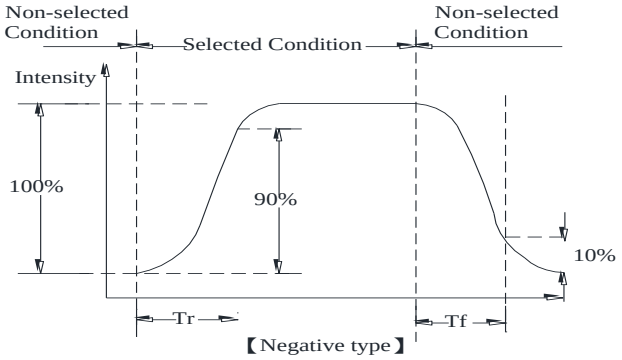
Definition of Operation Voltage (Vop)





Definition of Response Time (Tr , Tf)



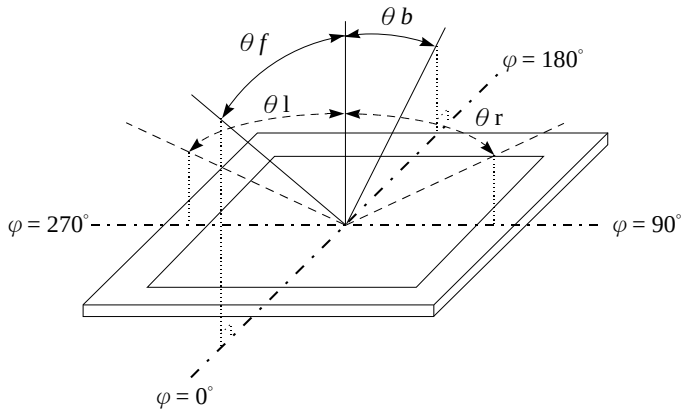


Conditions :

Operating Voltage : Vop　　Viewing Angle(θ ， φ) : 0°， 0°

Frame Frequency : 64 HZ　　Driving Waveform : 1/N duty , 1/a bias

Definition of viewing angle(CR≧2)



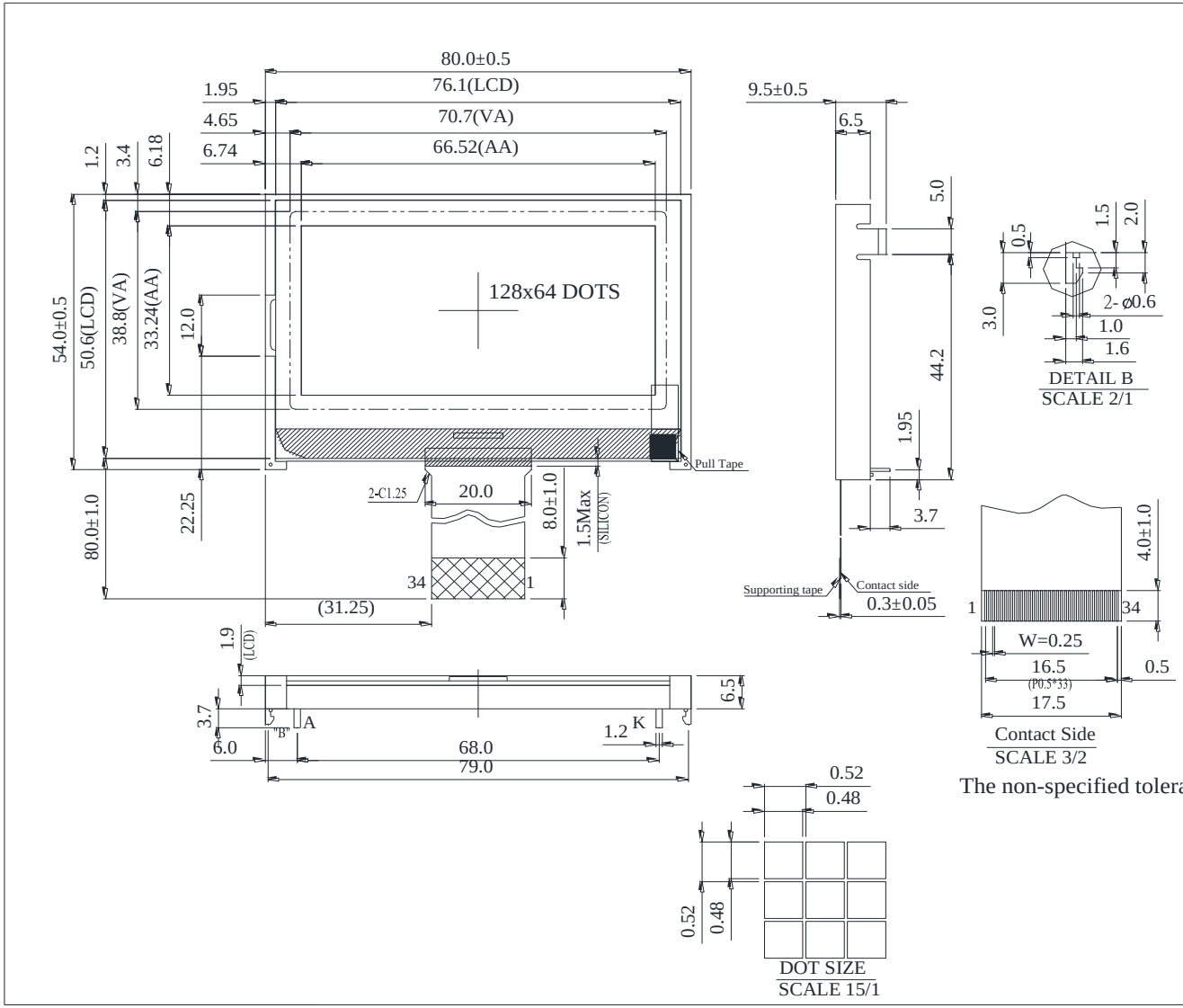
7.Interface Pin Function

Pin No.	Symbol	Level	Description
1	/CS1	I	The chip select signal
2	/RES	I	When RES is set to “L”, the setting are initialized.
3	A0	I	This is connect to the least significant bit of the normal MPU address bus, and it determines whether the data bits are data or command. A0 = “H”: Indicates that D0 to D7 are display data. A0 = “L”: Indicates that D0 to D7 are control data.
4	/WR(R/W)	I	• When connected to 8080 series MPU, this pin is treated as the “/WR” signal of the 8080 MPU and is LOW-active. The signals on the data bus are latched at the rising edge of the /WR signal. • When connected to 6800 series MPU, this pin is treated as the “R/W” signal of the 6800 MPU and decides the access type : When R/W = “H”: Read. When R/W = “L”: Write.
5	/RD(E)	I	• When connected to 8080 series MPU, this pin is treated as the “/RD” signal of the 8080 MPU and is LOW-active. The data bus is in an output status when this signal is “L”. • When connected to 6800 series MPU, this pin is treated as the “E” signal of the 6800 MPU and is HIGH-active. This is the enable clock input terminal of the 6800 Series MPU.
6~13	D0~D7	I/O	Data bus line
14	VDD	Power Supply	Power supply
15	VSS	Power Supply	Ground
16	VOUT	O	DC/DC voltage converter. Connect a capacitor between this terminal and vss or VDD
17	CAP5+	O	DC/DC voltage converter
18	CAP3+		
19	CAP1-		
20	CAP1+		
21	CAP2+		
22	CAP2-		

<table>
<tr><td>23</td><td>CAP4+</td><td></td><td></td></tr>
<tr><td>24</td><td>VRS</td><td>Power Supply</td><td>This is the internal-output VREG power supply for the LCD power supply voltage
regulator.</td></tr>
<tr><td>25</td><td>V4</td><td rowspan="5">Power Supply</td><td rowspan="5">This is a multi-level power supply for the liquid crystal drive.</td></tr>
<tr><td>26</td><td>V3</td></tr>
<tr><td>27</td><td>V2</td></tr>
<tr><td>28</td><td>V1</td></tr>
<tr><td>29</td><td>V0</td></tr>
<tr><td>30</td><td>VR</td><td>I</td><td>Output voltage regulator terminal. Provides the voltage between VSS and V0 through a resistive voltage divider.
IRS = “L” : the V0 voltage regulator internal resistors are not used.
IRS = “H” : the V0 voltage regulator internal resistors are used.</td></tr>
<tr><td>31</td><td>C86</td><td>I</td><td>This is the MPU interface selection pin.
C86 = “H”: 6800 Series MPU interface.
C86 = “L”: 8080 Series MPU interface</td></tr>
<tr><td>32</td><td>P/S</td><td>I</td><td>This is the parallel data input/serial data input switch terminal.
P/S = “H”: Parallel data input.
P/S = “L”: Serial data input.
The following applies depending on the P/S status:
<table>
<tr><th>P/S</th><th>Data/Command</th><th>Data</th><th>Read/Write</th><th>Serial Clock</th></tr>
<tr><td>“H”</td><td>A0</td><td>D0 to D7</td><td>/RD, /WR</td><td>X</td></tr>
<tr><td>“L”</td><td>A0</td><td>SI (D7)</td><td>Write only</td><td>SCL (D6)</td></tr>
</table>
When P/S = “L”, D0 to D5 fixed “H”.
/RD (E) and /WR (R/W) are fixed to either “H” or “L”.
With serial data input, It is impossible read data from RAM</td></tr>
<tr><td>33</td><td>/HPM</td><td>I</td><td>This is the power control terminal for the power supply circuit for liquid crystal drive.
/HPM = “H”: Normal mode
/HPM = “L”: High power mode</td></tr>
</table>

34	IRS	I	This terminal selects the resistors for the V0 voltage level adjustment. IRS = “H”: Use the internal resistors IRS = “L”: Do not use the internal resistors. The V0 voltage level is regulated by an external resistive voltage divider attached to the VR terminal

8.Contour Drawing

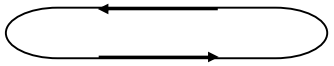


PIN NO.	SIGNAL	PIN NO.	SIGNAL
1	$\overline{CST}$	18	CAP3+
2	$\overline{RES}$	19	CAP1-
3	A0	20	CAP1+
4	$\overline{WR}$ (R/$\overline{W}$)	21	CAP2+
5	$\overline{RD}$ (E)	22	CAP2-
6	D0	23	CAP4+
7	D1	24	VRS
8	D2	25	V4
9	D3	26	V3
10	D4	27	V2
11	D5	28	V1
12	D6	29	V0
13	D7	30	VR
14	V_{DD}	31	C86
15	Vss	32	P/S
16	V_{OUT}	33	$\overline{HPM}$
17	CAP5+	34	IRS

The non-specified tolerance of dimension is ±0.3mm.

9.Reliability

Content of Reliability Test (Wide temperature, -20℃~70℃)

Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	— —
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity storage	The module should be allowed to stand at 60℃,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20℃ 25℃ 70℃  30min 5min 30min 1 cycle	-20℃/70℃ 10 cycles	— —
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	— —

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

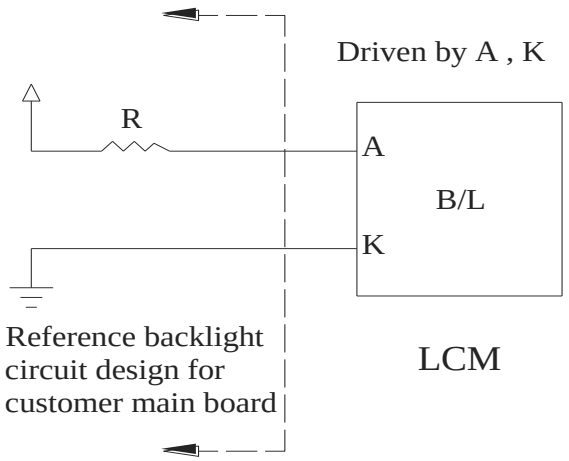
10.Backlight Information

Specification

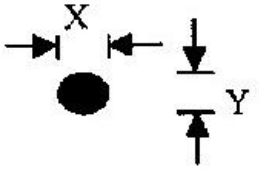
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Supply Current	ILED	—	96	120	mA	V=3.5V(Note)
Supply Voltage	V	3.4	3.5	3.6	V	—
Reverse Voltage	VR	—	—	3	V	—
Wave Length	λD	568	572	575	nm	V=3.5V
Luminance (Without LCD)	IV	44	55	—	cd/m^2	V=3.5V
Life Time	—	—	50000	—	Hr.	ILED≦96mA 25℃,50-60%RH
Color	Yellow Green					

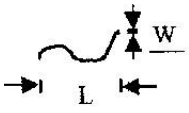
Note: A backlight driven by voltage will keep the drive current under the safe area (current between minimum and maximum).

If the B/L LED is driven by current only, the drive voltage cannot be considered as a reference value.



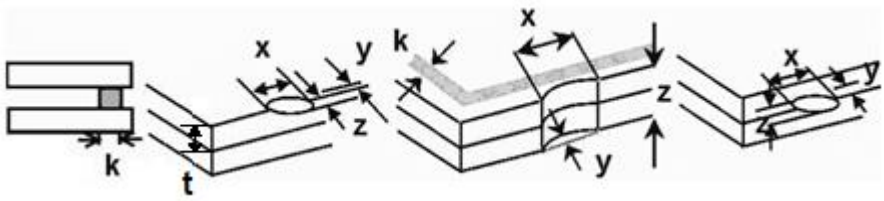
11.Inspection specification

<table>
<tr><th>No</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>01</td><td>Electrical Testing</td><td>Missing vertical, horizontal segment, segment contrast defect.
Missing character , dot or icon.
Display malfunction.
No function or no display.
Current consumption exceeds product specifications.
LCD viewing angle defect.
Mixed product types.
Contrast defect.</td><td>0.65</td></tr>
<tr><td>02</td><td>Black or white spots on LCD (display only)</td><td>2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present.
2.2 Densely spaced: No more than two spots or lines within 3mm</td><td>2.5</td></tr>
<tr><td rowspan="2">03</td><td rowspan="2">LCD black spots, white spots, contamination (non-display)</td><td>3.1 Round type : As following drawing
Φ=(x + y) / 2
 

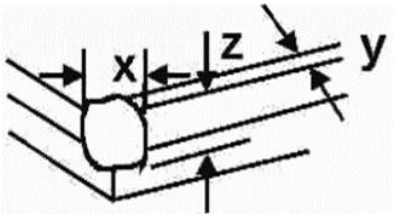
<table>
<tr><th>Size</th><th>Acceptable QTY</th></tr>
<tr><td>Φ≦0.10</td><td>Accept no dense</td></tr>
<tr><td>0.10＜Φ≦0.20</td><td>2</td></tr>
<tr><td>0.20＜Φ≦0.25</td><td>1</td></tr>
<tr><td>0.25＜Φ</td><td>0</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>3.2 Line type : (As following drawing)
 

<table>
<tr><th>Length</th><th>Width</th><th>Acceptable Q TY</th></tr>
<tr><td>---</td><td>W≦0.02</td><td>Accept no dense</td></tr>
<tr><td>L≦3.0</td><td>0.02＜W≦0.03</td><td rowspan="2">2</td></tr>
<tr><td>L≦2.5</td><td>0.03＜W≦0.05</td></tr>
<tr><td>---</td><td>0.05＜W</td><td>As round type</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>04</td><td>Polarizer bubbles</td><td>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.
<table>
<tr><th>Size Φ</th><th>Acceptable Q TY</th></tr>
<tr><td>Φ≦0.20</td><td>Accept no dense</td></tr>
<tr><td>0.20＜Φ≦0.50</td><td>3</td></tr>
<tr><td>0.50＜Φ≦1.00</td><td>2</td></tr>
<tr><td>1.00＜Φ</td><td>0</td></tr>
<tr><td>Total Q TY</td><td>3</td></tr>
</table></td><td>2.5</td></tr>
</table>

<table>
<tr><th>No</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>05</td><td>Scratches</td><td>Follow NO.3 LCD black spots, white spots, contamination</td><td></td></tr>
<tr><td>06</td><td>Chipped glass</td><td>Symbols Define:
x: Chip length y: Chip width z: Chip thickness
k: Seal width t: Glass thickness a: LCD side length
L: Electrode pad length:

6.1 General glass chip :
6.1.1 Chip on panel surface and crack between panels:
 

<table><tr><th>z: Chip thickness</th><th>y: Chip width</th><th>x: Chip length</th></tr><tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr><tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr></table>⊙If there are 2 or more chips, x is total length of each chip.

6.1.2 Corner crack:
 

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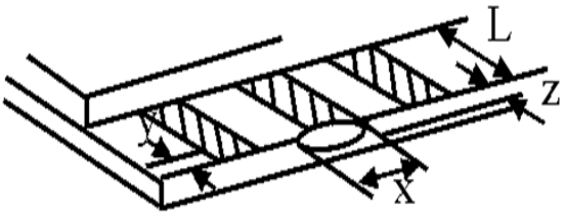
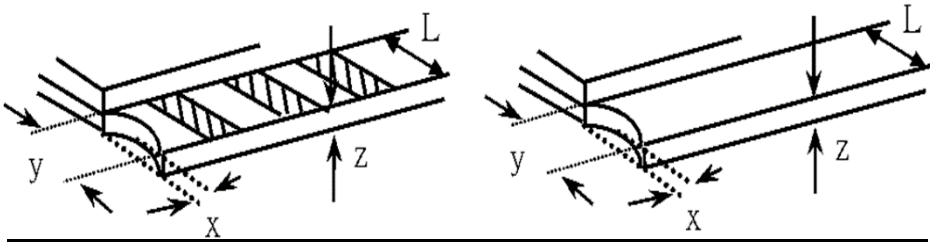
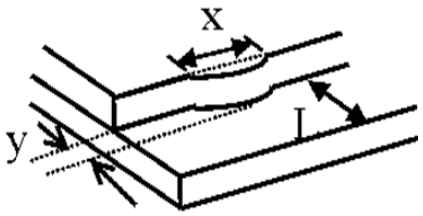
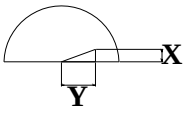
</table>

<table>
<tr><th>No</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>06</td><td>Glass crack</td><td>
Symbols :
x: Chip length y: Chip width z: Chip thickness
k: Seal width t: Glass thickness a: LCD side length
L: Electrode pad length
6.2 Protrusion over terminal :
6.2.1 Chip on electrode pad :


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦0.5mm</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
6.2.2 Non-conductive portion:


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦ L</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.
⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.
6.2.3 Substrate protuberance and internal crack.
<table>
<tr><th>y: width</th><th>x: length</th></tr>
<tr><td>y≦1/3L</td><td>x ≦ a</td></tr>
</table>


</td><td>2.5</td></tr>
</table>

No	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB  X * Y<=2mm^2	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	2.5
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65
		12.12 Visual defect outside of VA is not considered to be rejection.	0.65

12.Material List of Components for RoHs

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

   Exhibit A：The Harmful Material List

Material	Cd	Pb	Hg	Cr6+	PBB	PBDE	DEHP	BBP	DBP	DIBP
Limited Value	**100 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**
Above limited value is set up according to RoHS.										

2.Process for RoHS requirement：**(only for RoHS inspection)**

(1) Use the Sn/Ag/Cu soldering surface；the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp.：

Reflow：250℃,30 seconds Max.；

Connector soldering wave or hand soldering：320℃, 10 seconds max.

(3) Temp. curve of reflow, max. Temp.：235±5℃；

Recommended customer's soldering temp. of connector：280℃, 3 seconds.

13.Recommendable Storage

1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module.

winstar **LCM Sample Estimate Feedback Sheet**

Module Number： _____ **Page: 1**

1、Panel Specification：

1. Panel Type： ☐ Pass ☐ NG ,_____
2. View Direction： ☐ Pass ☐ NG ,_____
3. Numbers of Dots： ☐ Pass ☐ NG ,_____
4. View Area： ☐ Pass ☐ NG ,_____
5. Active Area： ☐ Pass ☐ NG ,_____
6. Operating Temperature： ☐ Pass ☐ NG ,_____
7. Storage Temperature： ☐ Pass ☐ NG ,_____
8. Others：_____

2、Mechanical Specification：

1. PCB Size： ☐ Pass ☐ NG ,_____
2. Frame Size： ☐ Pass ☐ NG ,_____
3. Materal of Frame： ☐ Pass ☐ NG ,_____
4. Connector Position： ☐ Pass ☐ NG ,_____
5. Fix Hole Position： ☐ Pass ☐ NG ,_____
6. Backlight Position： ☐ Pass ☐ NG ,_____
7. Thickness of PCB： ☐ Pass ☐ NG ,_____
8. Height of Frame to PCB： ☐ Pass ☐ NG ,_____
9. Height of Module： ☐ Pass ☐ NG ,_____
10. Others： ☐ Pass ☐ NG ,_____

3、Relative Hole Size：

1. Pitch of Connector： ☐ Pass ☐ NG ,_____
2. Hole size of Connector： ☐ Pass ☐ NG ,_____
3. Mounting Hole size： ☐ Pass ☐ NG ,_____
4. Mounting Hole Type： ☐ Pass ☐ NG ,_____
5. Others： ☐ Pass ☐ NG ,_____

4、Backlight Specification：

1. B/L Type： ☐ Pass ☐ NG ,_____
2. B/L Color： ☐ Pass ☐ NG ,_____
3. B/L Driving Voltage (Reference for LED Type)： ☐ Pass ☐ NG ,_____
4. B/L Driving Current： ☐ Pass ☐ NG ,_____
5. Brightness of B/L： ☐ Pass ☐ NG ,_____
6. B/L Solder Method： ☐ Pass ☐ NG ,_____
7. Others： ☐ Pass ☐ NG ,_____

>> Go to page 2 <<

5、**Electronic Characteristics of Module**：

1.	Input Voltage：	☐ Pass	☐ NG ,_____
2.	Supply Current：	☐ Pass	☐ NG ,_____
3.	Driving Voltage for LCD：	☐ Pass	☐ NG ,_____
4.	Contrast for LCD：	☐ Pass	☐ NG ,_____
5.	B/L Driving Method：	☐ Pass	☐ NG ,_____
6.	Negative Voltage Output：	☐ Pass	☐ NG ,_____
7.	Interface Function：	☐ Pass	☐ NG ,_____
8.	LCD Uniformity：	☐ Pass	☐ NG ,_____
9.	ESD test：	☐ Pass	☐ NG ,_____
10.	Others：	☐ Pass	☐ NG ,_____

6、**Summary**：

Sales signature：_____

Customer Signature：_____ **Date**： / /