

Display Elektronik GmbH

DATA SHEET

TFT MODULE

DEM 800320A3 TMH-PW-N

4,6" TFT

Product Specification

Ver.: 2

08.03.2024

Revision History

Revision	Date	Originator	Detail	Remarks
0	13.12.2023	ZWL	Initial Release	-
1	18.12.2023	LL	Modify Optical Characteristics	P6
2	08.03.2024	LL	Modify Module Parameter Modify AC electrical characteristics Modify Outline Drawing(B)	P4 P11 P24

Table of Contents

No.	Item	Page
1.	General Description	4
2.	Module Parameter	4
3.	Absolute Maximum Ratings	4
4.	DC Characteristics	5
5.	Backlight Characteristic	5
5.1.	Backlight Characteristics.....	5
5.2.	Backlighting circuit	5
6.	Optical Characteristics	6
6.1.	Optical Characteristics	6
6.2.	Definition of Response Time	6
6.3.	Definition of Contrast Ratio	7
6.4.	Definition of Viewing Angles.....	7
6.5.	Definition of Color Appearance	8
6.6.	Definition of Surface Luminance, Uniformity and Transmittance.....	8
7.	Block Diagram and Power Supply	9
8.	Interface Pins Definition	10
9.	AC electrical characteristics.....	11
9.1.	Parallel 24-bit RGB Mode	11
9.2.	Input Clock and Data Timing Diagram	11
9.3.	Power ON/OFF Sequence	12
10.	Quality Assurance	14
10.1.	Purpose	14
10.2.	Standard for Quality Test.....	14
10.3.	Nonconforming Analysis & Disposition	14
10.4.	Agreement Items.....	14
10.5.	Standard of the Product Visual Inspection	15
10.6.	Inspection Specification	15
10.7.	Classification of Defects.....	20
10.8.	Identification/marketing criteria	20
10.9.	Packing.....	20
11.	Reliability Specification	21
12.	Precautions and Warranty	22
12.1.	Safety	22
12.2.	Handling	22
12.3.	Storage.....	22
12.4.	Metal Pin (Apply to Products with Metal Pins)	22
12.5.	Operation	23
12.6.	Static Electricity	23
12.7.	Limited Warranty.....	23
13.	Outline Drawing	24

1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs and a Backlight Unit.

2. Module Parameter

Features	Details	Unit
Display Size (Diagonal)	4.6"	-
LCD type	TN TFT	-
Display Mode	Normally White / Transmissive	-
Resolution	800 x RGB x 320	Pixels
View Direction	12 O'clock	Best Image
Gray Scale Inversion Direction	6 O'clock	-
Module Outline	120.7 (H) x 57.08(V) x 3.10(MAX)(T) (Note1)	mm
Active Area	108 (H) x 43.2(V)	mm
Pixel Size	0.135 (H) x 0.135(V)	mm
Pixel Arrangement	R.G.B Vertical Stripe	-
Driver IC	ST5625CA & ST5091CA	-
Display Colors	16.7 Million	-
Interface	24-Bit RGB Interface	-
With or Without Touch Panel	Without	-
Operating Temperature	-20°C to +70°C	°C
Storage Temperature	-30°C to +80°C	°C
Weight	TBD	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

$V_{SS}=0V$, $T_a=25^{\circ}C$

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	-0.5	5.0	V
Storage Temperature	T _{STG}	-30	+80	°C
Operating Temperature	T _{OP}	-20	+70	°C

Note 1: If T_a below +50°C, the maximal humidity is 90%RH, if T_a over +50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	
Supply Voltage	VDD	2.7	3.3	3.6	V	
Logic Low Input Voltage	V _{IL}	0	-	0.3*VDD	V	
Logic High Input Voltage	V _{IH}	0.7*VDD	-	VDD	V	
Logic Low Output Voltage	V _{OL}	-	-	GND+0.4	V	
Logic High Output Voltage	V _{OH}	VDD-0.4	-	-	V	
Current Consumption All Black	Logic Analog	I _{CC+} I _{IN}	-	TBD	-	mA

5. Backlight Characteristic

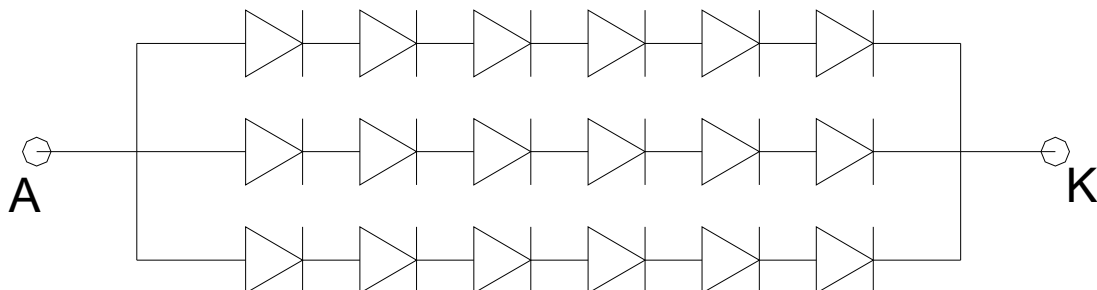
5.1. Backlight Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V _F	Ta=25°C, I _F =20mA/LED	16.8	19.2	20.4	V
Forward Current	I _F	Ta=25°C, V _F =3.2V	-	60	-	mA
Power Dissipation	P _D		-	1152	-	mW
LED Lifetime (+25 °C)	-	-	20000	30000	-	Hr
Uniformity	Avg		75	80	-	%
Drive Method	Constant Current					
LED Configuration	18 White LEDs (6 LEDs in one string and 3 groups in parallel)					

Note: LED Lifetime defined as follows: The final brightness is at 70% of original brightness.

The environmental conducted under ambient air flow, at Ta=+25°C±2 °C,
60%RH±5%, I_F=20mA.

5.2. Backlighting Circuit



6. Optical Characteristics

6.1. Optical Characteristics

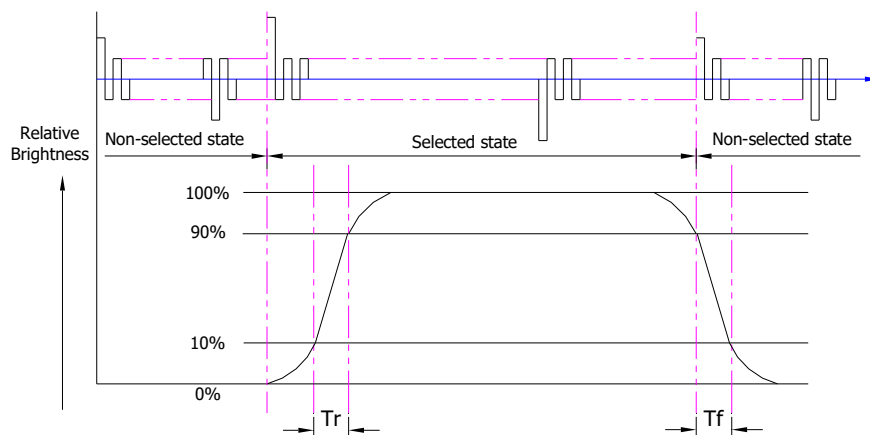
Ta=25°C,

V_{DD} =3.3V

	Item	Symbol	Condition	Specification			Unit	
				Min.	Typ.	Max.		
Backlight On (Transmissive Mode)	Luminance on TFT(I _f =20mA/LED)	Lv	Normally viewing angle θ _X = φ _Y =0°	416	520	-	cd/m ²	
	Contrast Ratio(See 7.3)	CR		300	500	-		
	Response Time (See 7.2)	TR+TF		-	20	-	ms	
	Chromaticity Transmissive (See 7.5)	Red	X _R	Center CR≥10	-	TBD	-	Deg.
			Y _R		-	TBD	-	
		Green	X _G		-	TBD	-	
			Y _G		-	TBD	-	
		Blue	X _B		-	TBD	-	
			Y _B		-	TBD	-	
	White	X _W	-	TBD	-			
Y _W		-	TBD	-				
Viewing Angle (See 6.4)	Horizontal	θ _{X+}	Center CR≥10	60	65	-	Deg.	
		θ _{X-}		60	65	-		
	Vertical	φ _{Y+}		45	50	-		
		φ _{Y-}		55	60	-		
NTSC Ratio(Gamut)				-	50.4	-	%	

6.2. Definition of Response Time

6.2.1. Normally Black Type (Negative)

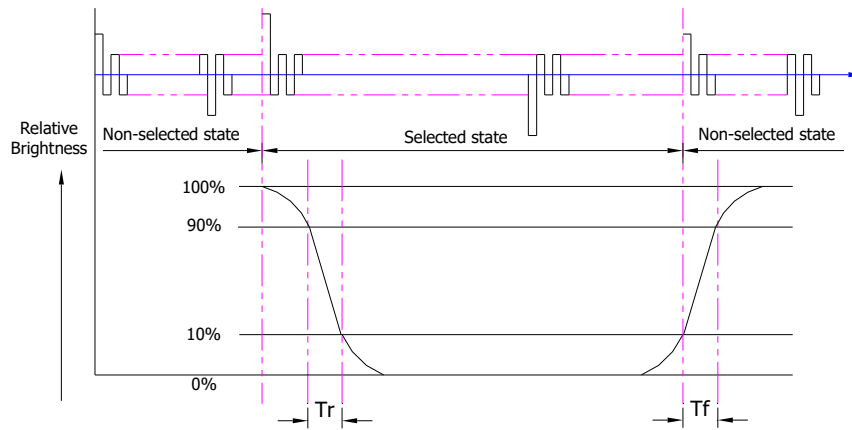


Tr is the time it takes to change form non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%

Note: Measuring machine: LCD-5100

6.2.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

6.3. Definition of Contrast Ratio

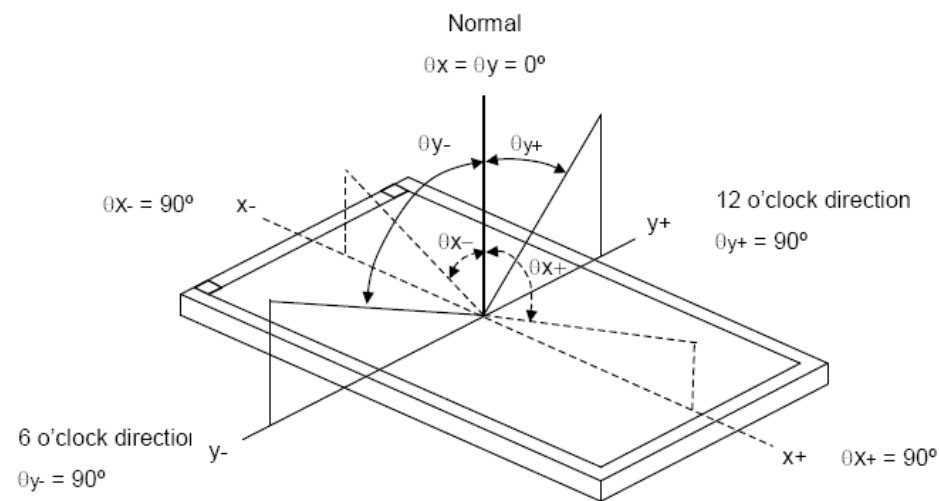
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test Pattern	A: All Pixels white
	B: All Pixel black
Contrast Setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.4. Definition of Viewing Angles



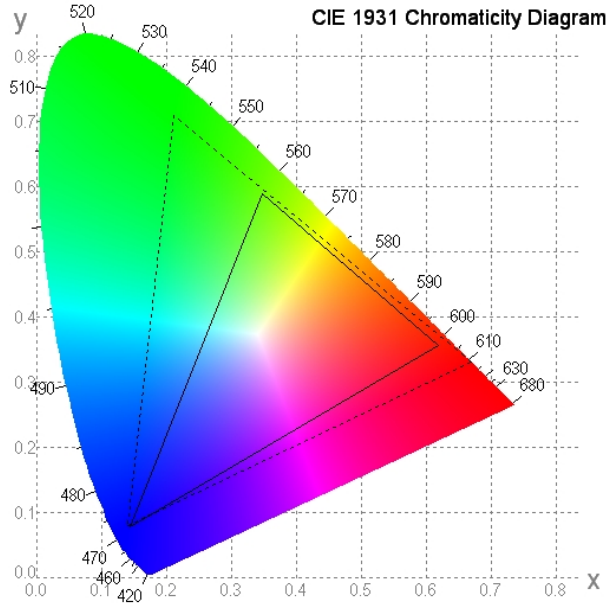
Measuring machine: LCD-5100 or EQUI

6.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)

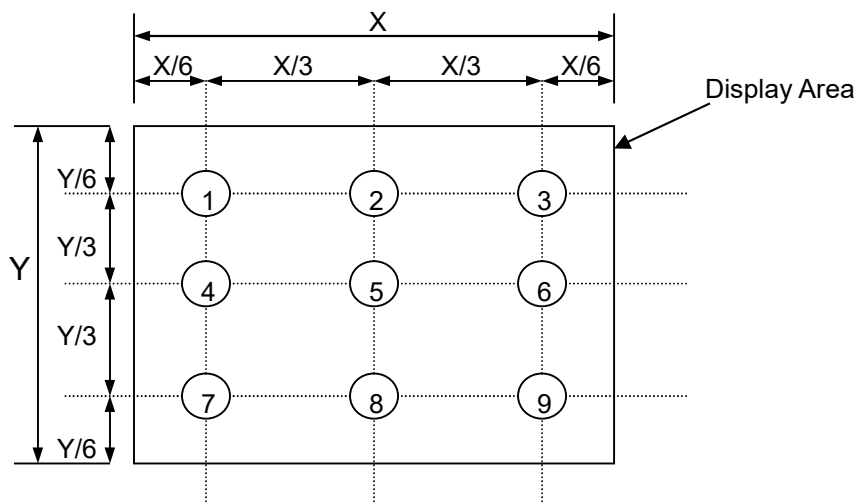


6.6. Definition of Surface Luminance, Uniformity and Transmittance

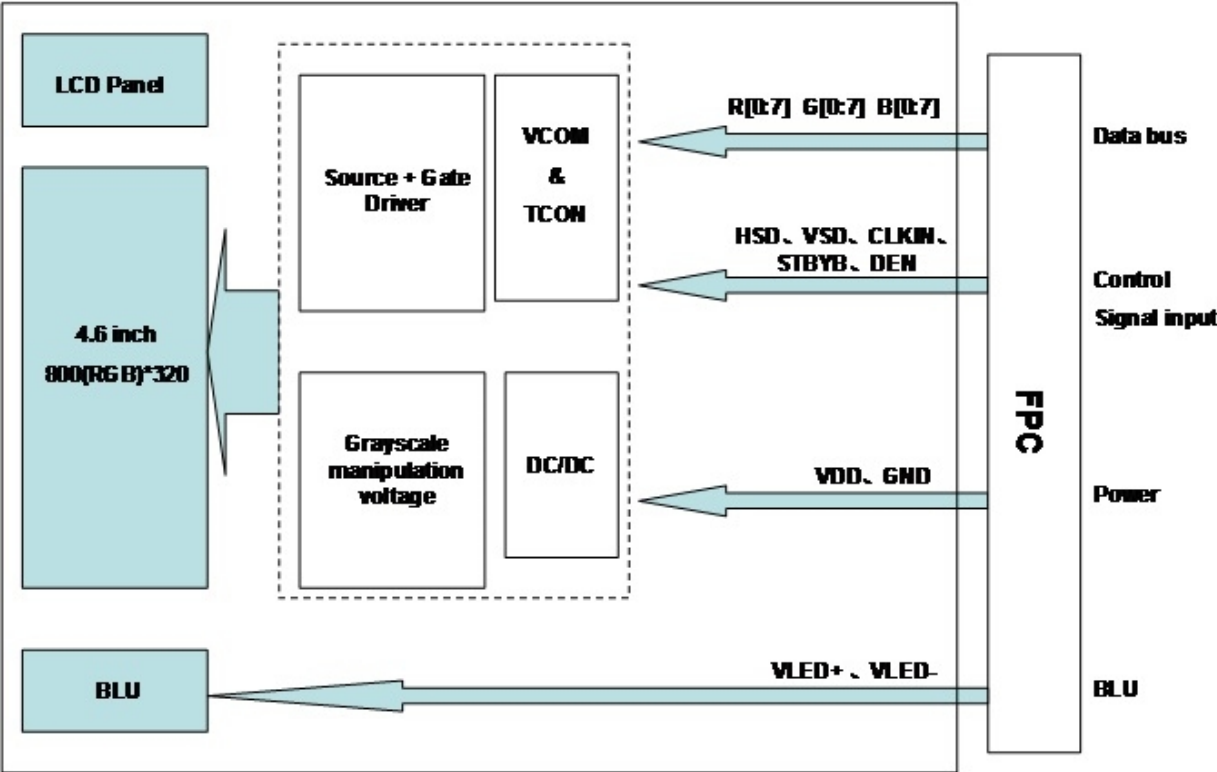
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

- 6.6.1. Surface Luminance: $L_v = \text{average} (L_{P1}:L_{P9})$
- 6.6.2. Uniformity = $\text{Minimal} (L_{P1}:L_{P9}) / \text{Maximal} (L_{P1}:L_{P9}) * 100\%$
- 6.6.3. Transmittance = $L_v \text{ on LCD} / L_v \text{ on Backlight} * 100\%$

Note: Measuring machine: BM-7



7. Block Diagram and Power Supply



8. Interface Pins Definition

No.	Symbol	Function	Remark
1	VLED-	LED Input Terminal(Cathode)	
2	VLED+	LED Input Terminal(Anode)	
3	GND	Power voltage	
4	VDD	Power voltage	
5	R0	Red data(LSB)	
6	R1	Red data	
7	R2	Red data	
8	R3	Red data	
9	R4	Red data	
10	R5	Red data	
11	R6	Red data	
12	R7	Red data(MSB)	
13	G0	Green data(LSB)	
14	G1	Green data	
15	G2	Green data	
16	G3	Green data	
17	G4	Green data	
18	G5	Green data	
19	G6	Green data	
20	G7	Green data(MSB)	
21	B0	Blue data(LSB)	
22	B1	Blue data	
23	B2	Blue data	
24	B3	Blue data	
25	B4	Blue data	
26	B5	Blue data	
27	B6	Blue data	
28	B7	Blue data(MSB)	
29	GND	Ground	
30	CLKIN	Clock for input data. Data latched at rising edge of this signal	
31	STBYB	Display on/off	
32	HSD	Horizontal sync signal	
33	VSD	Vertical sync signal	
34	DEN	Data enable. (In SYNC mode this pin should not be used.)	
35	NC	No connection	
36	GND	Ground	
37	NC(XR)	No connection	
38	NC(YD)	No connection	
39	NC(XL)	No connection	
40	NC(YU)	No connection	

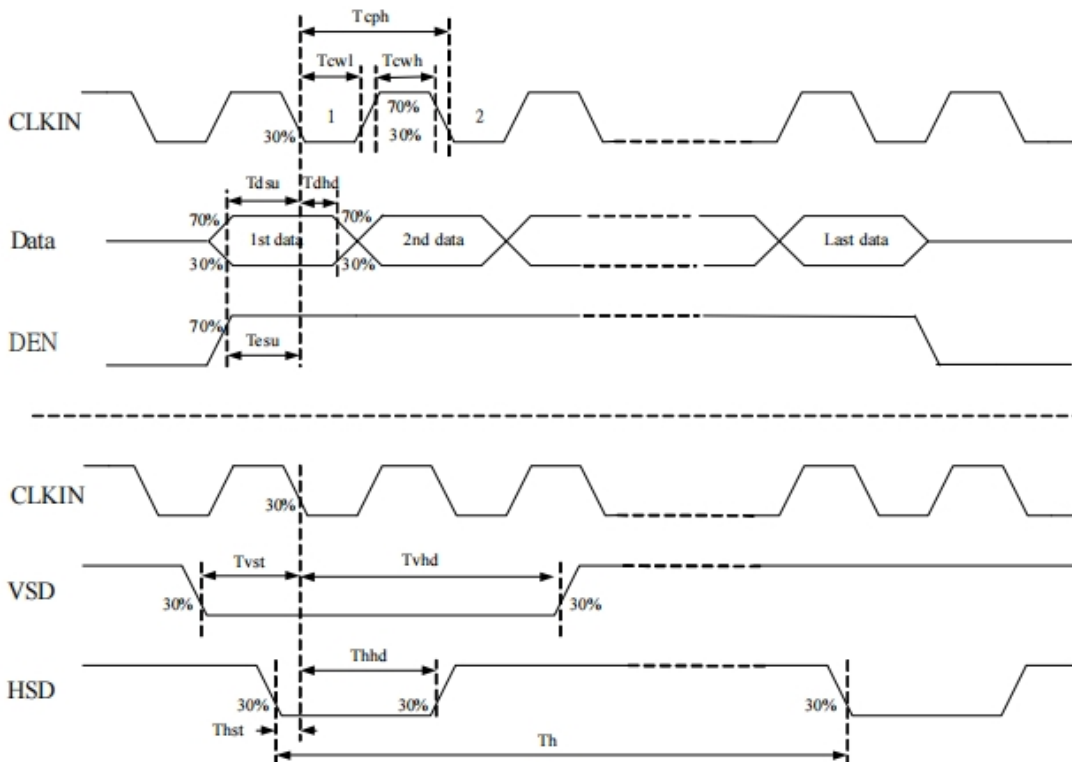
9. AC electrical characteristics

Parameter	Symbol	Min	Typ.	Max.	Unit	Conditions
VDD Power on Slew Rate	T_{POR}	-	-	20	ms	From 0V to 90% VDD
RSTB pulse width	T_{RST}	50	-	-	us	Clkin=50MHz
CLKIN cycle time	T_{cph}	20	-	-	ns	
CLKIN pulse duty	T_{cwh}	40	50	60	%	
VSD setup time	T_{vst}	8	-	-	ns	
VSD hold time	T_{vhd}	8	-	-	ns	
HSD setup time	T_{hst}	8	-	-	ns	
HSD hold time	T_{hhd}	8	-	-	ns	
Data setup time	T_{dsu}	8	-	-	ns	D[7:0], D1[7:0], D2[7:0] to clkin
Date hold time	T_{dhd}	8	-	-	ns	D[7:0], D1[7:0], D2[7:0] to clkin
DE setup time	T_{esu}	8	-	-	ns	
DE hold time	T_{ehd}	8	-	-	ns	
Output stable time	T_{sst}	-	-	6	us	10% to 90% target voltage. CL=120pF, R=10Kohm

9.1. Parallel 24-bit RGB Mode

Parameter	Symbol	Min	Typ.	Max.	Unit	Conditions
CLKIN Frequency	F_{clk}	-	40	50	MHz	VDD=2.7V~3.6V
CLKIN Cycle Time	T_{clk}	20	25	-	ns	
CLKIN Pulse Duty	T_{cwh}	40	50	60	%	Tclk
Time from HSD to Source Output	T_{hso}	-	64	-	CLKIN	
Time from HSD to LD	T_{hld}	-	64	-	CLKIN	
Time from HSD to STV	T_{hstv}	-	2	-	CLKIN	
Time from HSD to CKV	T_{hckv}	-	20	-	CLKIN	
Time from HSD to OEV	T_{hoev}	-	4	-	CLKIN	
LD pulse width	T_{wld}	-	10	-	CLKIN	
CKV pulse width	T_{wckv}	-	66	-	CLKIN	
OEV pulse width	T_{woev}	-	74	-	CLKIN	

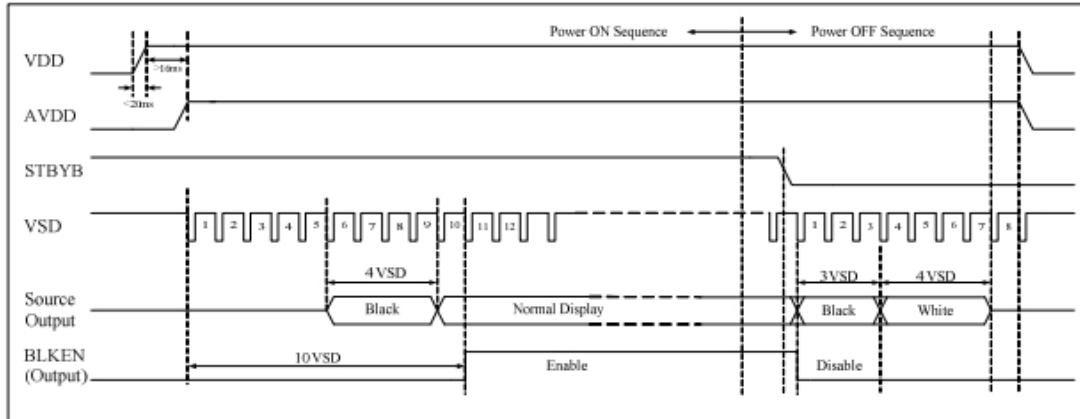
9.2. Input Clock and Data Timing Diagram



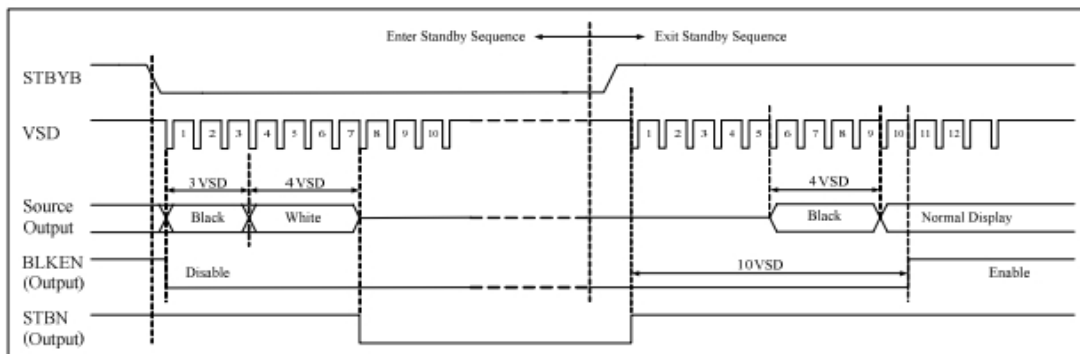
9.3. Power ON/OFF Sequence

In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

Power-On/Off Timing Sequence

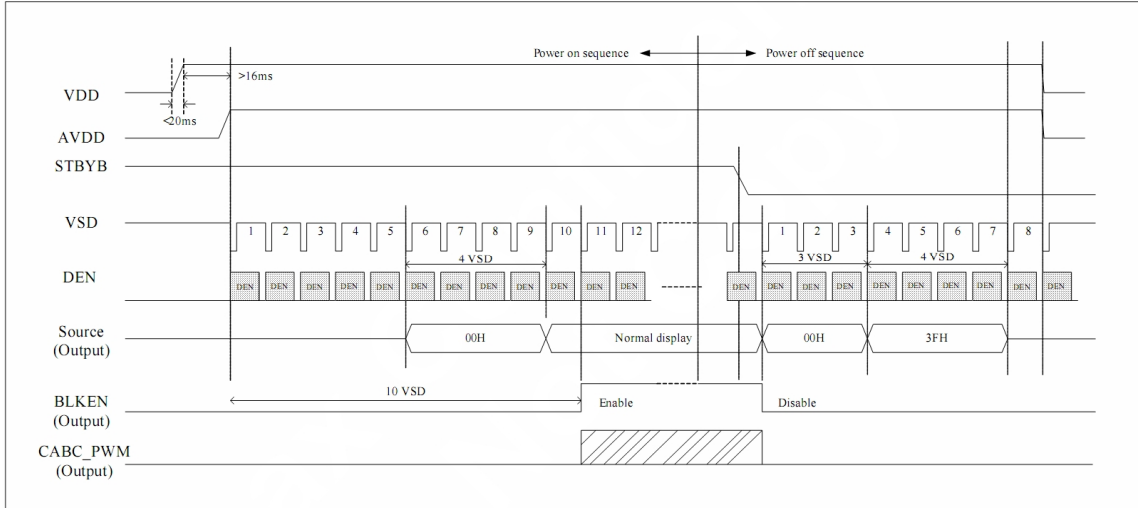


Standby Mode Sequence

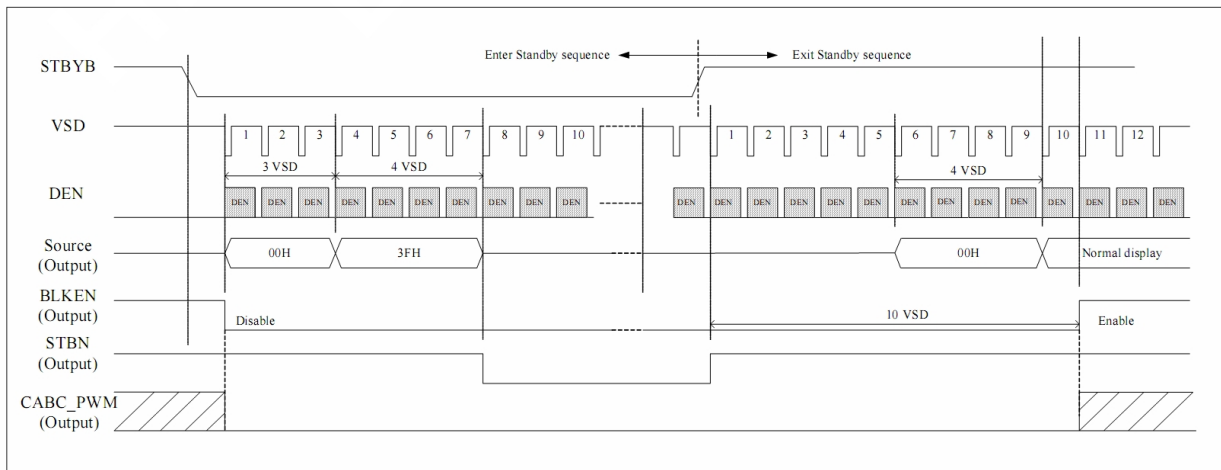


Power ON/OFF Control

The IC has a power on/off sequence control function. In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Please refer to "AC Characteristics" for more detail on timing.



Enter and exit standby mode sequence



10. Quality Assurance

10.1 Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

10.2 Standard for Quality Test

10.2.1 Sampling Plan:

GB2828.1-2012

Single sampling, normal inspection

10.2.2 Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65

10.2.3 Reliability Test:

Detailed requirement refer to Reliability Test Specification.

10.3 Nonconforming Analysis & Disposition

10.3.1 Nonconforming analysis:

10.3.1.1 Customer should provide overall information of non-conforming sample for their complaints.

10.3.1.2 After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

10.3.1.3 If cannot finish the analysis on time, customer will be notified with the progress status.

10.3.2 Disposition of nonconforming:

10.3.2.1 Non-conforming product over PPM level will be replaced.

10.3.2.2 The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

10.4 Agreement Items

Shall negotiate with customer if the following situation occurs:

10.4.1 There is any discrepancy in standard of quality assurance.

10.4.2 Additional requirement to be added in product specification.

10.4.3 Any other special problem.

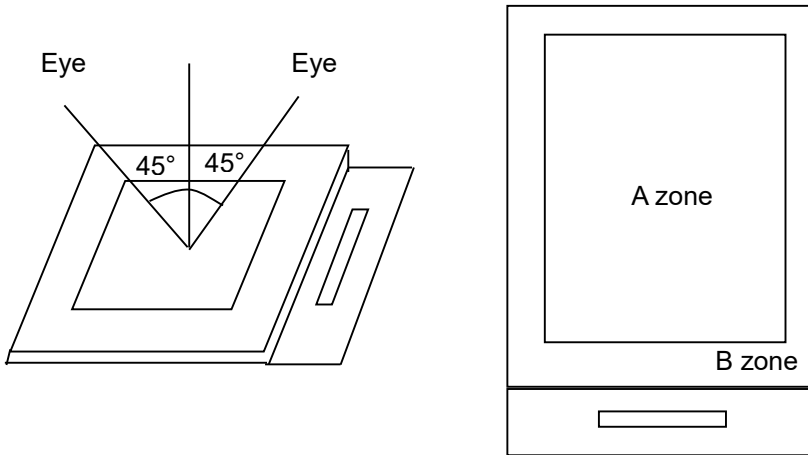
10.5 Standard of the Product Visual Inspection

10.5.1 Appearance inspection:

10.5.1.1 The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

10.5.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

10.5.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area,



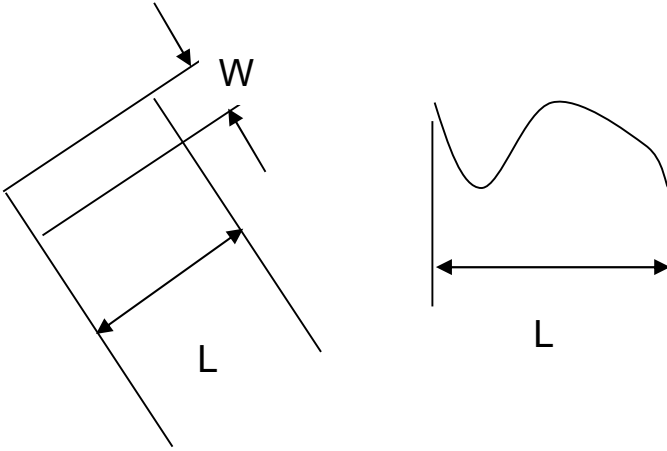
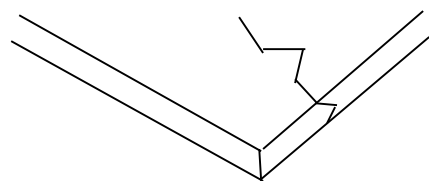
10.5.2 Basic principle:

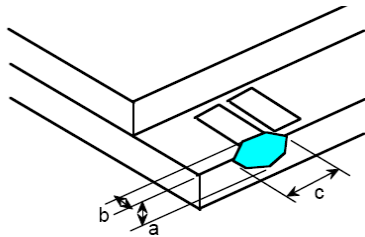
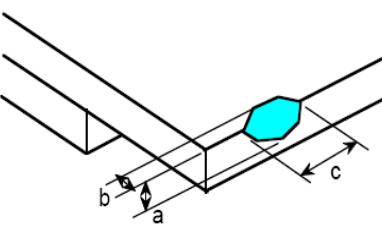
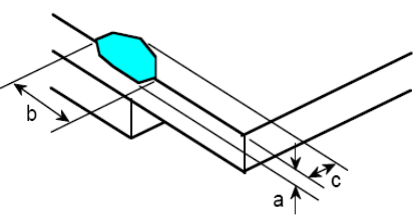
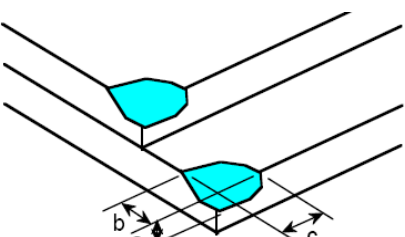
10.5.2.1 A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

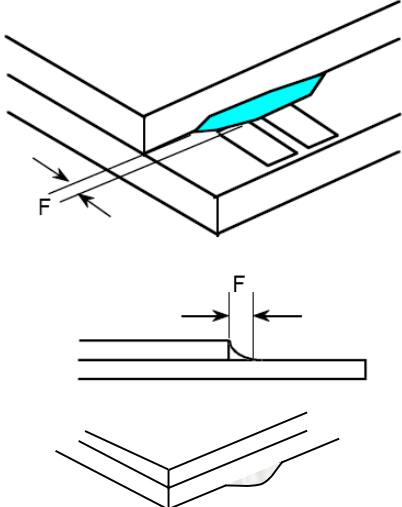
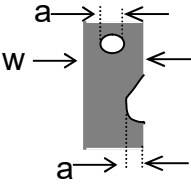
10.5.2.2 New item must be added on time when it is necessary.

10.6 Inspection Specification

No.	Item	Criteria (Unit: mm)																			
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 $\phi = (a + b) / 2$ Distance between 2 defects should more than 5mm apart.	<table border="1"> <thead> <tr> <th>Size</th> <th>Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.10$</td> <td></td> <td>Ignore</td> </tr> <tr> <td>$0.10 < \phi \leq 0.15$</td> <td></td> <td>2</td> </tr> <tr> <td>$0.15 < \phi \leq 0.25$</td> <td></td> <td>1</td> </tr> <tr> <td>$0.25 < \phi$</td> <td></td> <td>0</td> </tr> <tr> <td>Total</td> <td></td> <td>2 no include $\phi \leq 0.10$</td> </tr> </tbody> </table>	Size	Area	Acc. Qty	$\phi \leq 0.10$		Ignore	$0.10 < \phi \leq 0.15$		2	$0.15 < \phi \leq 0.25$		1	$0.25 < \phi$		0	Total		2 no include $\phi \leq 0.10$
			Size	Area	Acc. Qty																
$\phi \leq 0.10$		Ignore																			
$0.10 < \phi \leq 0.15$		2																			
$0.15 < \phi \leq 0.25$		1																			
$0.25 < \phi$		0																			
Total		2 no include $\phi \leq 0.10$																			

<p>02</p>	<p>Electrical Defect (Minor defect)</p>	<table border="1"> <thead> <tr> <th></th> <th>Display Area</th> <th>Total</th> <th></th> </tr> </thead> <tbody> <tr> <td>Bright dot</td> <td>0</td> <td>0</td> <td rowspan="3">Note 1</td> </tr> <tr> <td>Dark dot</td> <td>$N \leq 2$</td> <td>$N \leq 2$</td> </tr> <tr> <td>Total dot</td> <td>$N \leq 2$</td> <td>$N \leq 2$</td> </tr> <tr> <td>Mura</td> <td colspan="2">Not visible through 5% ND filters.</td> <td>Note 2</td> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>		Display Area	Total		Bright dot	0	0	Note 1	Dark dot	$N \leq 2$	$N \leq 2$	Total dot	$N \leq 2$	$N \leq 2$	Mura	Not visible through 5% ND filters.		Note 2
	Display Area	Total																		
Bright dot	0	0	Note 1																	
Dark dot	$N \leq 2$	$N \leq 2$																		
Total dot	$N \leq 2$	$N \leq 2$																		
Mura	Not visible through 5% ND filters.		Note 2																	
<p>03</p>	<p>Black and White line Scratch Foreign material (Line type) (Minor defect)</p>	 <table border="1"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td>$W \leq 0.1$</td> <td>Ignore</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.1 < W \leq 0.2$</td> <td>3</td> </tr> <tr> <td>$L > 2.5$</td> <td>$0.2 < W$</td> <td>0</td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3			
Length	Width	Acc. Qty																		
/	$W \leq 0.1$	Ignore																		
$L \leq 2.5$	$0.1 < W \leq 0.2$	3																		
$L > 2.5$	$0.2 < W$	0																		
Total		3																		
<p>04</p>	<p>Glass Crack (Minor defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>																		

<p>05</p>	<p>Glass Chipping Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>3</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$			
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	3											
$a < \text{Glass Thickness}$												
<p>06</p>	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>07</p>	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
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<p>08</p>	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c < 3.0, b < 3.0$</td> <td>Ignore</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												

<p>9</p>	<p>Glass Burr: (Minor defect)</p> 	<table border="1" data-bbox="858 264 1332 353"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$F < 1.0$</td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore				
Length	Acc. Qty									
$F < 1.0$	Ignore									
<p>10</p>	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.</p>								
<p>11</p>	<p>Bubble on Polarizer (Minor defect)</p>	<table border="1" data-bbox="737 1214 1204 1388"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.30$</td> <td>Ignore</td> </tr> <tr> <td>$0.30 < \varphi \leq 0.50$</td> <td>$N \leq 2$</td> </tr> <tr> <td>$0.50 < \varphi$</td> <td>$N = 0$</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.30$	Ignore	$0.30 < \varphi \leq 0.50$	$N \leq 2$	$0.50 < \varphi$	$N = 0$
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$\varphi \leq 0.30$	Ignore									
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$0.50 < \varphi$	$N = 0$									
<p>12</p>	<p>Dent on Polarizer (Minor defect)</p>	<table border="1" data-bbox="737 1456 1204 1630"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.25$</td> <td>Ignore</td> </tr> <tr> <td>$0.25 < \varphi \leq 0.50$</td> <td>$N \leq 4$</td> </tr> <tr> <td>$0.50 < \varphi$</td> <td>None</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.25$	Ignore	$0.25 < \varphi \leq 0.50$	$N \leq 4$	$0.50 < \varphi$	None
Diameter	Acc. Qty									
$\varphi \leq 0.25$	Ignore									
$0.25 < \varphi \leq 0.50$	$N \leq 4$									
$0.50 < \varphi$	None									
<p>13</p>	<p>Bezel</p>	<p>13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.</p>								

14	Touch Panel	<p>D: Diameter W: width L: length</p> <p>14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$</p> <p>2dots are acceptable and the distance between defects should more than 10 mm.</p> <p>$D > 0.4$ is unacceptable</p> <p>14.2 Dent: $D > 0.40$ is unacceptable</p> <p>14.3 Scratch: $W \leq 0.03$, $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$, $L \leq 10$ is acceptable</p> <p>Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.</p>
15	PCB	<p>15.1 No distortion or contamination on PCB terminals.</p> <p>15.2 All components on PCB must same as documented on the BOM/component layout.</p> <p>15.3 Follow IPC-A-600F.</p>
16	Soldering	Follow IPC-A-610C standard
17	Electrical Defect (Major defect)	<p>The below defects must be rejected.</p> <p>17.1 Missing vertical / horizontal segment,</p> <p>17.2 Abnormal Display.</p> <p>17.3 No function or no display.</p> <p>17.4 Current exceeds product specifications.</p> <p>17.5 LCD viewing angle defect.</p> <p>17.6 No Backlight.</p> <p>17.7 Dark Backlight.</p> <p>17.8 Touch Panel no function.</p>

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

10.7 Classification of Defects

10.7.1 Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

10.7.2 Two minor defects are equal to one major in lot sampling inspection.

10.8 Identification/marketing criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

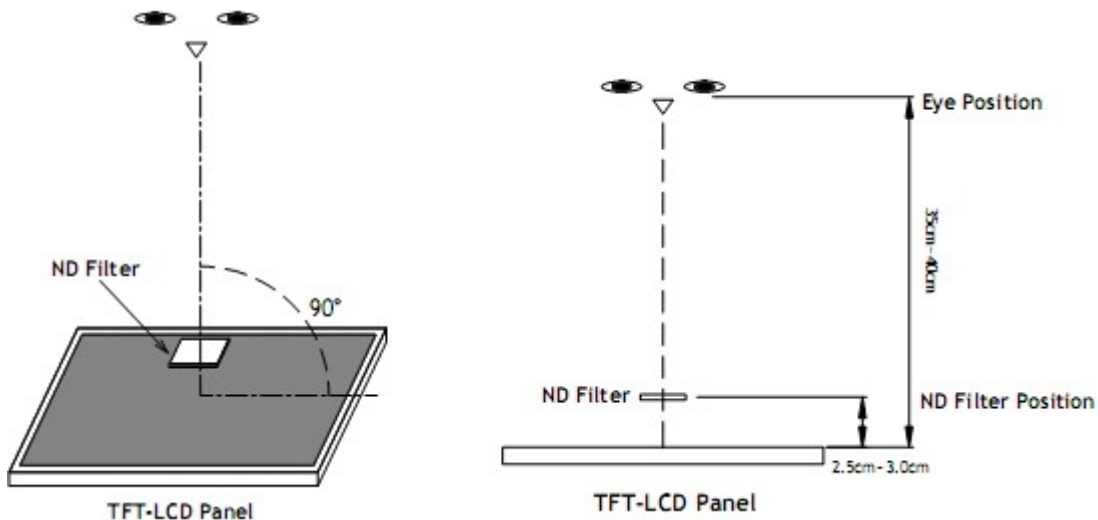
10.9 Packing

10.9.1 There should be no damage of the outside carton box, each packaging box should have one identical label.

10.9.2 Modules inside package box should have compliant mark.

10.9.3 All direct package materials shall offer ESD protection

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

11. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	+70°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity Operating	+50°C, 90%RH, 96Hrs	2	GB/T2423.3-2016
4	High Temperature Storage	+80°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min~+70°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	-	GB/T5170.14-2009
8	Electrical Static Discharge	Air: ±8kV 150pF/330Ω 5 times Contact: ±4kV 150pF/330Ω 5 times	2	GB/T17626.2-2018
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	-	GB/T2423.7-2018

Note1. No defection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

12. Precautions and Warranty

12.1.Safety

12.1.1 The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

12.1.2 Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

12.2.Handling

12.2.1 Reverse and use within ratings in order to keep performance and prevent damage.

12.2.2 Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

12.3.Storage

12.3.1. Do not store the LCD module beyond the specified temperature ranges.

12.3.2. Strong light exposure causes degradation of polarizer and color filter

12.4.Metal Pin (Apply to Products with Metal Pins)

12.4.1 Pins of LCD and Backlight

12.4.1.1 Solder tip can touch and press on the tip of Pin LEAD during the soldering

12.4.1.2 Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

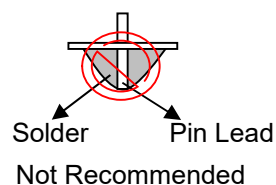
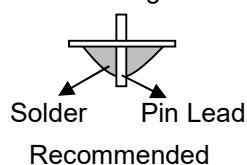
Maximum Solder Temperature: 370°C

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20°C

Typical Soldering Time: ≤3s

12.4.1.3 Solder Wetting



12.4.2Pins of EL

12.4.2.1 Solder tip can touch and press on the tip of EL leads during soldering.

12.4.2.2 No Solder Paste on the soldering pad on the motherboard is recommended.

12.4.2.3 Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290°C

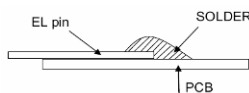
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

12.4.2.4 No horizontal press on the EL leads during soldering.

12.4.2.5 180° bend EL leads three times is not allowed.

12.4.2.6 Solder Wetting

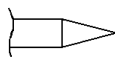


Recommended

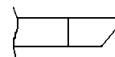


Not Recommended

12.4.2.7 The type of the solder iron:

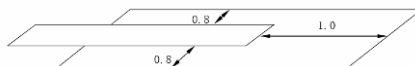


Recommended



Not Recommended

12.4.2.8 Solder Pad



12.5 Operation

- 12.5.1 Do not drive LCD with DC voltage
- 12.5.2 Response time will increase below lower temperature
- 12.5.3 Display may change color with different temperature
- 12.5.4 Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.
- 12.5.5 Do not connect or disconnect the LCM to or from the system when power is on.
- 12.5.6 Never use the LCM under abnormal condition of high temperature and high humidity.
- 12.5.7 Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 12.5.8 Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

12.6 Static Electricity

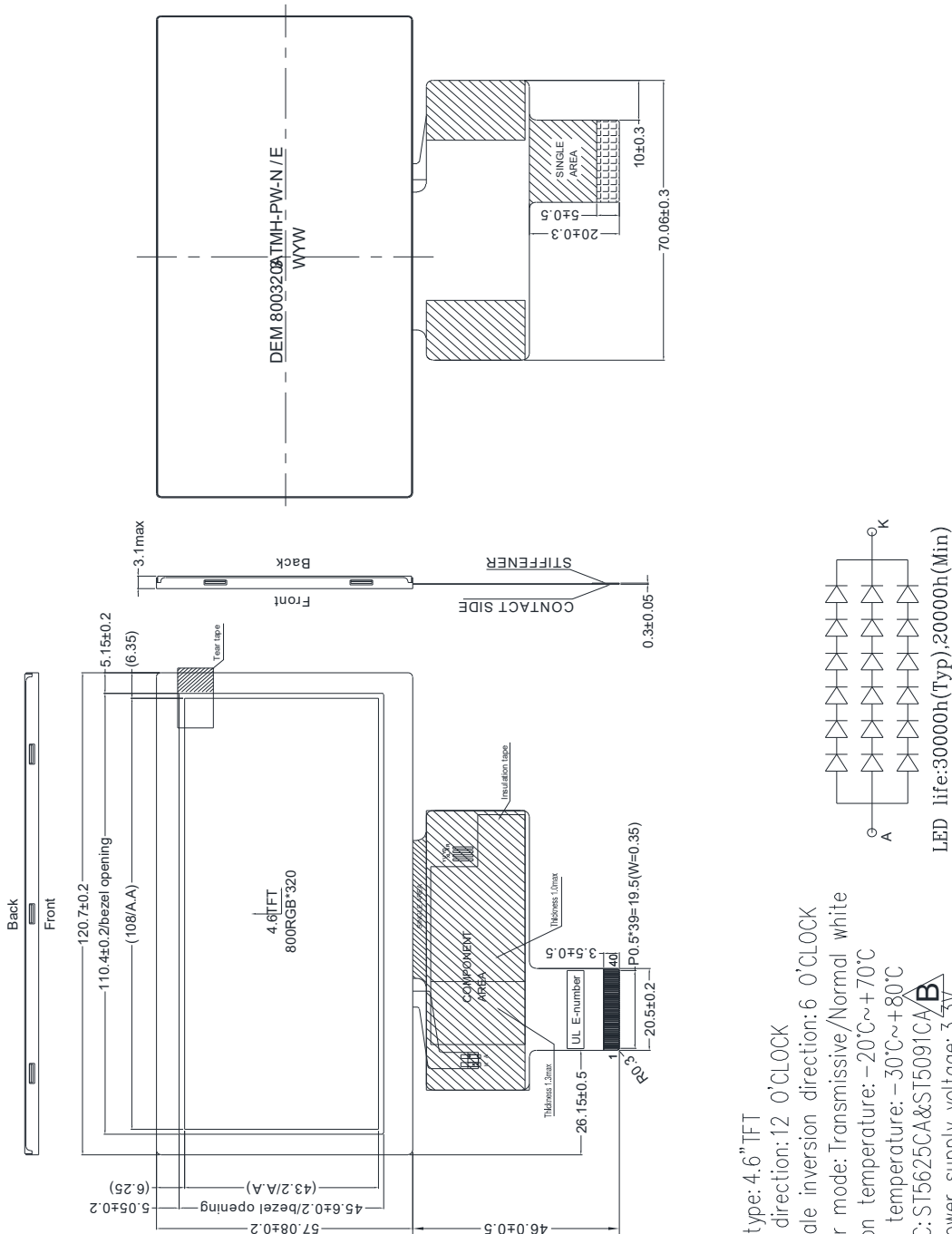
- 12.6.1 CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 12.6.2 The normal static prevention measures should be observed for work clothes and benches.
- 12.6.3 The module should be kept into anti-static bags or other containers resistant to static for storage.

12.7 Limited Warranty

- 12.7.1 Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 12.7.2 If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 12.7.3 After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

13. Outline Drawing

NO.	PIN NAME
1	VLED-
2	VLED+
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	CLKIN
31	STBYB
32	HSD
33	VSD
34	DEN
35	NC
36	GND
37	NC(XR)
38	NC(YD)
39	NC(XL)
40	NC(YU)



NOTES:

1. Display type: 4.6" TFT
2. Viewing direction: 12 O'CLOCK
3. Gary scale inversion direction: 6 O'CLOCK
4. Polarizer mode: Transmissive/Normal white
5. Operation temperature: -20°C~+70°C
6. Storage temperature: -30°C~+80°C
7. Driver IC: ST5625CA&ST5091CA/B
8. Logic power supply voltage: 3.3V
9. Backlight: White/18 LEDs/19.2V(typ)/60mA
Brightness: 410(min)/520(TYP)cd/m²
10. ROHS must be complied
11. Unspecification tolerance are ±0.2